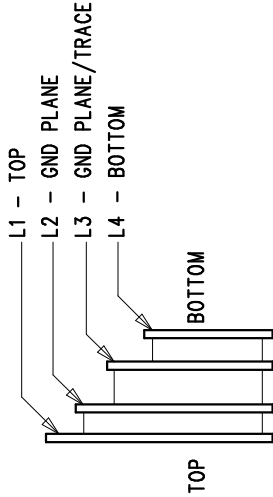
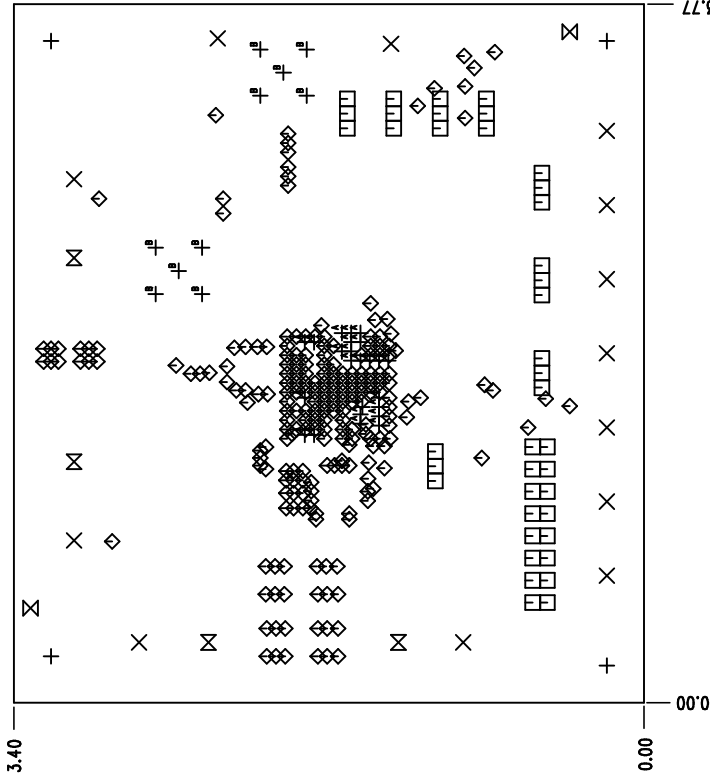


REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG.
-	2	2nd PROTOTYPE	YAN L.
			5-18-11

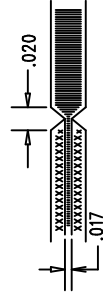
SHOWN FROM TOP SIDE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
5. FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):

SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	YES	+/-0.003
0.095	13	X	YES	+/-0.003
0.035	40	□	YES	+/-0.003
0.01	223	◇	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.205	4	⊗	YES	+/-0.003
0.008	24	† ^A	YES	+/-0.003
0.055	10	† ^B	YES	+/-0.003



APPROVALS		UNLESS OTHERWISE SPECIFIED	
		DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	
PCB DES.	HZ		
APP ENG.	YAN L.		
TITLE: FABRICATION DRAWING 10A STEP-DOWN μMODULE® REGULATOR WITH INPUT AND OUTPUT OVP		1530 MCCARTHY BLVD MILPITAS, CA 95035 TEL: (408) 432-9000 FAX: (408) 432-9001 WWW.LINEARTECH.COM LET CUSTOMER USE ONLY	
SCALE = NONE		FILENAME: DC1543A-2.PCB	
SIZE		IC NO.	REV
N/A		LT4641EV	2
DEMO CIRCUIT		1543A	
SHEET 1 OF 1			