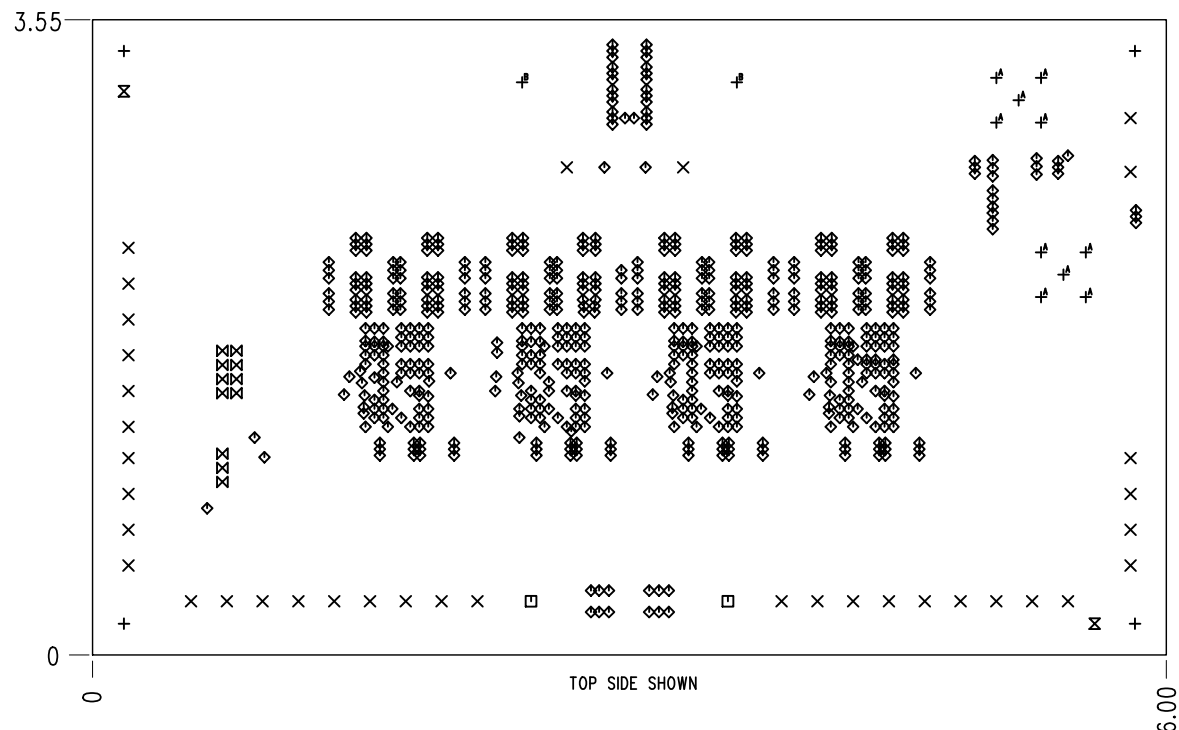
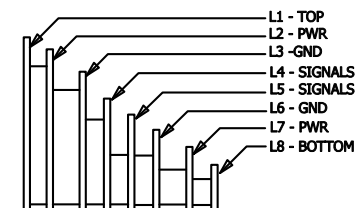


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	JL	06-09-17



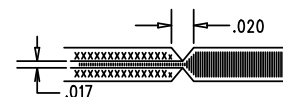
SIZE	QTY	SYM	PLATED	TOL
0.01	633	◇	YES	+/- .003
0.035	11	⊗	YES	+/- .003
0.055	10	+ ^A	YES	+/- .003
0.065	36	⊗	YES	+/- .003
0.07	2	⊗	NO	+/- .003
0.187	4	+ ^B	NO	+/- .005
0.189	2	+ ^B	YES	+/- .005
0.205	2	□	YES	+/- .005

LAYER STRUCTURE




NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 8 LAYERS WITH 2 OZ. CU ON ALL LAYERS
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
O/O ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- DESIGN HAS SOLDER MASK DEFINED PADS ON U1-U4 WITH 0.029 PAD, 0.025 SOLDER MASK, AND 0.024 SOLDER PASTE. DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED		APPROVALS		 <div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	LT		
		APP ENG.	JL	TITLE: FABRICATION DRAWING HIGH EFFICIENCY, POLY-PHASE STEP-DOWN μMODULE REGULATOR	
				SIZE N/A	IC NO. LTM4645EY/LTM4647EY DEMO CIRCUIT 2616A
					REV 2
		SCALE = NONE		FILENAME: DC2616A-2.PCB	
				SHT 1 OF 1	