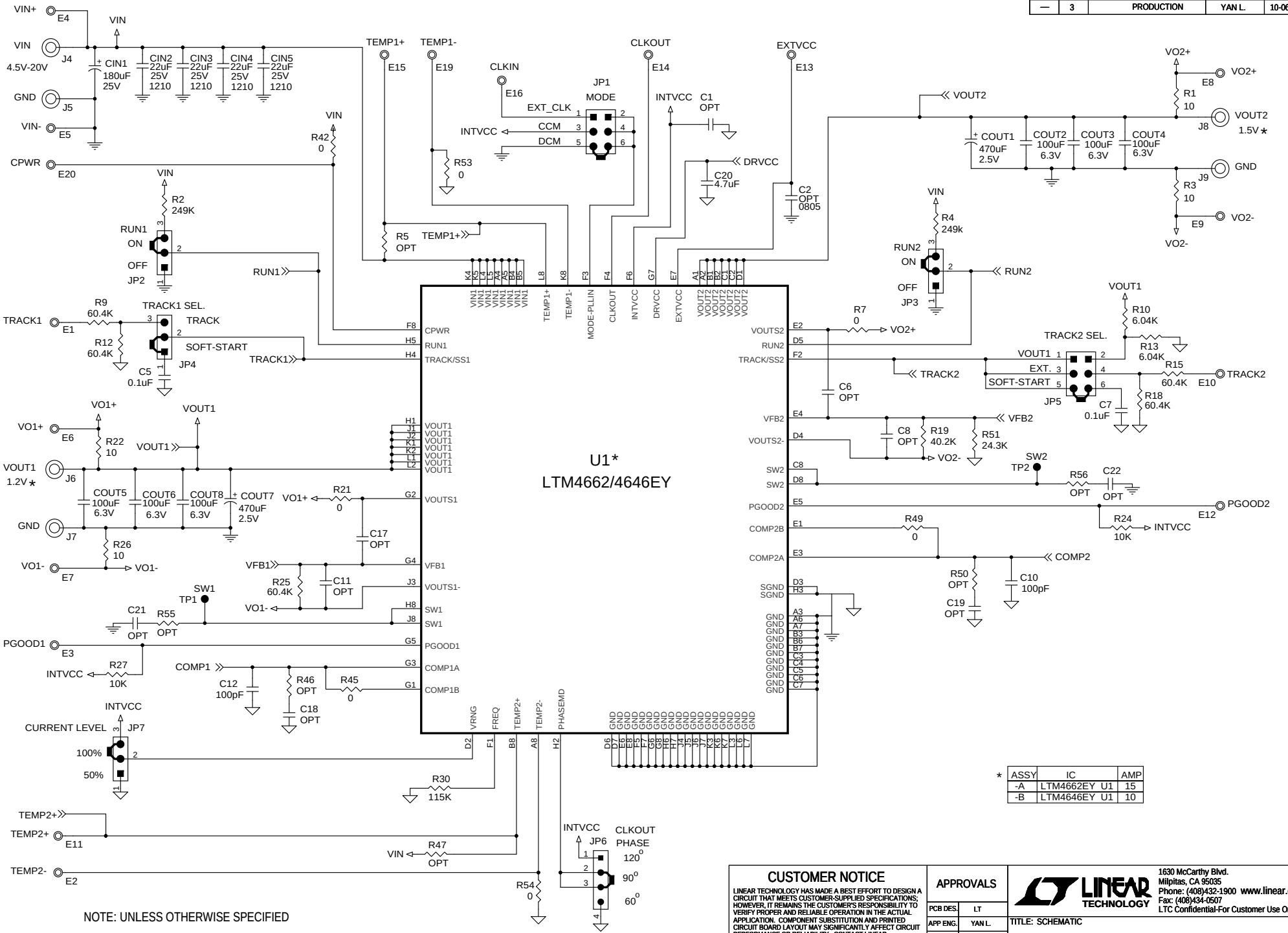
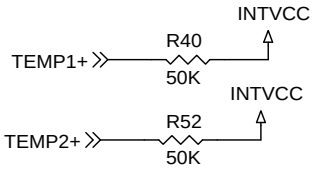
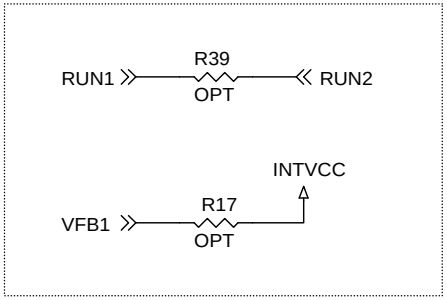
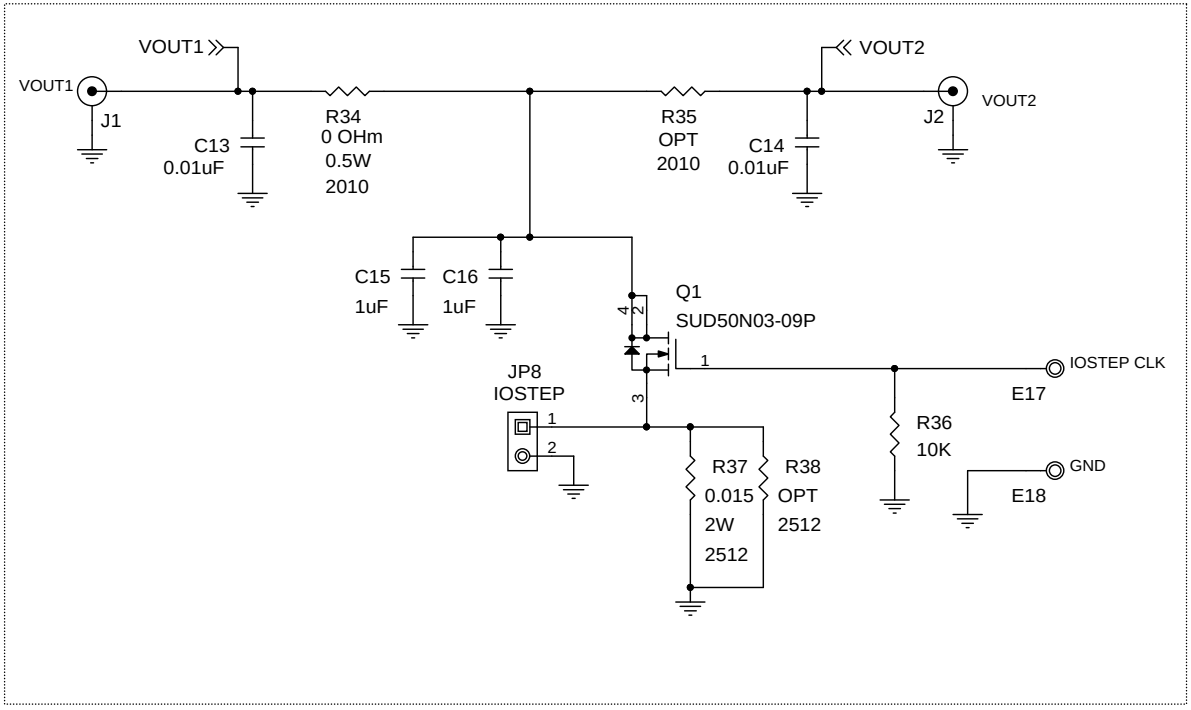

REVISION HISTORY
ECO REV DESCRIPTION APPROVED DATE
— 3 PRODUCTION YAN L. 10-06-17
U1*
LTM4662/4646EY
VIN+ E4
VIN J4 4.5V-20V
GND J5
VIN- E5
CPWR E20
CIN1 180uF 25V
CIN2 22uF 25V 1210
CIN3 22uF 25V 1210
CIN4 22uF 25V 1210
CIN5 22uF 25V 1210
R42 0
TEMP1+ E15
TEMP1- E19
R53 0
R5 OPT
CLKIN E16
CLKOUT E14
EXTVCC E13
JP1 MODE
EXT_CLK
CCM
DCM
INTVCC
C1 OPT
DRVCC
C20 4.7uF
C2 OPT 0805
R2 249K
RUN1
ON
OFF
JP2
TRACK1
E1
R9 60.4K
R12 60.4K
TRACK1 SEL.
TRACK
SOFT-START
JP4
C5 0.1uF
VO1+ E6
VO1- E7
VOUT1 1.2V*
J6
J7
R22 10
R26 10
COUT5 100uF 6.3V
COUT6 100uF 6.3V
COUT8 100uF 6.3V
COUT7 470uF 2.5V
R21 0
C17 OPT
R25 60.4K
C11 OPT
SW1
TP1
C21 OPT
R55 OPT
PGOOD1 E3
R27 10K
INTVCC
COMP1
C12 100pF
R46 OPT
R45 0
C18 OPT
CURRENT LEVEL
JP7
100%
50%
R30 115K
TEMP2+ E11
TEMP2- E2
R47 OPT
R54 0
JP6
PHASE
120°
90°
60°
CPWR
RUN1
TRACK/SS1
VOUTS1
VFB1
VOUTS1-
SW1
PGOOD1
COMP1A
COMP1B
VRNG
FREQ
TEMP2+
TEMP2-
PHASEMD
VOUTS2
RUN2
TRACK/SS2
VFB2
VOUTS2-
SW2
PGOOD2
COMP2B
COMP2A
SGND
GND
R7 0
VO2+
C6 OPT
C8 OPT
R19 40.2K
R51 24.3K
VO2-
TP2
R56 OPT
C22 OPT
R49 0
R24 10K
INTVCC
R50 OPT
C19 OPT
C10 100pF
COMP2
R4 249k
RUN2
JP3
VOUT1
R10 6.04K
R13 6.04K
TRACK2 SEL.
EXT.
SOFT-START
JP5
R15 60.4K
R18 60.4K
C7 0.1uF
TRACK2
VO2+ E8
VO2- E9
VOUT2 1.5V*
J8
J9
R1 10
R3 10
COUT1 470uF 2.5V
COUT2 100uF 6.3V
COUT3 100uF 6.3V
COUT4 100uF 6.3V
* ASSY IC AMP
-A LTM4662EY U1 15
-B LTM4646EY U1 10
NOTE: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS AND CAPACITORS ARE 0603.
CUSTOMER NOTICE
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.
APPROVALS
PCB DES. LT
APP ENG. YAN L.
SCALE = NONE
LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507
LTC Confidential-For Customer Use Only
TITLE: SCHEMATIC
HIGH EFFICIENCY, DUAL PHASE STEP-DOWN uMODULE REGULATOR
SIZE N/A
IC NO. LTM4662/4646EY
DEMO CIRCUIT 2527A
REV. 3
DATE: Friday, October 06, 2017
SHEET 1 OF 2



OPTIONAL JUMPER FOR 1 OUTPUT CONFIGURATION



Load Transient Circuit



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APPROVALS	
PCB DES.	LT
APP ENG.	YAN L.

SCALE = NONE

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

HIGH EFFICIENCY, DUAL PHASE STEP-DOWN uMODULE REGULATOR

SIZE	IC NO.	REV.
N/A	LTM4662/4646EY DEMO CIRCUIT 2527A	3

DATE: Friday, October 06, 2017