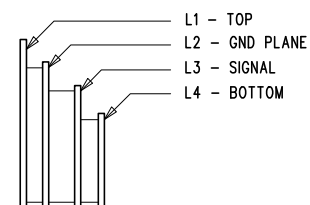
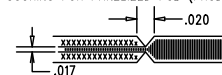


3.90


LAYER STRUCTURE



1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
O/O ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. to ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



9. DESIGN HAS SOLDER MASK DEFINED PADS ON U1 WITH 0.029 PAD, 0.025 SOLDER MASK, AND 0.024 SOLDER PASTE.
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED		APPROVALS				1630 MCCARTHY BLVD MILPITAS, CA 95035 FAX (408) 432-1900 WWW.LINEAR.COM LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY		
DIMENSIONS ARE IN INCHES		PCB DES.	LT	APP ENG.	JL	TITLE: FABRICATION DRAWING		
TOLERANCES:						HIGH EFFICIENCY, DUAL 25A, SINGLE 50A, STEP-DOWN μ MODULE REGULATOR		
0.XXX = ± 0.01						SIZE	IC NO. LTM4650EY	REV 1
0.XXX = ± 0.005						N/A	DEMO CIRCUIT 2378A/B	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994						SCALE = NONE		
THIRD ANGLE PROJECTION						FILENAME: DC2378A-1PCB		SHT 1 OF 1
