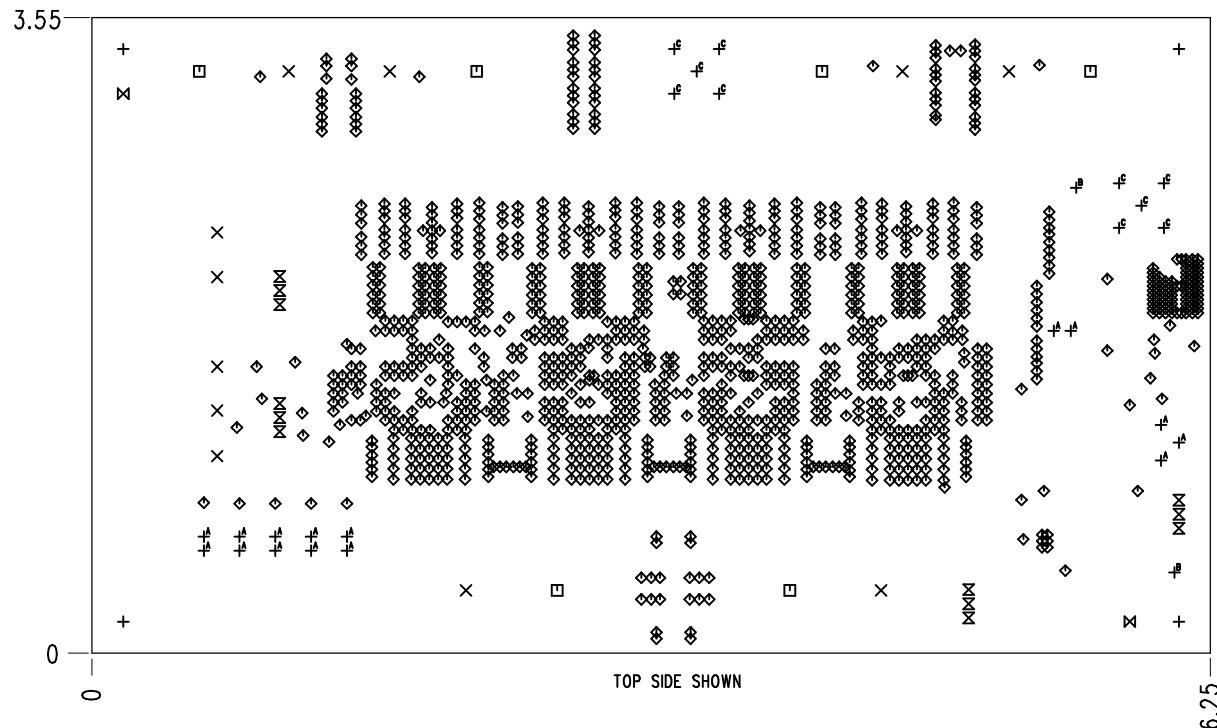
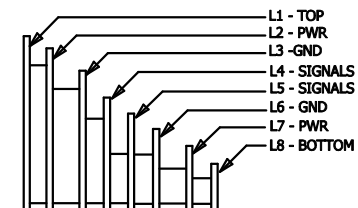


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	LW	06-15-21



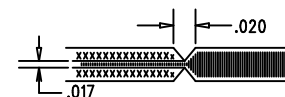
SIZE	QTY	SYM	PLATED	TOL
0.01	1313	◇	YES	+/- .003
0.035	15	+ <sup>A</sup>	YES	+/- .003
0.039	12	⊗	YES	+/- .003
0.055	10	+ <sup>C</sup>	YES	+/- .003
0.067	11	×	YES	+/- .003
0.07	2	⊗	NO	+/- .003
0.095	2	+ <sup>B</sup>	YES	+/- .003
0.187	4	+	NO	+/- .005
0.205	6	□	YES	+/- .003

#### LAYER STRUCTURE



#### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 8 LAYERS WITH 2 OZ. CU ON ALL LAYERS  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- DESIGN HAS SOLDER MASK DEFINED PADS ON U1-U4 WITH 0.029 PAD, 0.025 SOLDER MASK, AND 0.024 SOLDER PASTE. DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		APPROVALS		ANALOG DEVICES POWER BY LINEAR™ www.analog.com	
		PCB DES.	LT	TITLE: FABRICATION DRAWING HIGH EFFICIENCY, POLYPHASE 200A DC/DC STEP-DOWN μMODULE REGULATOR WITH WIDE OUTPUT VOLTAGE RANGE	
		APP ENG.	LIYAO W.		
				SIZE	IC NO. LTM4650A-1
				N/A	DEMO CIRCUIT 3064A
		SCALE = NONE		FILENAME: DC3064A-2.PCB	REV 2
				SHT 1 OF 1	