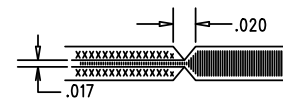


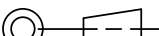


0.062"
4 LAYERS

0.0028"(MIN.) LAYER 1
0.0035" PREPREG
0.0014"(MIN.) LAYER 2
0.0014"(MIN.) CORE MATERIAL
0.0014"(MIN.) LAYER 3
0.0035" PREPREG
0.0028"(MIN.) LAYER 4

1. FAB PER IPC-A-600.
2. MATERIAL: —LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
 - FINISHED THICKNESS TO BE 0.062 +/- .005
 - TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
O/O ARE PRIMARY DATUMS.
4. DRILLING: —DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
5. FINISH: —SMOBC USING LPI BOTH SIDES, COLOR GREEN.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.008	116	⊕ ^A	YES	+/-0.003
0.01	176	⊗	YES	+/-0.003
0.035	11	⊕ ^B	YES	+/-0.003
0.065	10	⊗	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.095	14	◇	YES	+/-0.003
0.187	4	⊕	YES	+/-0.005
0.215	12	□	YES	+/-0.005

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION 	APPROVALS		 ANALOG DEVICES		 POWER BY LINEAR™	
	PCB DES.	LT	FOR ADI CUSTOMER USE ONLY			
	APP ENG.	DB	TITLE: FABRICATION DRAWING			
			LOW EMI POSITIVE AND NEGATIVE OUTPUT μ MODULE REGULATOR			
			SIZE N/A	IC NO. LTM4655EY DEMO CIRCUIT 2594A	REV 3	
			FILENAME: DC2594A-3.PCB		SHT 1 OF 1	
SCALE = NONE						