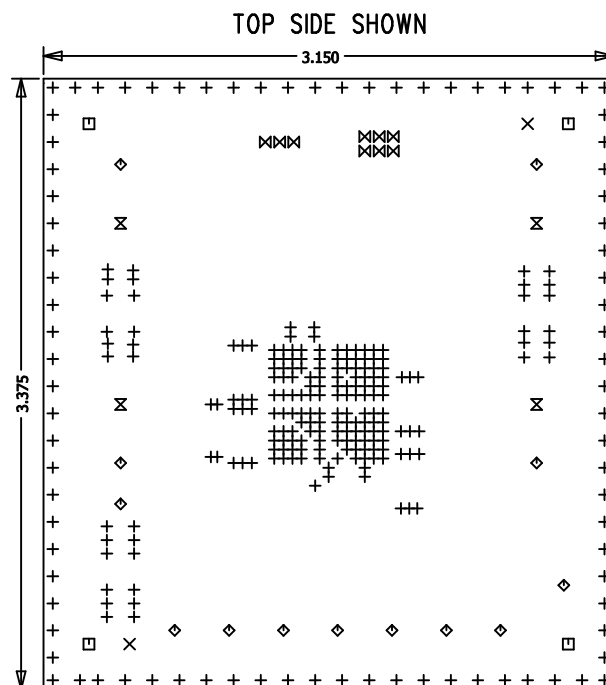
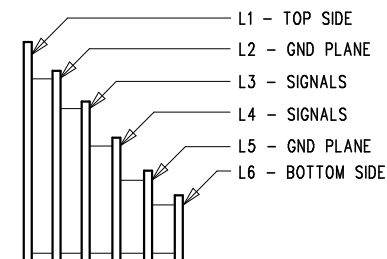


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	GP	02-18-20



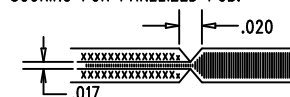
SIZE	QTY	SYM	PLATED	TOL
0.01	284	+	YES	+/- .003
0.035	9	⊗	YES	+/- .003
0.07	2	⊗	NO	+/- .003
0.095	13	◇	YES	+/- .003
0.187	4	□	YES	+/- .003
0.205	4	⊗	YES	+/- .003

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON ALL LAYERS
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- DESIGN HAS SOLDER MASK DEFINED PADS ON U1.
SOLDER MASK IS .025 AND PAD SIZE IS .029 .
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED		APPROVALS		ANALOG DEVICES		POWER BY LINEAR	
DIMENSIONS ARE IN INCHES		PCB DES.	LT			www.analog.com	
TOLERANCES:		APP ENG.	GP			FOR ADI CUSTOMER USE ONLY	
0.XX = ± 0.01						TITLE: FABRICATION DRAWING	
0.XXX = ± 0.005						SHORT CIRCUIT PROTECTED SYNCHRONOUS	
INTERPRET DIM AND TOL						BOOST μMODULE REGULATOR	
PER ASME Y14.5M-1994				SIZE	IC NO.	LTM4656EY	REV
THIRD ANGLE PROJECTION				N/A		DC2634A	3
		SCALE = NONE		FILENAME: DC2634A-3.PCB		SHT 1 OF 1	