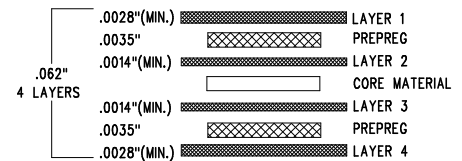


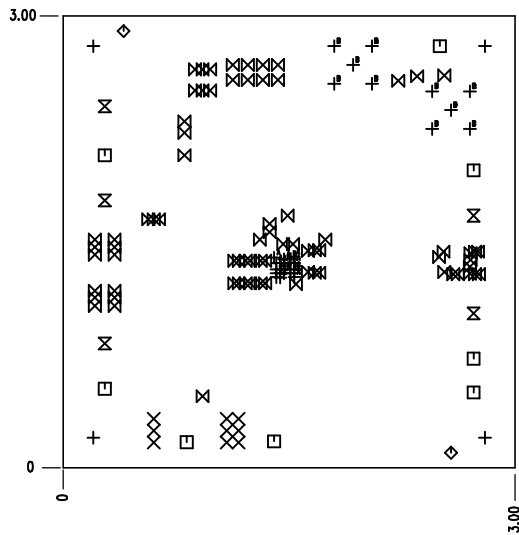
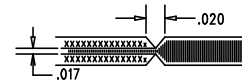
| REVISION HISTORY | | | | |
|------------------|-----|-------------|-----------|----------|
| ECO | REV | DESCRIPTION | APP. ENG. | DATE |
| - | 3 | PROTOTYPE | YL | 01-17-20 |

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1.
U1 SOLDER MASK SIZE IS 13 MIL AND PAD SIZE IS 14 MIL.
DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



| SIZE | QTY | SYM | PLATED | TOL |
|-------|-----|----------------|--------|----------|
| 0.006 | 22 | ⊕ ^A | YES | +/-0.003 |
| 0.01 | 75 | ⊗ | YES | +/-0.003 |
| 0.035 | 9 | ⊗ | YES | +/-0.003 |
| 0.056 | 10 | ⊕ ^B | YES | +/-0.003 |
| 0.07 | 2 | ◇ | NO | +/-0.003 |
| 0.095 | 8 | □ | YES | +/-0.003 |
| 0.187 | 4 | ⊕ | YES | +/-0.003 |
| 0.215 | 5 | ⊗ | YES | +/-0.003 |

| | | | | |
|--|-----------|----------|---|------------|
| UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION | APPROVALS | | FOR ARI CUSTOMER USE ONLY | |
| | ENG. | LT | | |
| | APP. ENG. | YAN L. | TITLE: FABRICATION DRAWING | |
| | | | LOW VIN, HIGH EFFICIENCY 10A STEP-DOWN DC/DC μMODULE REGULATOR | |
| SCALE = NONE | | SIZE N/A | IC NO. LTM4658EY DEMO CIRCUIT 2861A | REV 3 |
| FILENAME: DC2861A-3.PCB | | | | SHT 1 OF 1 |