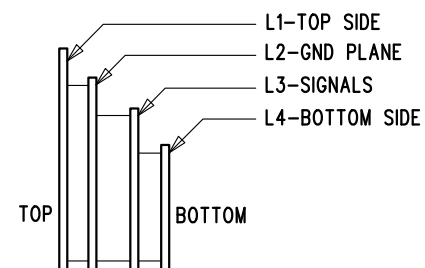
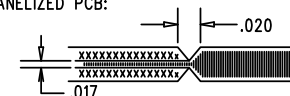


LAYER STRUCTURE

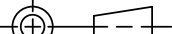




NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062+/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
O/O ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



9. DESIGN HAS SOLDER MASK DEFINED PADS ON U1
WITH 0.018 PAD, 0.014 SOLDER MASK, AND 0.013 SOLDER PASTE.
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.
10. PLUG ALL VIA'S WITH NON-CONDUCTIVE EPOXY
AND PLATE OVER. AND PLANARIZE FINAL SURFACE.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION 	APPROVALS		 ANALOG DEVICES		 POWER BY LINEAR™		www.analog.com FOR ADI CUSTOMER USE ONLY
	PCB DES.	LT	TITLE: FABRICATION DRAWING ULTRA TINY 1.5A TEC μ MODULE REGULATOR				
	APP ENG.	CZ					
	SCALE = NONE		SIZE	IC NO.	LTM4663EV DC2902A	REV	2
			N/A				
			FILENAME: DC2902A-2.PCB				SHT 1 OF 2