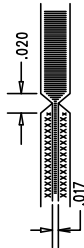




NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 8 LAYERS WITH 2 OZ. CU ON INNER AND OUTER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN LOCATION TO CENTER
5. FINISH: -SWOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ALDER MASK e.g. TO ADD LOGO OR DATE CODE.
7. DESIGN HAS SOLDER MASK DEFINED PADS ON U1-U4.
U1-U4 SOLDER MASK SIZE IS .025 INCH AND PAD SIZE IS 0.029 INCH
DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
8. PGBS ARE TO BE RoHS COMPLIANT.
9. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: $0.0XX = \pm 0.01$ $0.0XX = \pm 0.005$ INTERPRET DIM AND TOL PER ASME Y14.5M-2004 THIRD ANGLE PROJECTION			SCALE = NONE	
	APPROVALS			
PCB DES.	HZ			
APP ENG.	JIAN L.			
		TITLE: FABRICATION DRAWING HIGH EFFICIENCY, POLY-PHASE, DC/DC STEP-DOWN μ MODULE REGULATOR WITH POWER SYSTEM MANAGEMENT		
		SIZE	1C DNO LTM4675EY / LTM4650EY	REV
		N/A	DEMO CIRCUIT 2481A-A/B	1
		FILENAME: DC2481A-1PCB		SHT 1 OF 1

SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003
0.012	537	×	YES	+/-0.003
0.07	2	□	NO	+/-0.003
0.067	24	◇	YES	+/-0.003
0.189	6	⊗	YES	+/-0.003
0.035	25	⊗	YES	+/-0.003
0.008	110	⊥ ^A	YES	+/-0.003
0.01	393	⊥ ^B	YES	+/-0.003
0.03543	12	⊥ ^C	YES	+/-0.003
0.055	10	⊥ ^D	YES	+/-0.003
0.0315	2	⊥ ^F	YES	+/-0.003
0.037	26	⊥ ^G	YES	+/-0.003