

REVISION HISTORY			
ECO#	REV	DESCRIPTION	APP. ENG. DATE
-	2	PRODUCTION	H.ZHOU 2-21-18

LAYER STRUCTURE

L1 - TOP SIDE
 L2 - INNER LAYER 2
 L3 - INNER LAYER 3
 L4 - INNER LAYER 4
 L5 - INNER LAYER 5
 L6 - INNER LAYER 6
 L7 - INNER LAYER 7
 L8 - BOTTOM SIDE

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR
 -FINISHED THICKNESS TO BE 0.02 ± 0.005
 -VIA & PADS WITH 2.0X CU FOR ALL THE LAYERS.
 -FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES TO BE 0.005 ± 0.0025
 -HOLE LOCATIONS ARE SPECIFIED AFTER PLATING.
 -HOLE LOCATION TOLERANCES ARE ±0.003 IN RELATION TO CENTER
- FINISH: -SMBG USING UPI BOTH SIDES, COLOR GREEN.
 -FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
 -DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1-U5.
 U1-U5 SOLDER MASK SIZE IS 0.025 AND PAD SIZE IS 0.029
 DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
- PADS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (4 BBS/PANEL)
- FILL ALL VIAS WITH NON CONDUCTIVE MATERIALS.

SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003
0.067	27	X	YES	+/-0.003
0.189	8	□	YES	+/-0.003
0.012	659	◇	YES	+/-0.003
0.008	622	⊗	YES	+/-0.003
0.035	31	⊠	YES	+/-0.003
0.055	10	⊞	YES	+/-0.003
0.0315	2	⊟	YES	+/-0.003
0.037	26	⊠	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
 DIMENSIONS ARE IN INCHES
 DECIMALS = ± 0.01
 FRACTIONS = ± 0.005
 INTERPRET DIM AND TOL
 AS YOUNG HUNTER
 THIRD ANGLE PROJECTION

APPROVALS	
PCB DES:	HZ
APP ENG:	H.ZHOU
DATE:	
SCALE:	NONE

ANALOG DEVICES **Linear** FOR AN CUSTOMER USE ONLY
 130 MCCARTHY BLVD
 WILMINGTON, MA 01897
 TEL: (508) 321-2000
 WWW.LINEAR.COM

TITLE: FABRICATION DRAWING
 HIGH EFFICIENCY, POLY-PHASE, DC/DC STEP-DOWN
 MODULE REGULATOR WITH DIGITAL POWER MANAGEMENT

SIZE: IC NO. L1M4678EY
 N/A DEMO CIRCUIT 2638A-A//B/C/D
 FILENAME: DC2638A.PCB

SHT. 1 OF 1