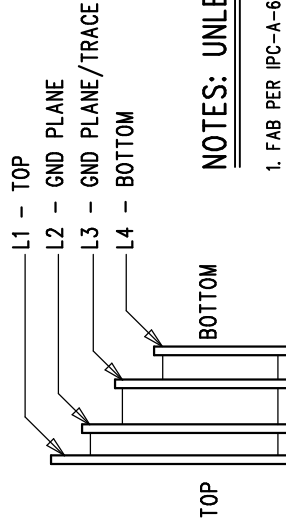
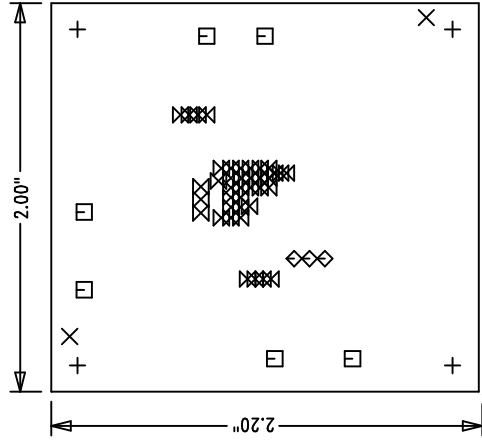


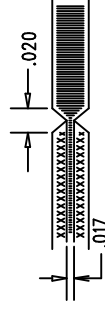
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	ANDY R.	10-19-12

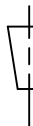


NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
 - FINISHED THICKNESS TO BE 0.062 +/--.005
 - TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
5. FINISH: -SWOBC USING LPI BOTH SIDES, COLOR GREEN.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. DESIGN HAS SOLDER MASK DEFINED PADS ON U1.
 - U1 SOLDER MASK SIZE IS 25 MIL AND PAD SIZE IS 29 MIL.
 - DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
9. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):

SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	YES	+/-0.003
0.07	2	X	NO	+/-0.003
0.095	6	□	YES	+/-0.003
0.035	3	◇	YES	+/-0.003
0.01	39	⊗	YES	+/-0.003



<div>UNLESS OTHERWISE SPECIFIED</div> <div>DIMENSIONS ARE IN INCHES</div> <div>TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005</div> <div>INTERPRET DIM AND TOL PER ASME Y14.5M-1994</div> <div>THIRD ANGLE PROJECTION</div> <div></div>		APPROVALS		<div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1600 WWW.LINEAR.COM LINEAR TECHNOLOGY FOR CUSTOMER USE ONLY</div>	
		PCB DES.	HZ		
		APP ENG.	ANDY R.		
TITLE: FABRICATION DRAWING INVERTING/SEPIC μ MODULE [®] CONVERTER WITH UP TO 700mA OUTPUT				SIZE IC NO. LTM8045EY REV N/A DEMO CIRCUIT 1533A 2	
SCALE = NONE				FILENAME: DC1533A-2.PCB SHT 1 OF 1	