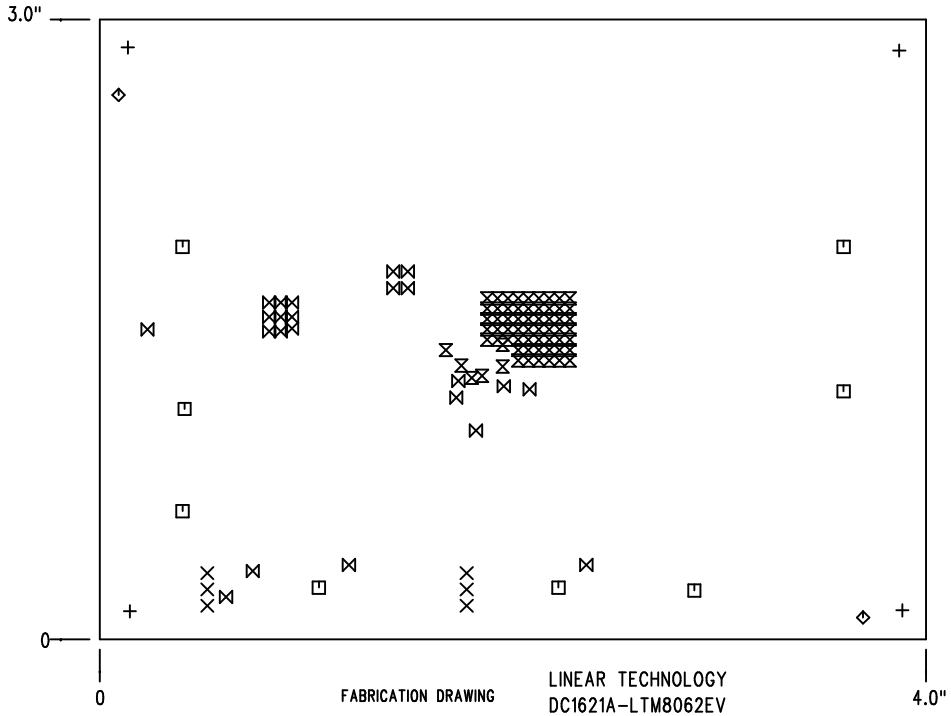
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
–	2	PRODUCTION	JESUS R.	07-01-10



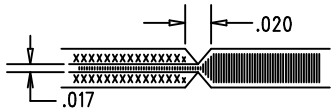
FABRICATION DRAWING

LINEAR TECHNOLOGY
DC1621A–LTM8062EV
32V,2A, μMODULE (R)
POWER TRACKING BATTERY CHARGER
DATE: 07–01–10

LAYER 1 (COMPONENT SIDE)
LAYER 2
LAYER 3
LAYER 4 (SOLDER SIDE)

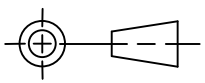
NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC–A–600.
2. MATERIAL: –EPOXY FIBERGLASS, NEMA GRADE FR–4
   –FINISHED THICKNESS TO BE 0.062" +/– .005"
   –TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
   –FLAMMABILITY RATING: 94 V–0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
4. DRILLING: –DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
   –ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
   –HOLE LOCATION TOLERANCES ARE +/–0.003" IN RELATION TO CENTER
5. FINISH: –SMOBC USING LPI BOTH SIDES, COLOR GREEN.
   –GOLD IMMERSION BOTH SIDES. (LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
   –FOR SILKSCREEN: BOTH SIDES USE WHITE NON–CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.185	4	+	NO	+/–0.003
0.031	6	X	YES	+/–0.003
0.095	8	□	YES	+/–0.003
0.07	2	◇	NO	+/–0.003
0.01	63	⧖	YES	+/–0.003
0.012	23	⋈	YES	+/–0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL PER ASME Y14.5M–1994
THIRD ANGLE PROJECTION



APPROVALS	
PCB DES.	AK
APP ENG.	JESUS R.

SCALE = NONE

LINEAR TECHNOLOGY
1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432–1900
www.linear.com

TITLE: FABRICATION DRAWING
32V, 2A, μMODULE (R)
POWER TRACKING BATTERY CHARGER

SIZE	IC NO.	REV
N/A	LTM8062EV DEMO CIRCUIT 1621A	2

FILENAME: DC1621A–2.PCB