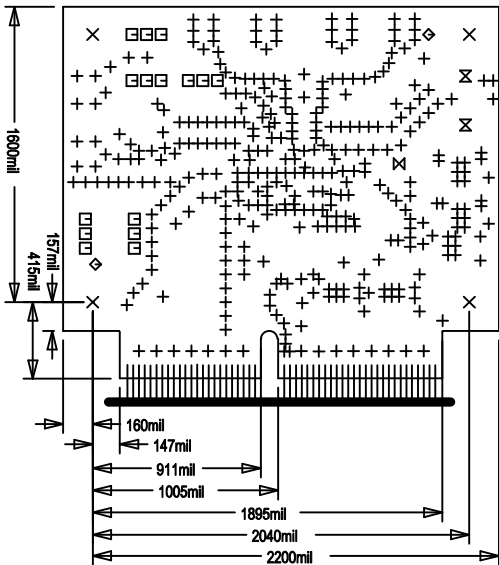


Linear Tech. Corp.
Demo Circuit 1398A

Component Side

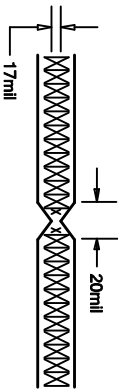


SIZE	QTY	SYM	PLATED	TOL
10	361	+	YES	+/-3mil
187	4	X	YES	+/-3mil
31	15	□	YES	+/-3mil
70	2	◇	NO	+/-3mil
63	1	⊗	YES	+/-3mil
94	2	⊗	YES	+/-3mil

REVISIONS		
REV	APPR	DATE

NOTES : Unless Otherwise Specified

1. FAB PER IPC-A-600.
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.
2 OZ. COPPER FINISH ON EXTERNAL LAYERS.
1 OZ. COPPER FINISH ON INTERNAL LAYERS.
THICKNESS .060 +/- .005 TOTAL OF 4 LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM.
0.00 ARE PRIMARY DATUMS.
3. SIZE: DIMENSIONS SHOWN AND TOLERANCES +/-0.010
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER. .001 INCH THICK MIN. ALL
HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-3mil
INCH IN RELATION TO CENTER
5. FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.
GOLD IMMERSION BOTH SIDES.
6. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
7. DO NOT ALTER ARTWORK TO ADD LOGO OR DATE CODE BUT YOU MAY
MODIFY PAD SIZE TO MEET END FINISH.
8. PCBs SHALL BE RoHS COMPLIANT.
9. SCORING:



10. IMPEDANCE CONTROL: 30mils TRACE = 50 OHMS
11. FINGER AREA: GOLD PLATE TO MIN. OF 0.0008mm
PER MIL-G-45204, TYP II, CL1, GRADE C, OVER
0.005mm LOW STRESS NICKEL PER QQ-N-290, CL2.

Fabrication Drawing

APPROVALS		LINEAR TECHNOLOGY	
DRAWN	INT	DATE	
CHECK			
DESIGN	RudyB		
ENGR	Dwags		
TITLE:		LTM9001IV 16-Bit Receiver Subsystem, Alternate Pinout	
FB	Demo Circuit 1398A	REV. 1	
SCALE = NONE		DES-24xxx	SHT 1 of 1

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