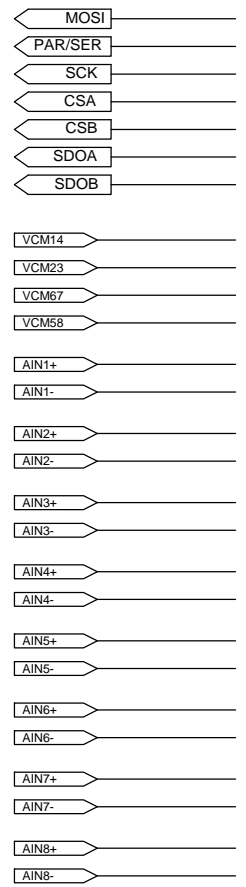
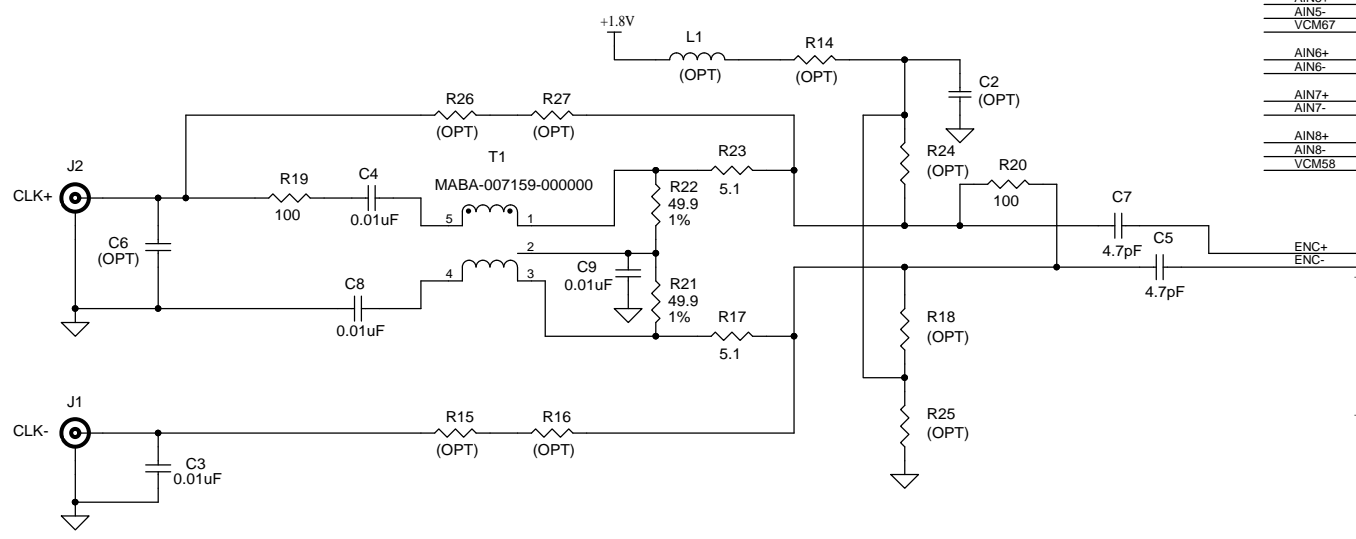
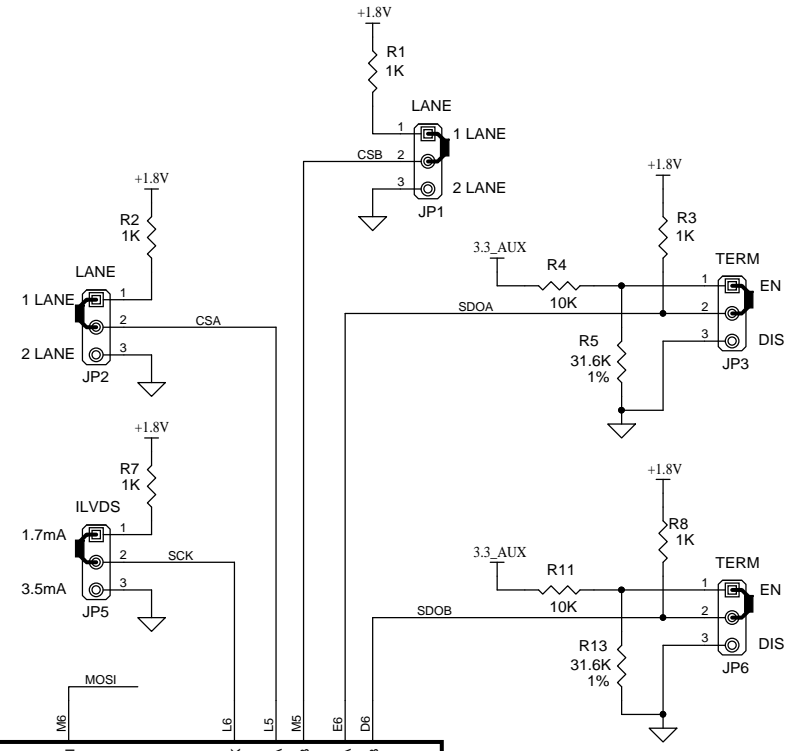
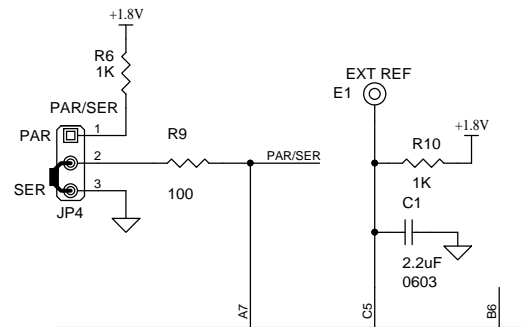
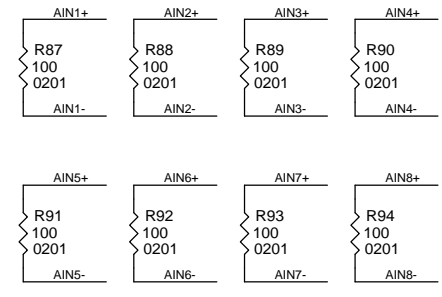


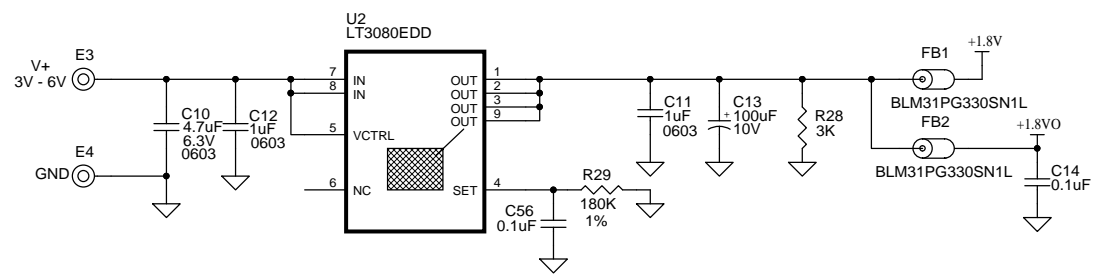
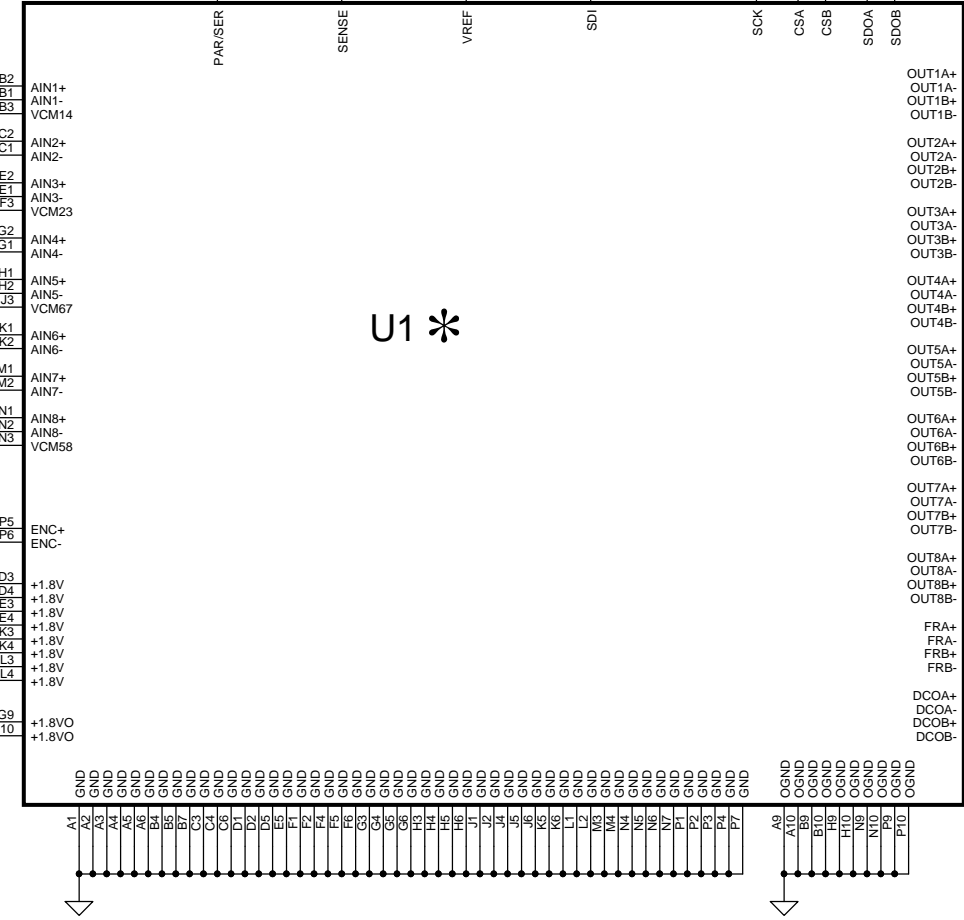
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION	CLARENCE M.	01-18-17



ASSY	U1 *	Bits	Msp/s
DC1751A-A	LTM9011Y-14	14	125
DC1751A-B	LTM9010Y-14	14	105
DC1751A-C	LTM9009Y-14	14	80
DC1751A-D	LTM9008Y-14	14	65
DC1751A-E	LTM9007Y-14	14	40
DC1751A-F	LTM9006Y-14	14	25



AIN1+	B2	AIN1+	B2
AIN1-	B1	AIN1-	B1
VCM14	B3	VCM14	B3
AIN2+	C2	AIN2+	C2
AIN2-	C1	AIN2-	C1
AIN3+	E2	AIN3+	E2
AIN3-	E1	AIN3-	E1
VCM23	F3	VCM23	F3
AIN4+	G2	AIN4+	G2
AIN4-	G1	AIN4-	G1
AIN5+	H1	AIN5+	H1
AIN5-	H2	AIN5-	H2
VCM67	J3	VCM67	J3
AIN6+	K1	AIN6+	K1
AIN6-	K2	AIN6-	K2
AIN7+	M1	AIN7+	M1
AIN7-	M2	AIN7-	M2
AIN8+	N1	AIN8+	N1
AIN8-	N2	AIN8-	N2
VCM58	N3	VCM58	N3



NOTE: UNLESS OTHERWISE SPECIFIED:  
 1. ALL RESISTORS ARE 0402  
 ALL CAPACITORS ARE 0402

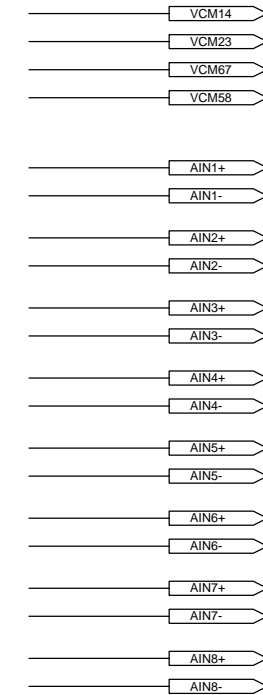
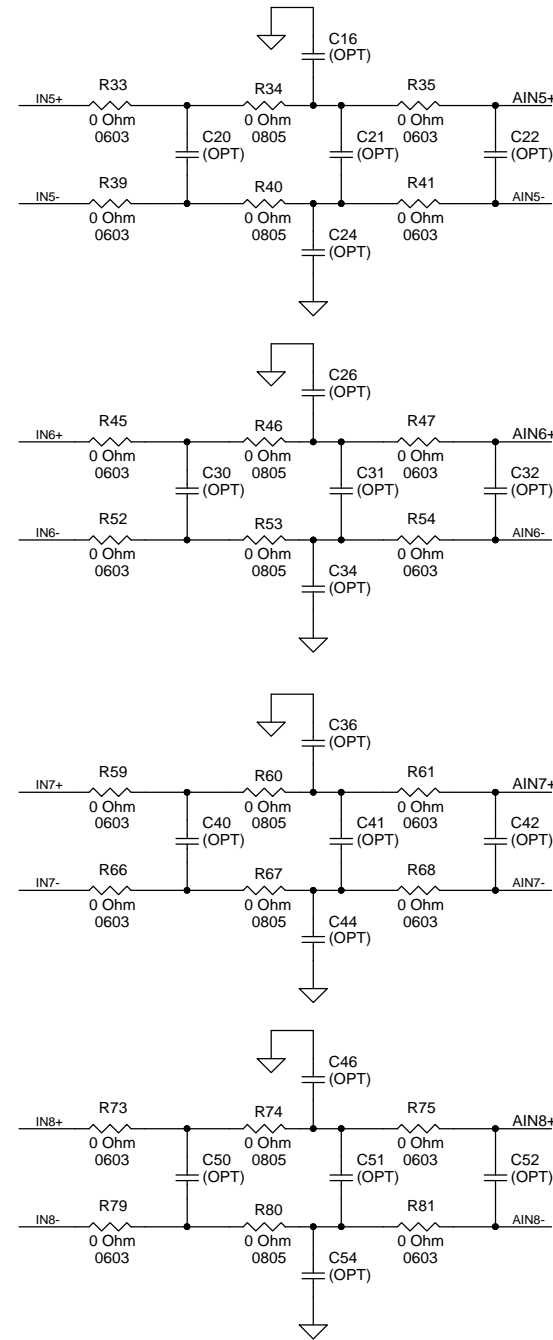
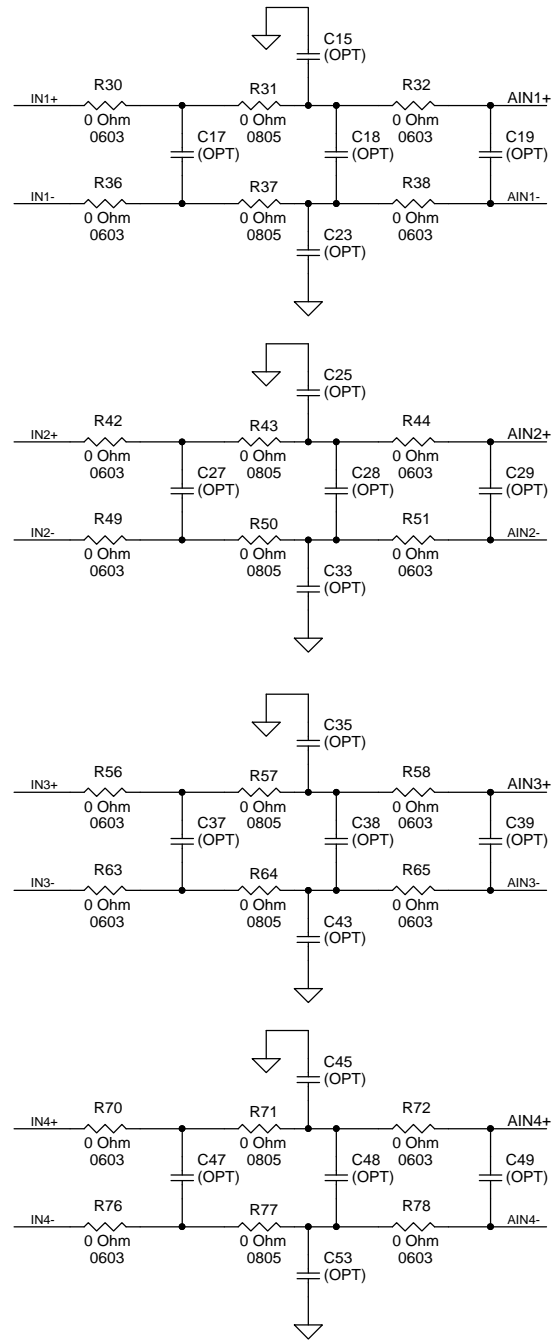
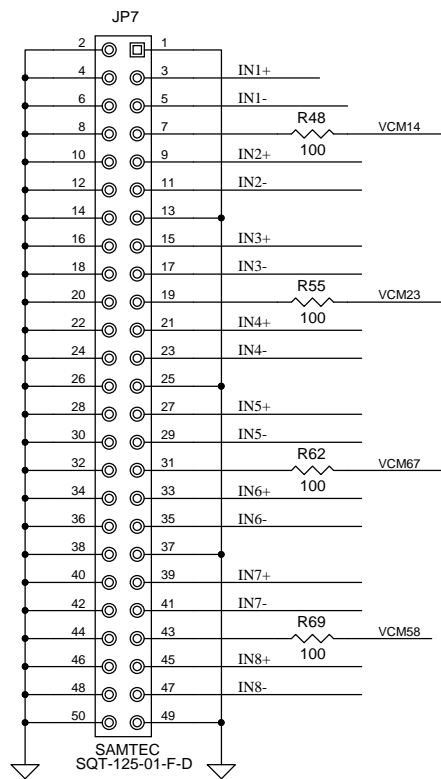
**CUSTOMER NOTICE**  
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS	
PCB DES.	R8
APP ENG.	CLARENCE M.
SCALE	NONE

**LINEAR TECHNOLOGY**  
 1630 McCarthy Blvd.  
 Milpitas, CA 95035 www.linear.com  
 Phone: (408)432-1900  
 Fax: (408)434-0507  
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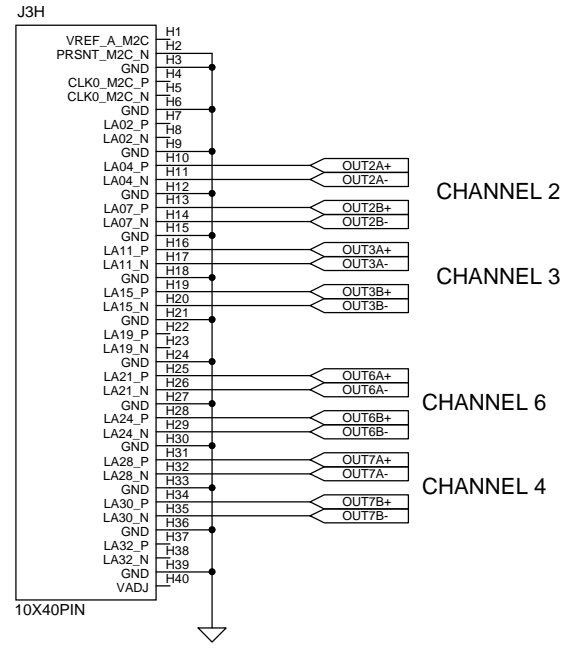
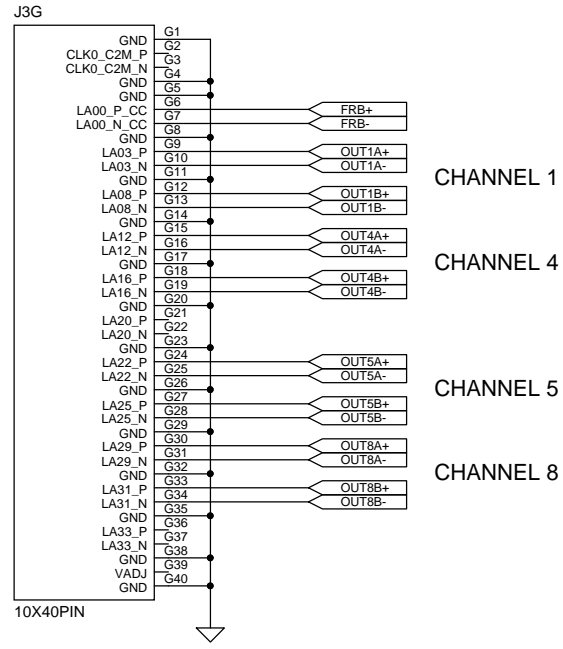
**LOW POWER OCTAL-ADC**

SIZE	IC NO.	REV.
N/A	LTM90XXIY-14 FAMILY	2
DATE:	Wednesday, January 18, 2017	
SHEET 1 OF 3		

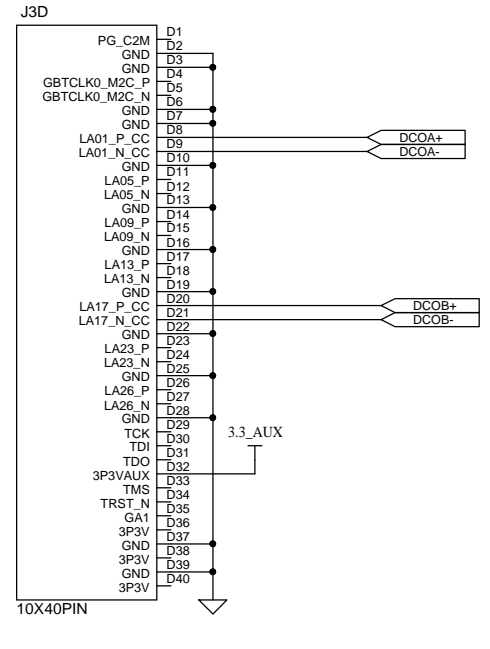


CUSTOMER NOTICE		APPROVALS		LINEAR TECHNOLOGY		1630 McCarthy Blvd. Milpitas, CA 95035 www.linear.com Phone: (408)432-1900 Fax: (408)434-0507 LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY			
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	R/B	TITLE: SCHEMATIC		SIZE	IC NO.	LTM9011IY-14 FAMILY DEMO CIRCUIT 1751A	REV.
		APP ENG.	CLARENCE M.			N/A			2
		THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE		DATE:	Wednesday, January 18, 2017	SHEET 2 OF 3	

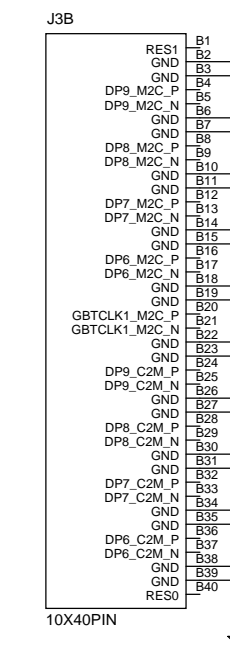
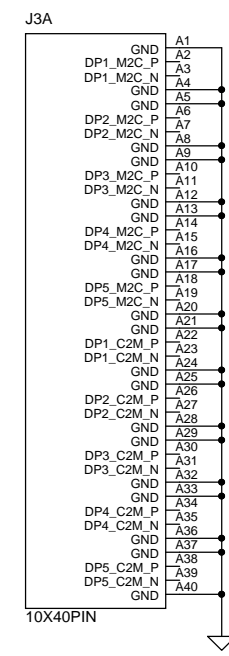
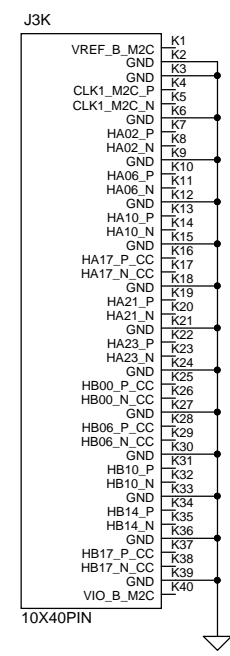
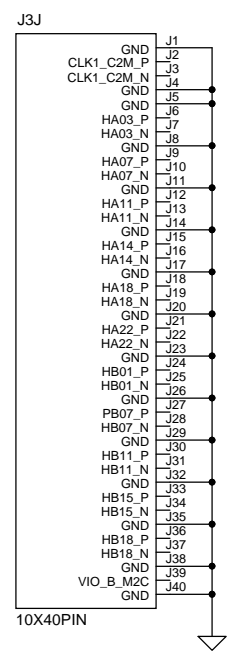
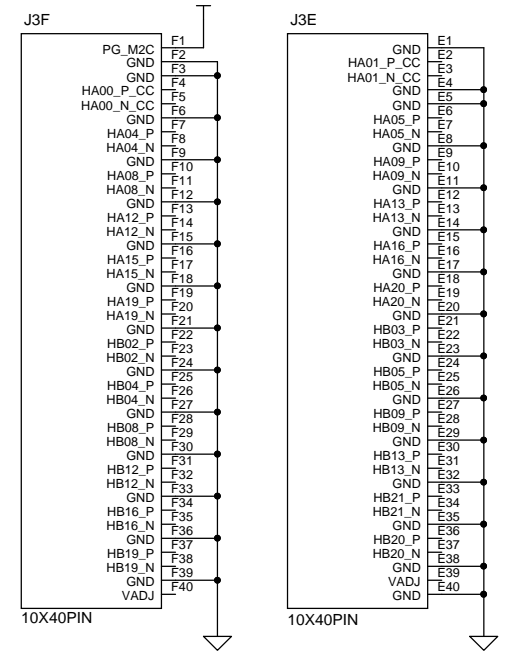
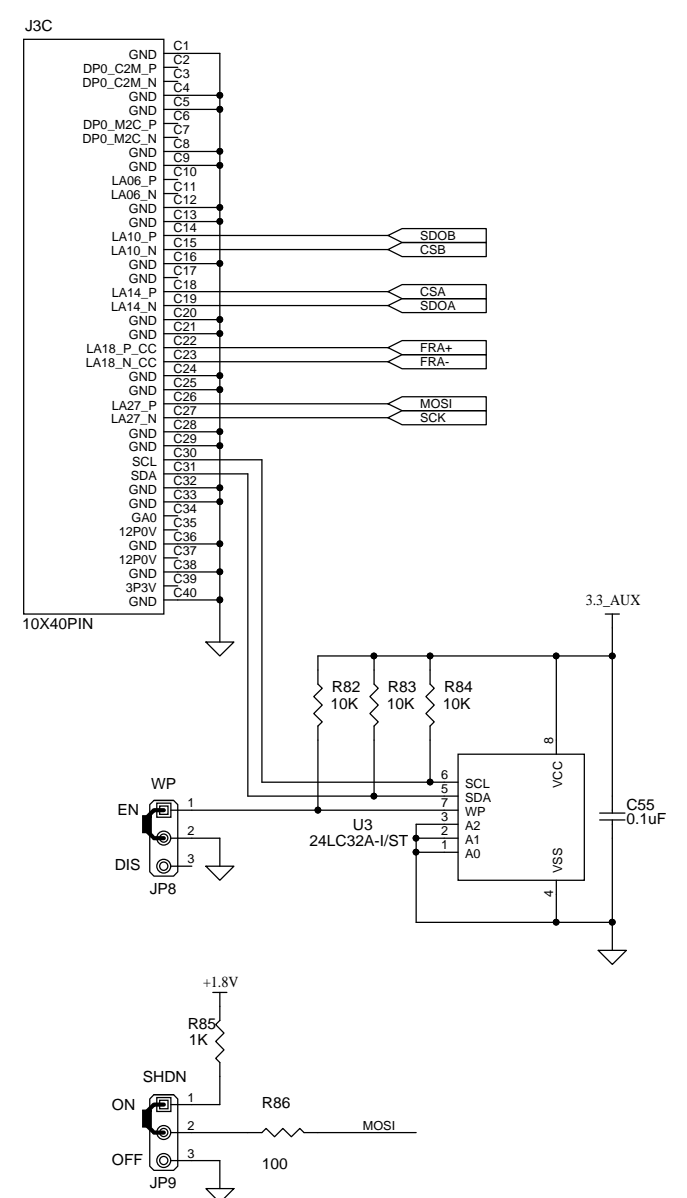
FRAME CLOCK B



DATA CLOCK



FRAME CLOCK A



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THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.				APP ENG.	CLARENCE M.	SIZE N/A IC NO. LTM90XXIY-14 FAMILY DEMO CIRCUIT 1751A REV. 2
				SCALE = NONE		DATE: Wednesday, January 18, 2017 SHEET 3 OF 3