

# ETERNA (TM) CASTELLATED MOTE WITH CHIP ANTENNA

## Content:

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## Notes:

1. Assembly Options:
  - 1.a) X1 & X5: installed crystals (32kHz and 20 MHz resp.)
  - 1.b) R12 TCK termination not installed
  - 1.c) Battery holder not installed
  - 1.d) Accelerometer not installed
  - 1.e) 1/4 Wave stub disconnected

## 2. Associated Documents



**PCB FAB**  
600-0167 REV3



**BOM**  
700-0219 REV2



**ASY DWG**  
705-0167 REV3

## Revision History:

Rev	Description	ECO	Author
01	Initial release Based on 700-0167 rev3 using LTC5800IWR-IPRA	1226	CN
02	Change 32kHz & 20MHz XTAL	1394	RMP



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## CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND IS SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN:

CHECKED:

APPROVED:

ENGINEER:

DESIGNER:



**Linear Technology Corporation**

1630 McCarthy Blvd. Phone: (408)432-1900  
Milpitas, CA 95035 Fax: (408)434-0507

TITLE: LTP5901IPC-IPRC  
PCA SCH, ETERNA CAST. IP MNGR EXT. MEM, RUSSIAN

SIZE A DWG NO. 710-0219 REV 02

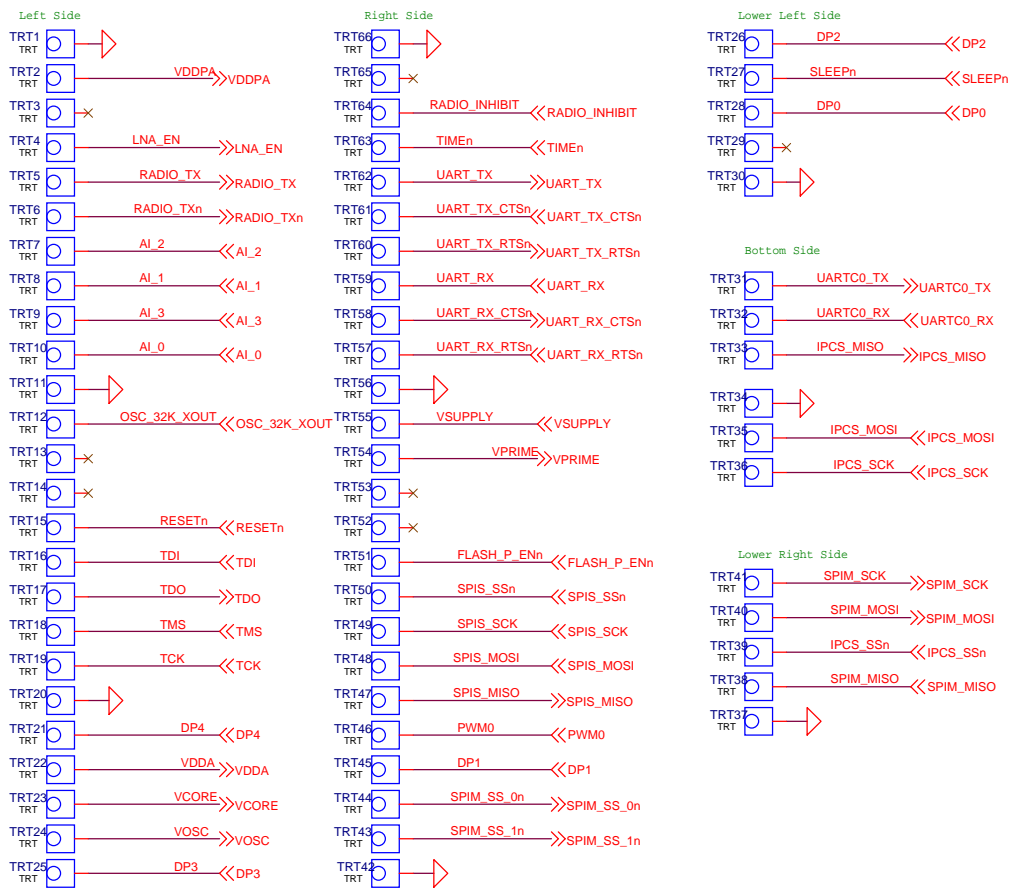
DATE: Wednesday, July 29, 2015 SHEET 1 OF 4







CASTELLATIONS




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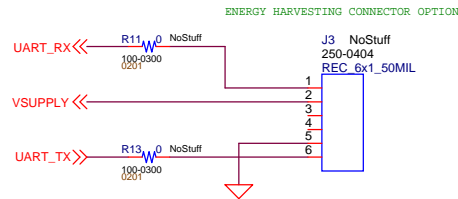
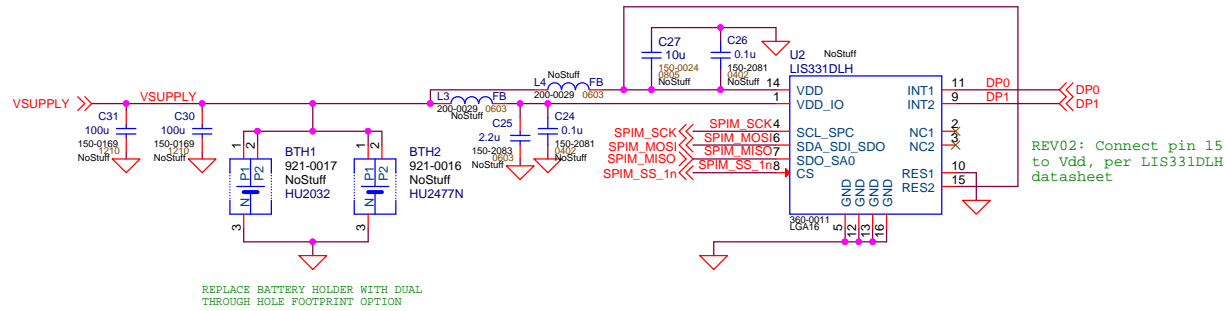
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ENGINEER:				
DESIGNER:	SIZE A	DWG NO. 710-0219	REV 02	
	DATE: Tuesday, July 28, 2015		SHEET 3 OF 4	



# BATTERY HOLDER & ACCELEROMETER OPTIONS



PLACE R11, R13 & J3 ON BOTTOM, MAY INTERFERE WITH BATTERY HOLDER.  
J3 SHROUD SHALL PROTRUDE FROM EDGE OF BOARD OPPOSITE TO CHIP ANTENNA.  
PLACE R11 and R13 NEAR U1 TO MINIMIZE UART\_RX AND UART\_TX NET LENGTH.



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**dust networks™** **Linear Technology Corporation**  
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