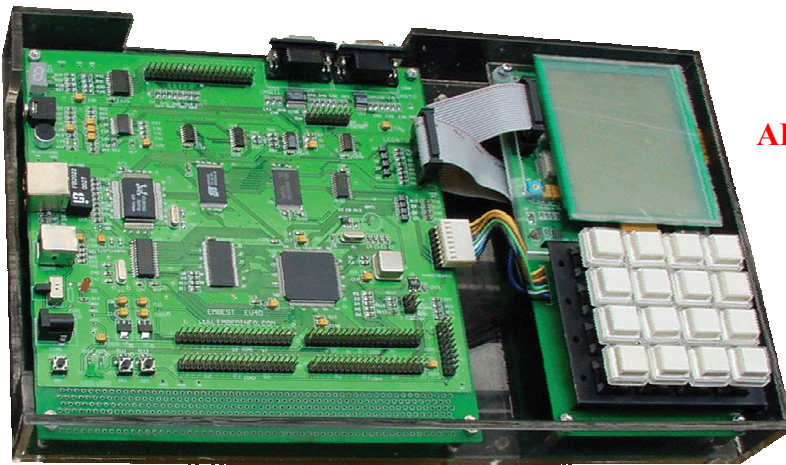


Embest S3CEV40 Evaluation Board



ARM - Samsung S3C44B0X EVB

-----10 BaseT, USB, 2x RS232,
LCD, Boot Code, ucLinux,
uCos, ...

OverView

Embest S3CEV40 Evaluation Board for S3C44B0X is a platform that is suitable for code development of Samsung's S3C44B0X 16/32-bit RISC microcontroller (General ARM) for hand-held device and general applications.

S3C44B0X consists of 16-/32-bit RISC (ARM7TDMI) CPU core, 8KB cache, optional internal SRAM, LCD controller (up to 256 color DSTN), 2-ch UART with hand-shake(IRDA1.0,16-byte FIFO), 4-ch DMA, System manager (chip select logic, FP/ EDO/SDRAM controller), 6-ch timers with PWM, 71-bit general purpose I/O ports, RTC, 8-ch 10-bit ADC, IIC-BUS interface, IIS-BUS interface, Sync. SIO interface and PLL for clock.

Embest S3CEV40 EVB consist of S3C44B0X, boot Flash, SDRAM, LCD&TSP touch screen interface, two serial communication ports, ethernet interface, USB interface, keyboard interface, IIS(sound) interface, exterior IDE port, Nand Flash, RTC, JTAG interface and 8-SEG.

Embest S3CEV40 EVB provide a high performance and low cost solution for embedded engineers.

Board Supporting Package Contents

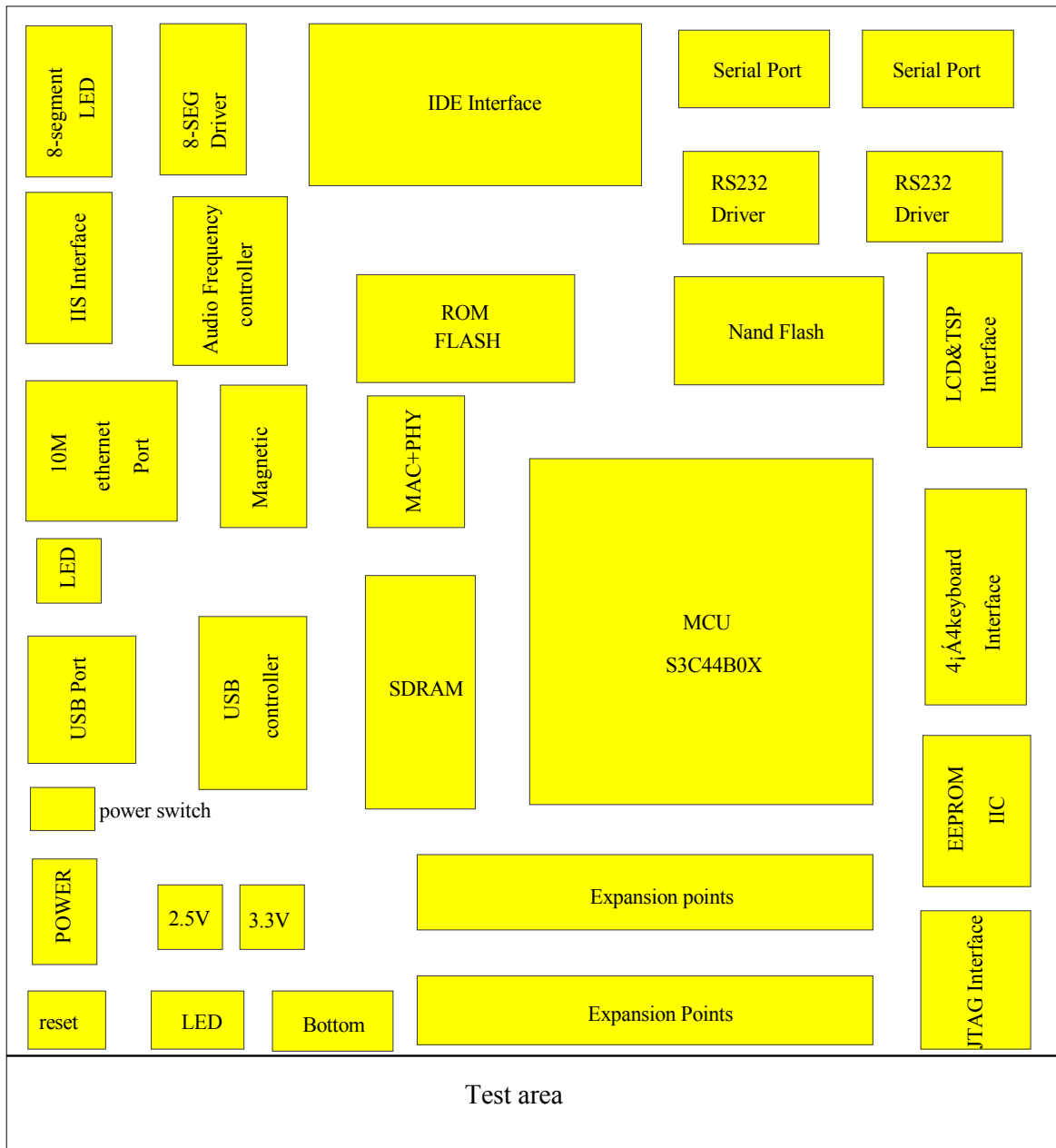
Embest S3CEV40 EVB board supporting package contens the following components:

- Embest S3CEV40 Evaluation Board
- a DB9 plug-to-DB9 socket straight-through serial cable
- 5.0V DC Power supply
- USB cable
- Board Software and Documentation CD-ROM.

The evaluation board is capable of supporting different kinds of debugging systems, using an ICE interface.

Circuit description

Embest S3CEV40 EVB consists of logic components, several control/status display block, and a debug interface block. S3CEV40's detailed block diagram, and its components are shown in below figure .



Hardware Specification

- 5.0V DC or USB power supply
- 2 M bytes 16-bit Flash
- 8 M bytes 16-bit SDRAM
- 4K bit EEPROM with IIC BUS
- Two serial ports
- Reset button
- two interrupt buttons and two LEDS

- external IDE port
- LCD&TSP touch screen port
- 20-pin JTAG interface connector
- USB connecter
- 4 groups 2×20 PIN expansion connectors of CPU
- 10M Ethernet interface connector
- 8-SEG display
- IIS Interface: Speaker & Microphone
- 16 M Bytes Nand Flash
- Test area & test pin
- 320×240 LCD * Option
- 4×4 keyboard * Option

Software Specification

- Boot software and test software
- sample codes
- uCos source tree
- uClinux source tree
- Preload uClinux binary image in flash

Power Supply

S3CEV40 is designed to operate at 2.5V for core and 3.3V for I/O. Power to S3CEV40 is supplied through a DC jack power adapter which supports the 3.3V voltage drives the current .

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