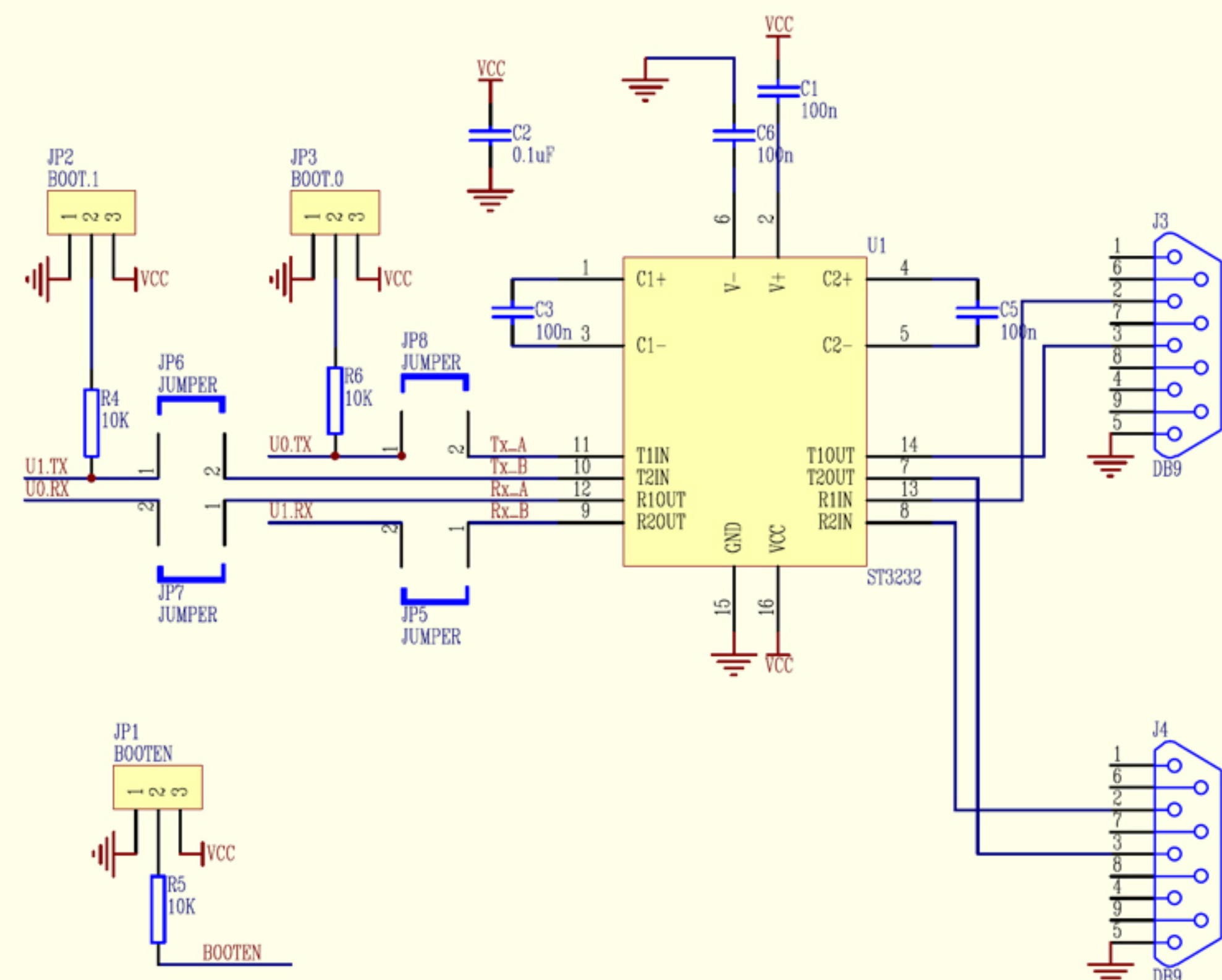
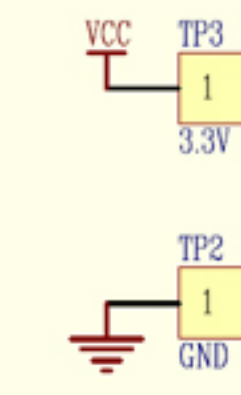
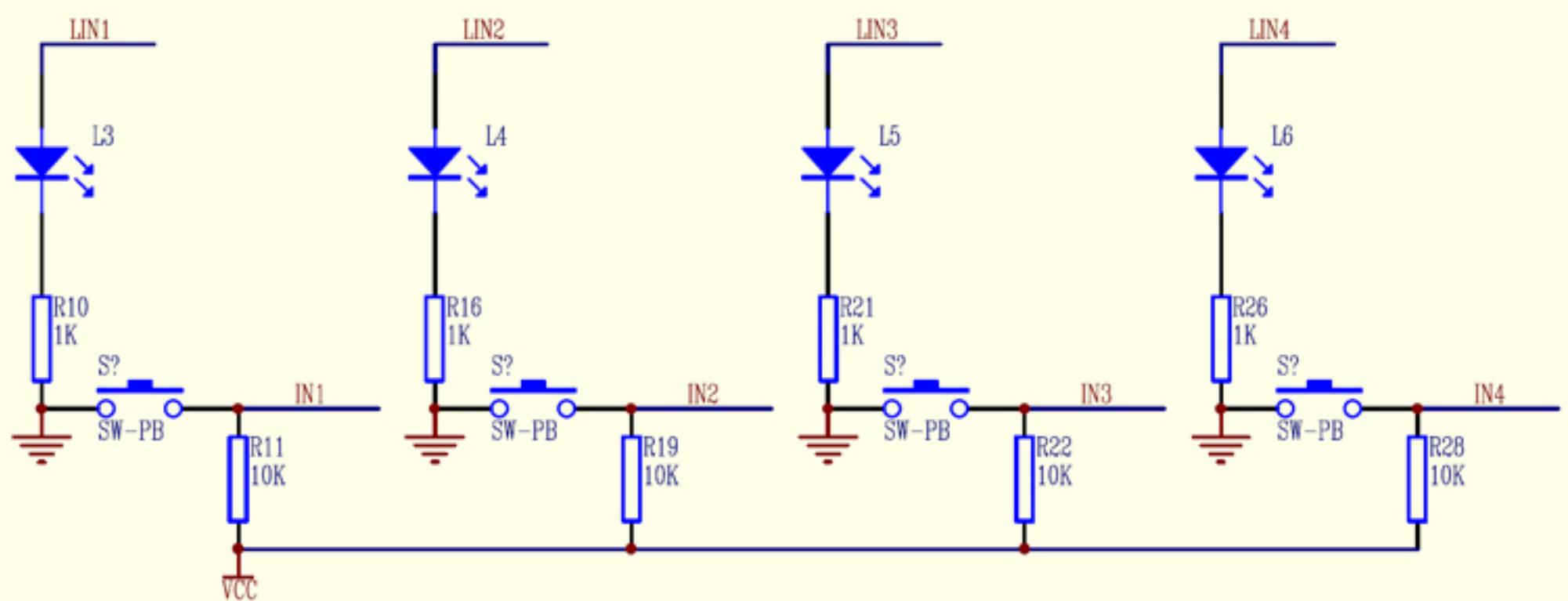
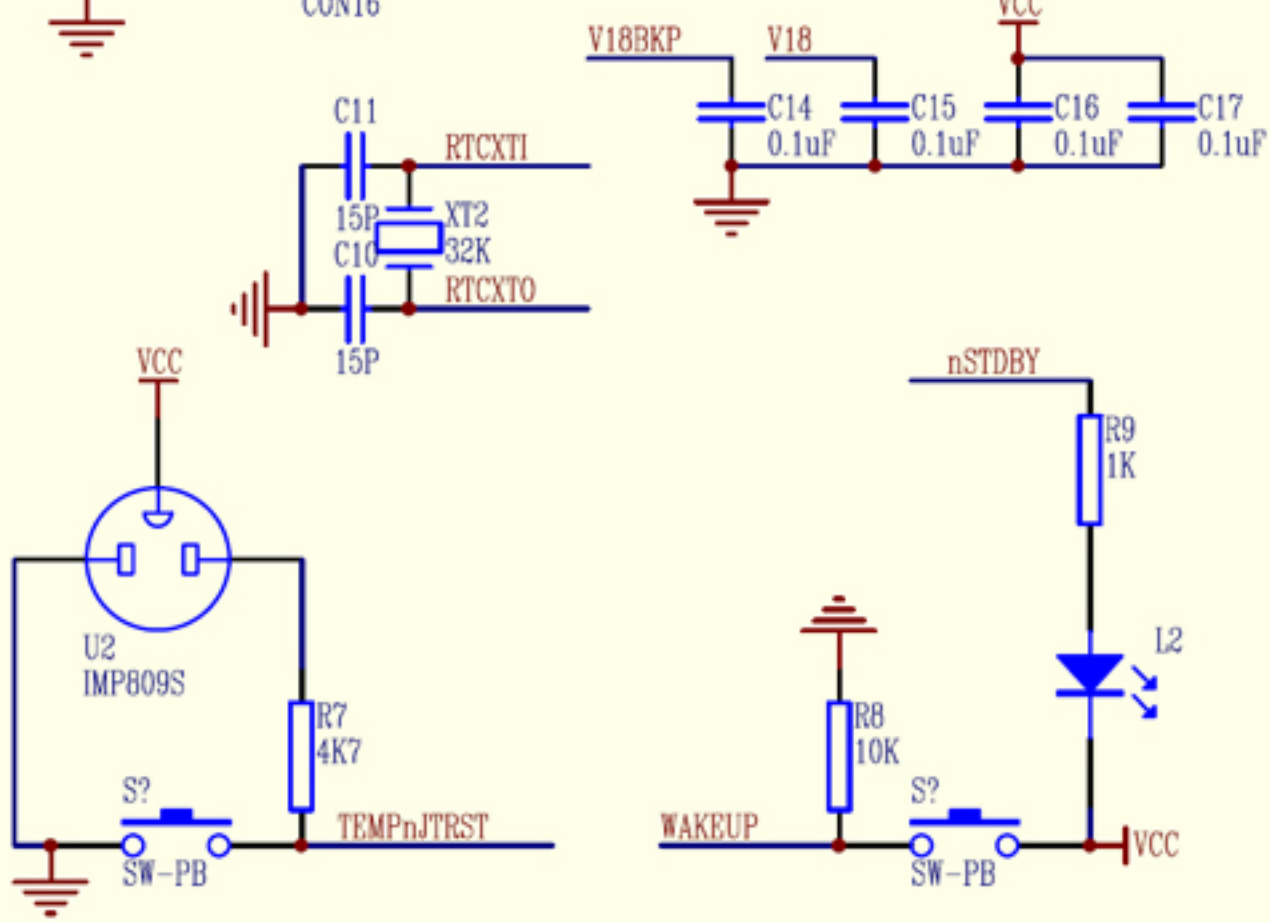
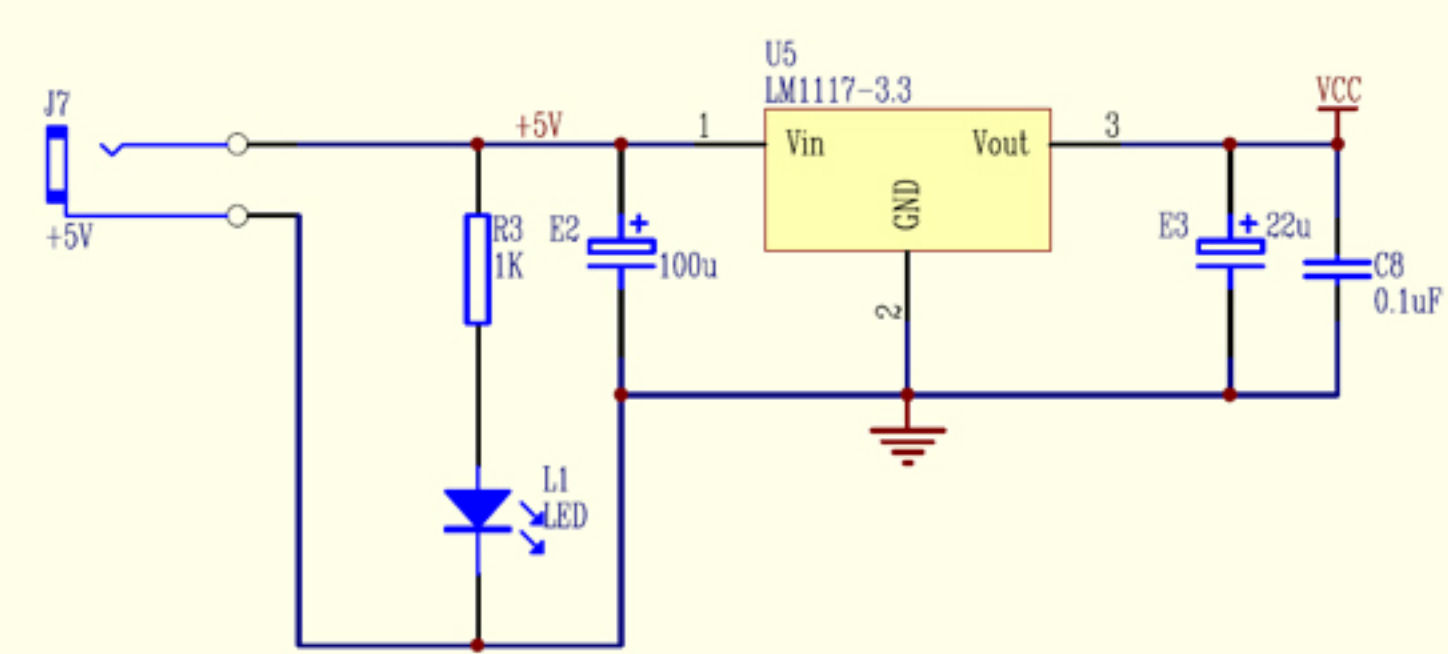
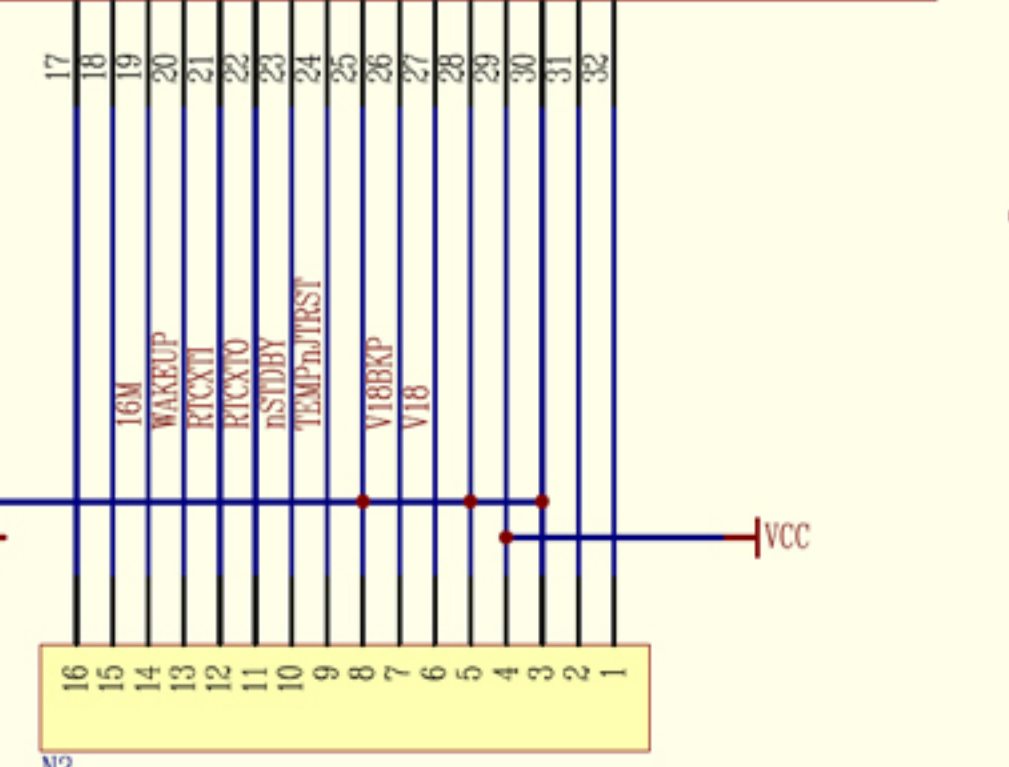
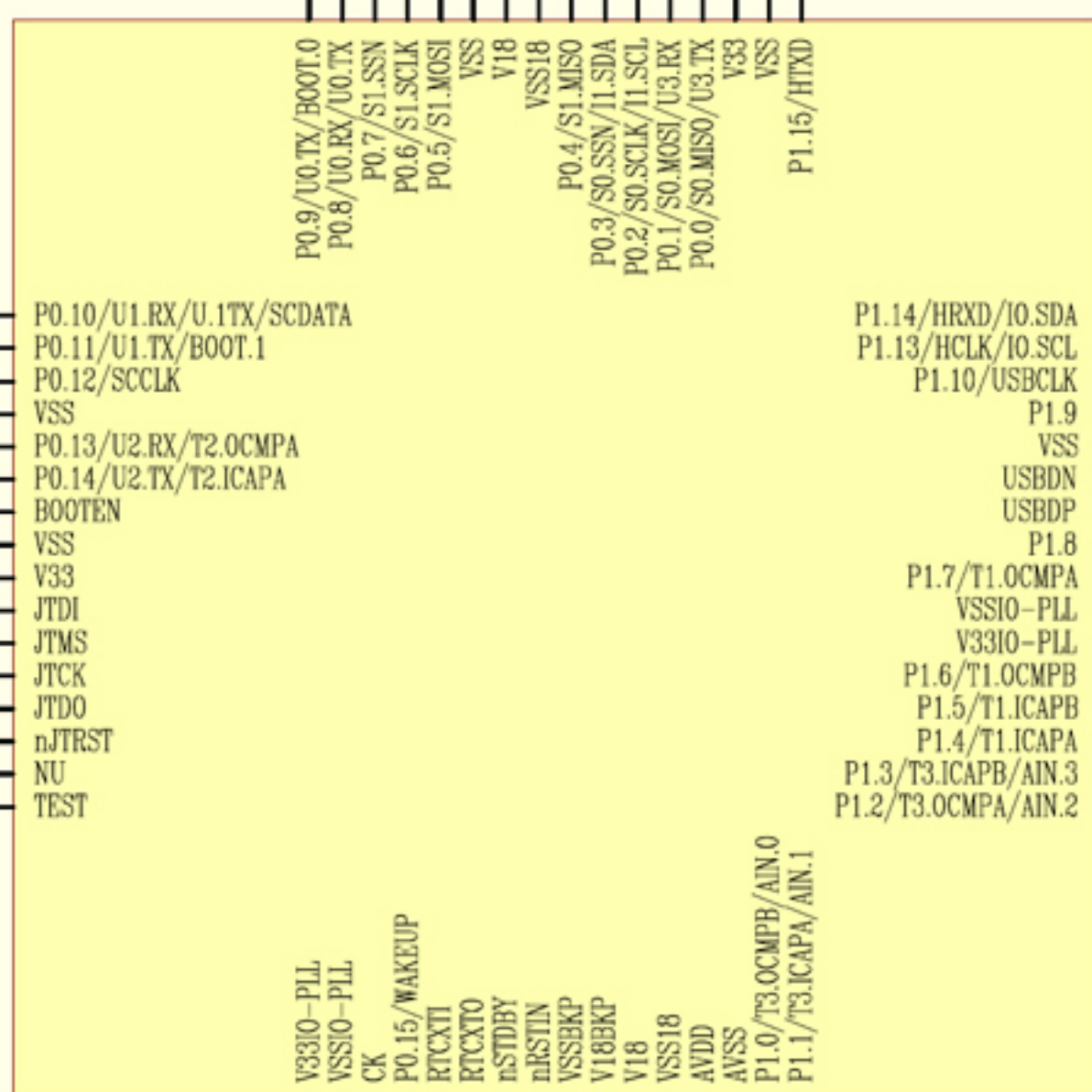
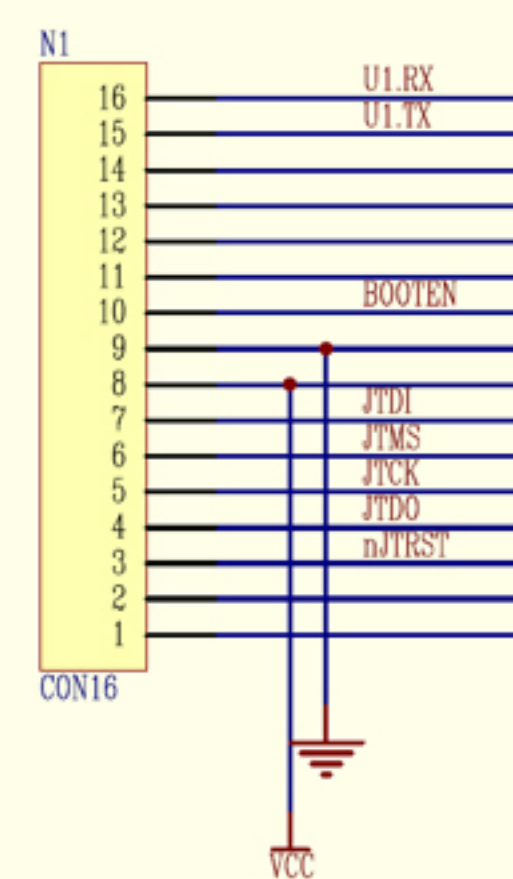
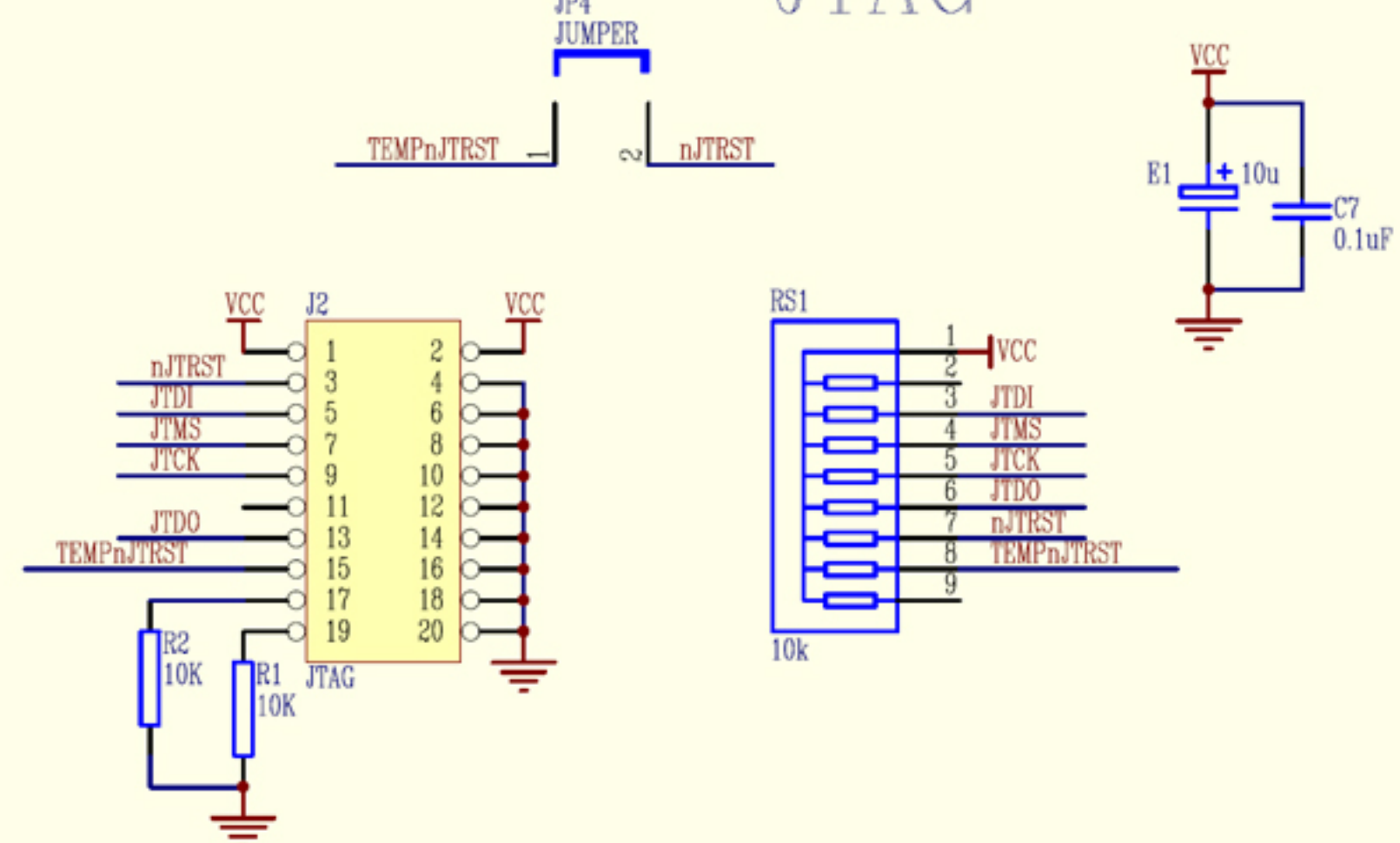


# RS232



# JTAG



Title		
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File:	D:\lab_program\ST_Demo_board\EV71x.ddb	Drawn By: