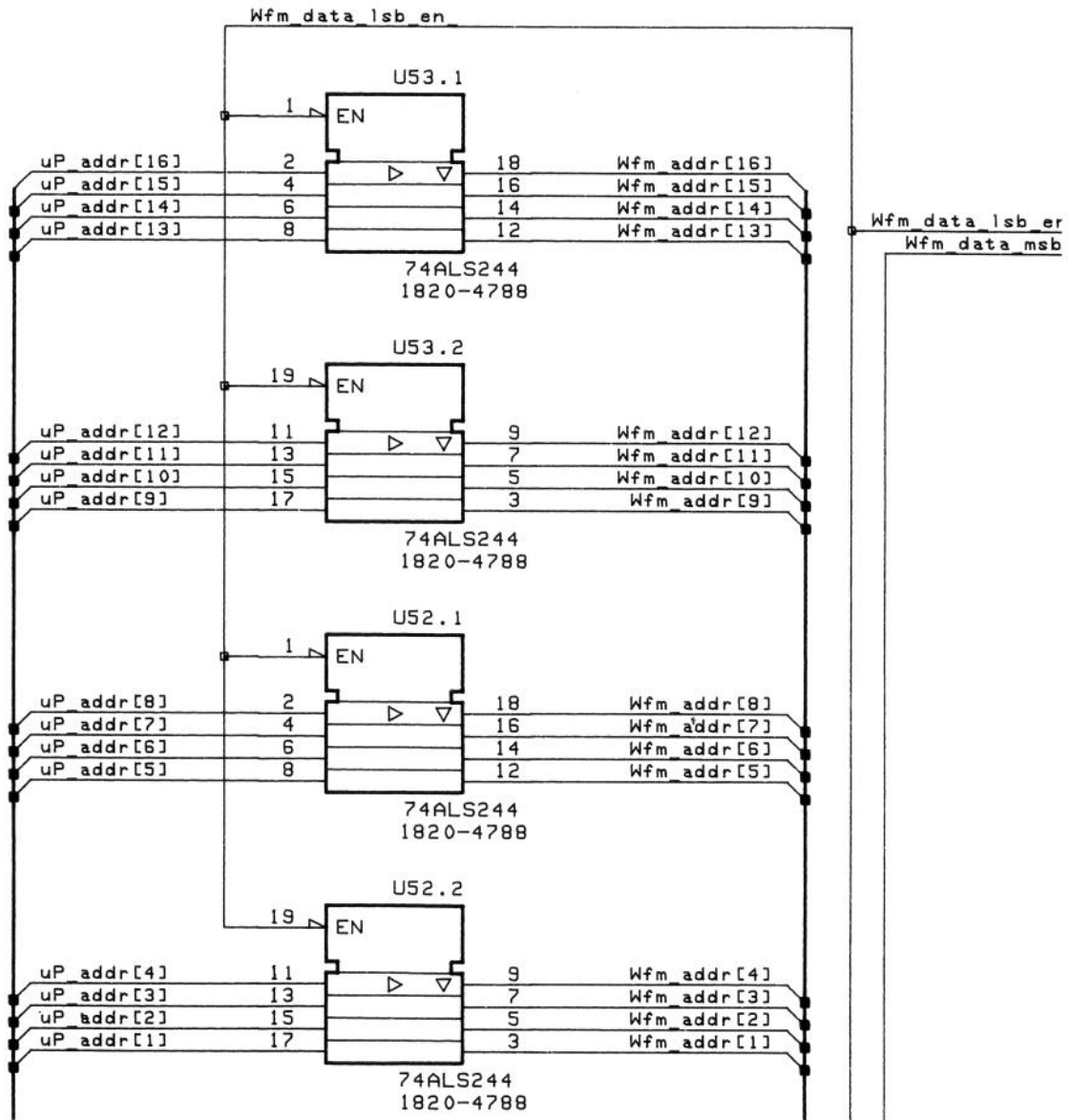


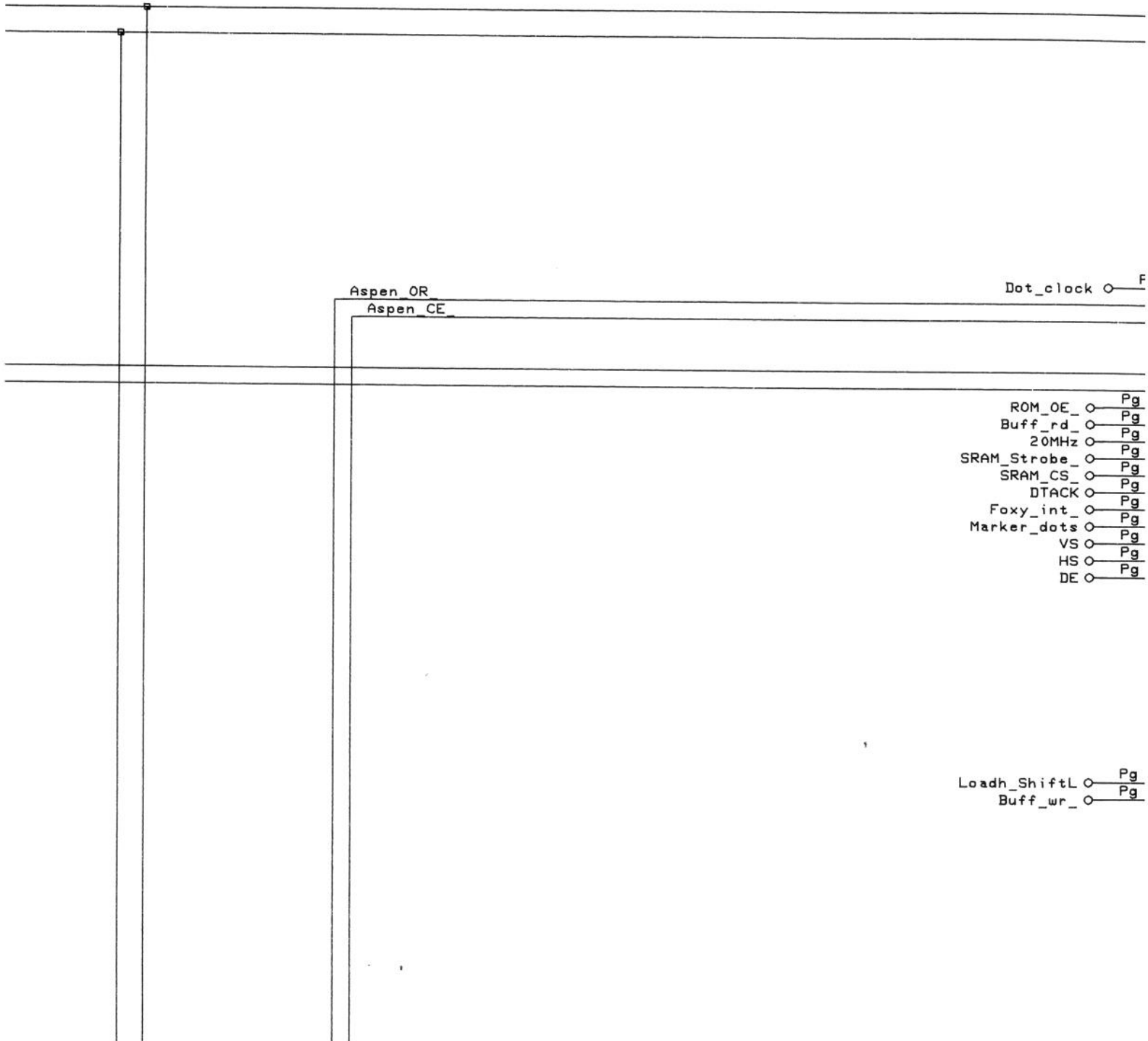
sheet 1 of 11

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Sys\_Reset\_ Pg 2  
uP\_Rwr\_ Pg 2

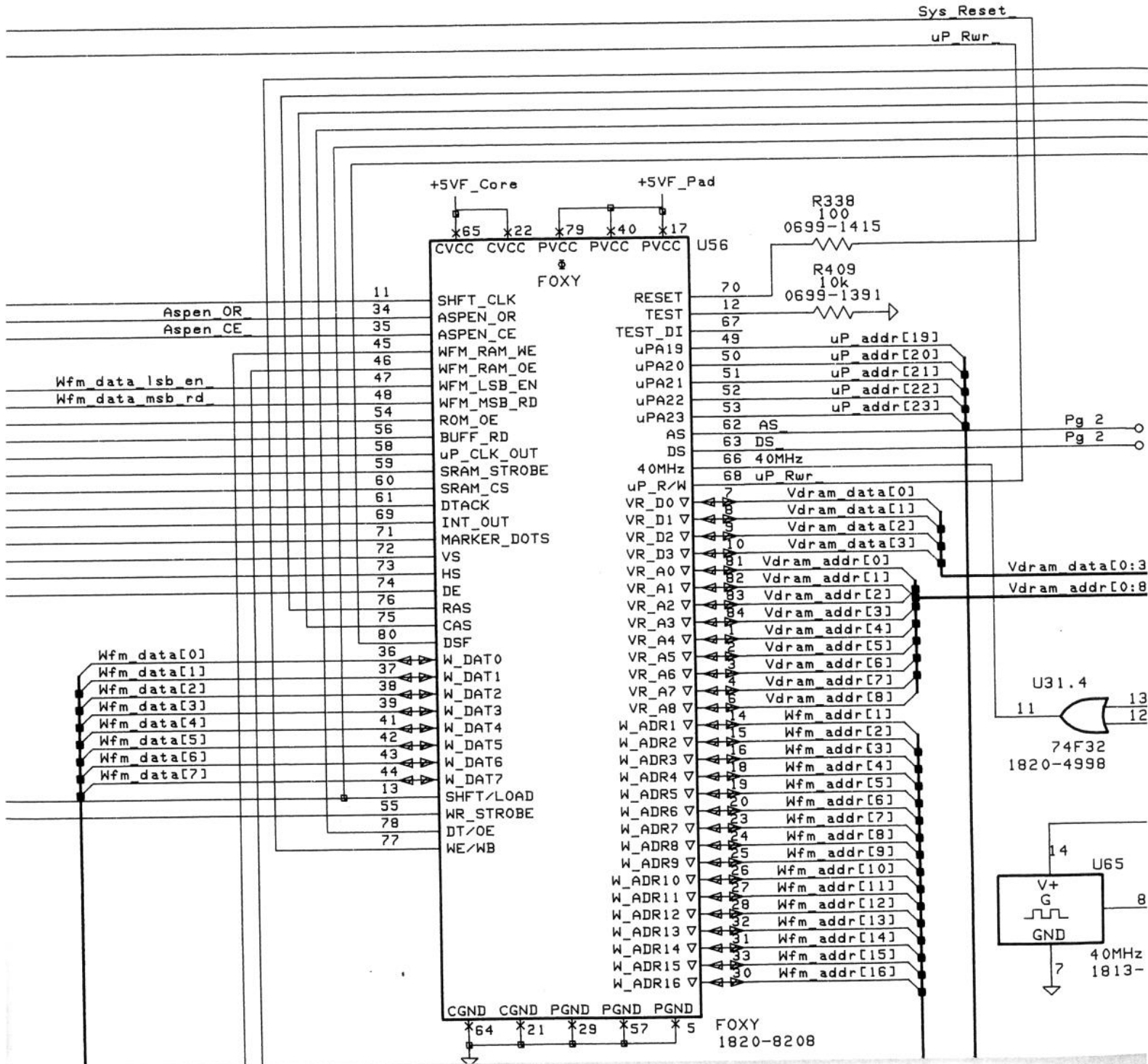


sheet 1 of 11  
page 2 upper



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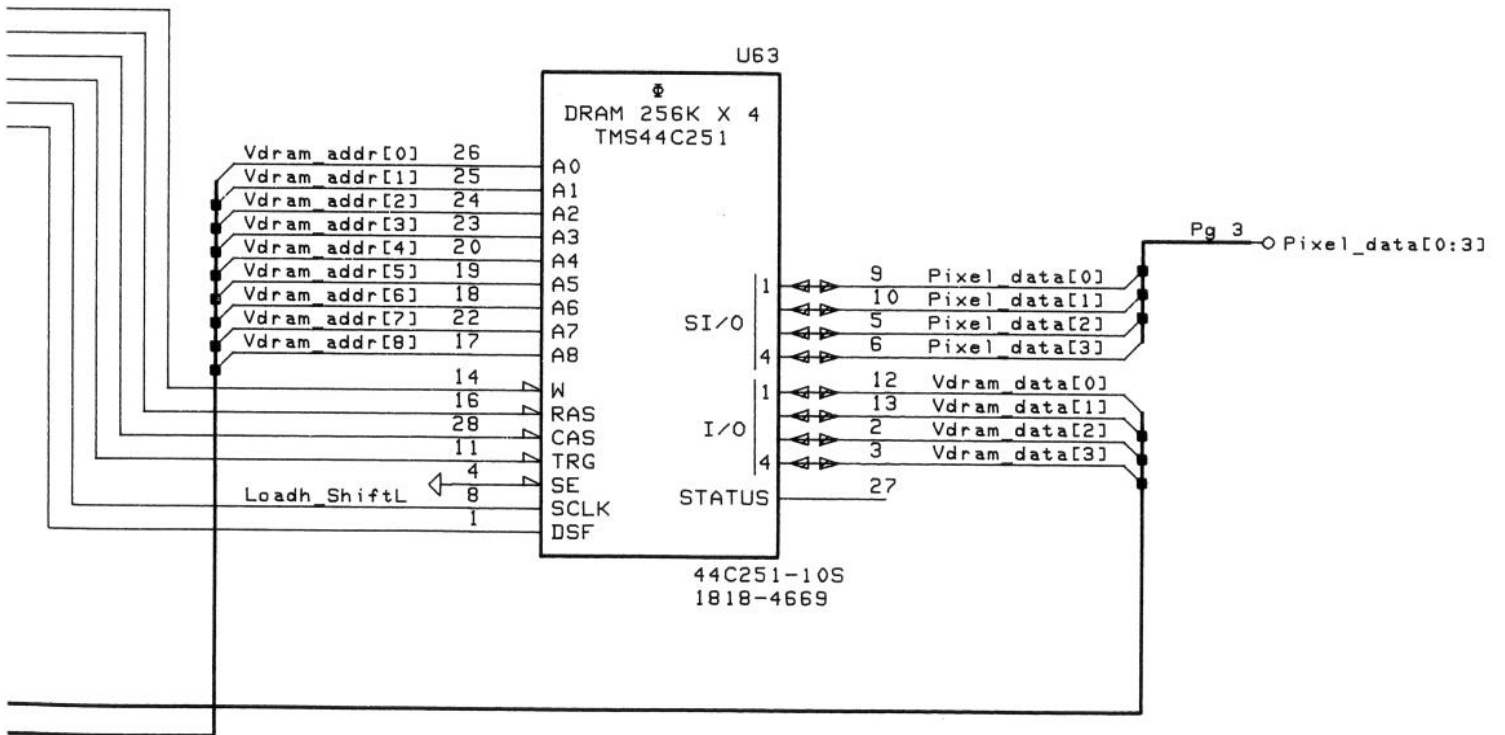


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UNIX/DCS /data/specs/dcs/54600/66501

9		11	12	14	15			D- 54600-66501-3		
29	30	32	33	38	43	SYM	REVISIONS		APPROVED	DATE
						A	AS ISSUED		JS	



R400  
 100  
 19-1415

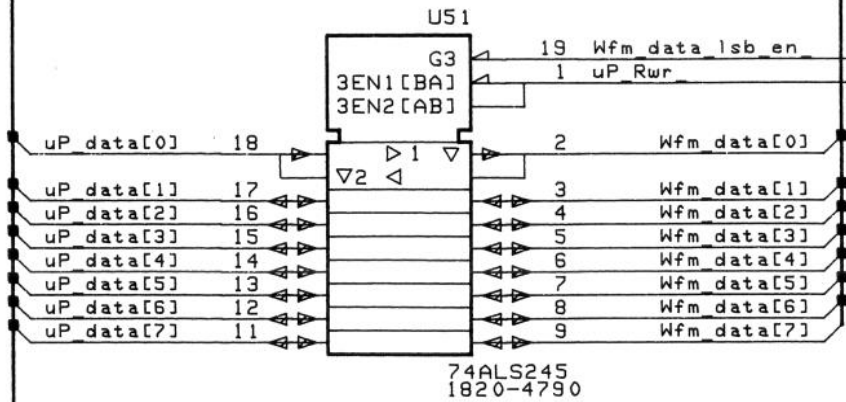
E5  
 CB322513  
 9170-1421  
 +5V  
 C146

uP\_addr[1:23] Pg 2,3

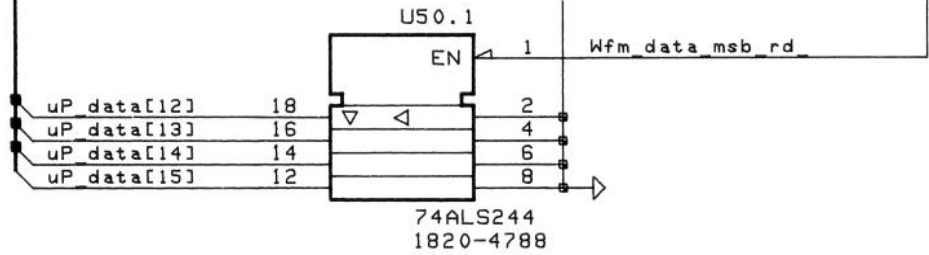
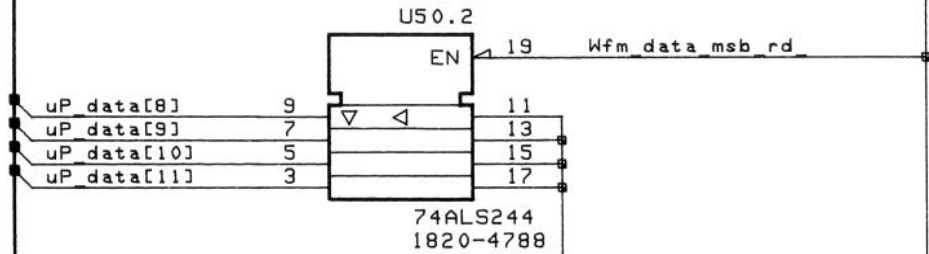
uP\_data[0:15] Pg 2,3

Wfm addr[

Wfm data[



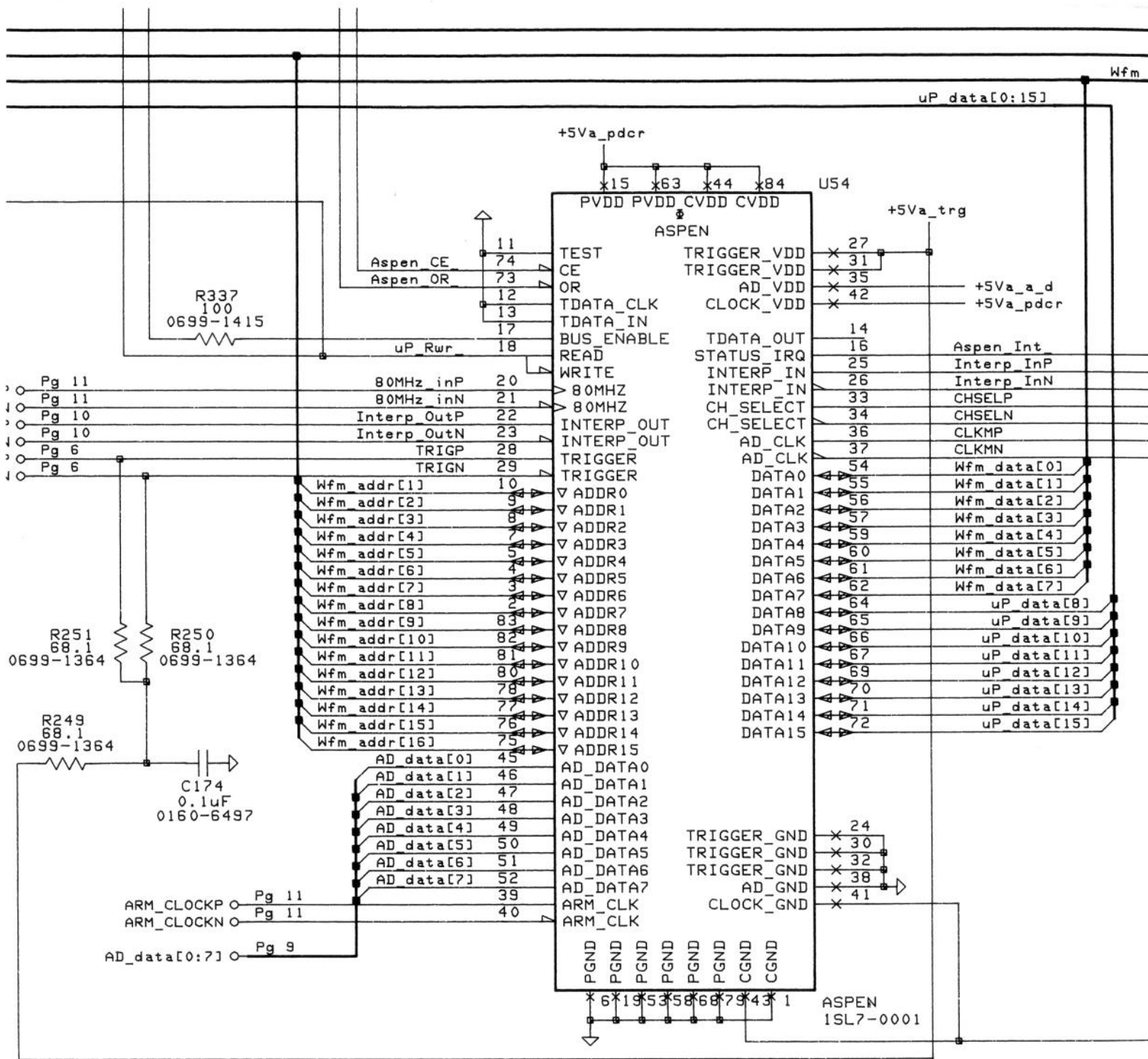
80Mf  
80Mf  
Interf  
Interf



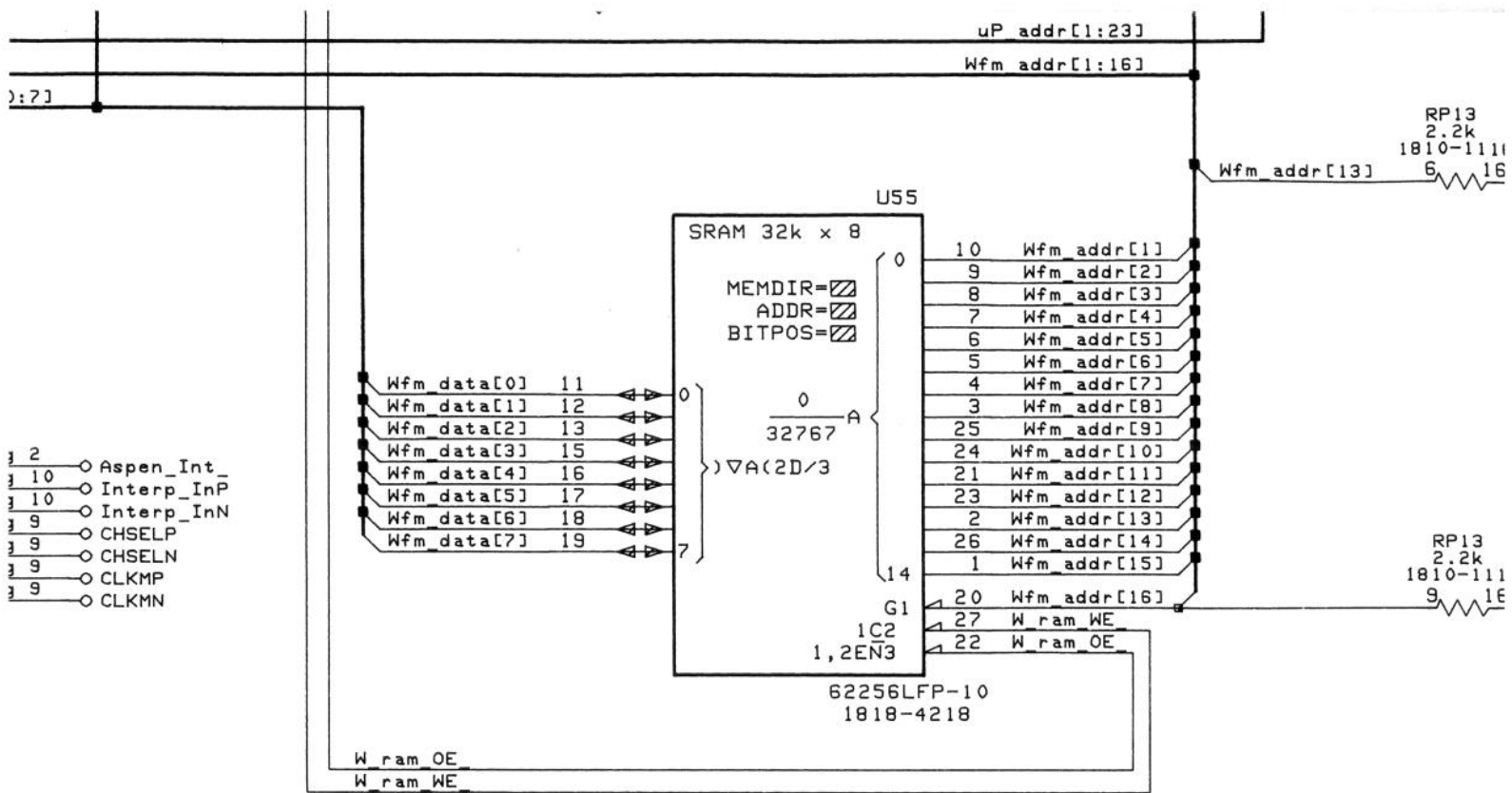
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Page 1 lower



sheet 1 of 11  
 page 2 lower




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Page 3 lower

+5V

+5V

Parts on this schematic:				
C146, 174				
E5				
R337, 338, 249-251, 400, 409				
RP13.6, .9				
U31.4, 50-56, 63, 65				
Power & Ground connections:				
	U31,65	U50-53	U55	U63
+5V	14	20	28	21
GND	7	10	14	7,15

Page 4 lower

DRAWN BY	JIM STRINGER	DATE	6/27/91	WAVEFORM BUS	 <b>HEWLETT PACKARD</b>
ENGINEER					
RELEASE TO PROD.		TITLE		PART NUMBER	
APPROVED BY		SCALE	SHEET 1 OF 11	D-54600-66501-3	