

APPENDIX H - ABBREVIATIONS

A		D	
AA	Announced Address	D/A	Digital to Analog
AC	Altitude Code	DAC	Digital to Analog Converter
AC/ac	Alternating Current	dB	Decibel
ACAS	Airborne Collision Avoidance System	dBi	Decibels above isotropic
ACS	Comm-A Capability Subfield	dBm	Decibels above one milliwatt
A/D	Analog to Digital	dB W/m ²	Decibels above one watt per square meter
ADC	Analog to Digital Converter	DCXO	Digitally Compensated Crystal Oscillator
AIS	Comm-A Capability Subfield	DET	Detector
ALT	Altitude	DF	Downlink Format
AMPL	Amplifier	DI	Designator Identification
ANT	Antenna	Div	Division
AP	Address Parity	DMA	Direct Memory Access
AQ	Special Acquisition	DMM	Digital Multimeter
ATC	Air Traffic Control	DPCS	Dual Port RAM Chip Select
ATC	ATCRBS (on screen)	DPOE	Dual Port RAM Output Enable
ATCRBS	Air Traffic Control Radar Beacon System	DPR	Dual Port RAM
ATE	Automatic Test Equipment	DPSK	Differential Phase Shift Key
ATTEN	Attenuation	DR	Downlink Request
AUTO	Automatic	DRAM	Dynamic RAM
AVG	Average	DRVR	Driver
		DSP	Digital Signal Processor
		DTR	Data Terminal Ready
	B		E
BATT	Battery	ECS	Extended Capability Subfield
BCS	Comm-B Capability Subfield	ELM	Extended Length Message
BDS	B-Definition Subfield	EOC	End of Cycle
BIT	Built In Test	EPROM	Erasable Programmable Read-Only Memory
BOT	Bottom	ERP	Effective Radiated Power
BPF	Bandpass Filter	ESB	Encoded Sense Bits
BR	Bridge Rectifier	Ext	External
BT	Battery		
	C		F
C	Centigrade	FCC	Federal Communications Commission
CA	Transponder Capability	FET	Field Effect Transistor
CAS	Column Address Strobe	FIFO	First In First Out
CFS	Continuation Subfield	FPM	Feet Per Minute
CHAN	Channel	FREQ	Frequency
CHC	Cancel Horizontal Resolution Advisory Complement	FS	Flight Status
CLI	Coordination Lock Indicator	Ft	Feet
CMOS	Complementary Metal-Oxide Semiconductor		
COMM	Communication	GEN	Generator
COMP	Compensator	GND	Ground
CONT	Control	GPIB	General Purpose Interface Bus
Cont	Continued		
COMPTR	Comparator		
CR	Diode		
CTS	Clear to Send		
CVC	Cancel Vertical Resolution Advisory Complement		
CW	Continuous Wave	h	Hexadecimal
		HET	Heterodyne
		hr	Hour
		HRC	Horizontal Resolution Advisory Complement
		Hz	Hertz

I		N	
ID	Identification (4906 Code)	N/A	Not Applicable
IDS	Identifier Designators Subfield	NC	C-Segment Number
IF	Intermediate Frequency	ND	D-Segment Number
II	Interrogator Identification	nmi	Nautical Miles
IIS	Interrogator Identification Subfield	NO	Number
INTERR	Interrogation	ns	Nanosecond (10 ⁻⁹)
I/O	Input/Output		
ITM	Intermode		
K		P	
KE	ELM Control	PAL	Programmable Array Logic
kHz	Kilohertz (10 ³)	PC	Protocol
km	Kilometer (10 ³)	PC	Printed Circuit
ks	Knots (Velocity)	PCB or PCBD	Printed Circuit Board
		PCS	Peripheral Chip Select
		PI	Parity/Interrogator Identity
		PLCS	Places
		PPG	Pulse Power Gate
		PPM	Pulse Position Modulation
		PR	Reply Probability
		PRF	Pulse Repetition Frequency
		PROM	Programmable Read-Only Memory
		PSK	Phase Shift Keying
		PWM	Pulse Width Modulation
		PWR	Power
L		R	
LC	Inductor-Capacitor	RAM	Random Access Memory
LCA	Logic Cell Array	RAC	Resolution Advisory Complement
LCD	Liquid Crystal Display	RAS	Row Address Strobe
LCK	Coordination Lock Subfield	RC	Reply Control
LCS	Lower Chip Select	RC	Resistor-Capacitor
LED	Light Emitting Diode	RCV	Receive
LO	Local Oscillation	RCVR	Receiver
LOAMP	Logarithmic Amplifier	Ref	Reference
LOG	Logarithmic	REFLD	Reflected
LOS	Lockout Subfield	RF	Radio Frequency
LPF	Low-Pass Filter	RGLTR	Regulator
LSD	Least Significant Digit	RI	Reply Information, Air-to-Air
		RL	Reply Length
		RMS	Root Mean Square
		ROM	Read Only Memory
		RR	Reply Request
		RRS	Reply Request Subfield
		RSS	Reservation Status Sub-field
		RTCA	Radio Technical Commission for Aeronautics
		RX	Receive
M			
m	Meters		
MA	Comm-A Message		
Max	Maximum		
MB	Comm-B Message		
MBS	Multisite Comm-B Subfield		
m	Meters		
MA	Comm-A Message		
Max	Maximum		
MB	Comm-B Message		
MBS	Multisite Comm-B Subfield		
MC	Comm-C Message		
MCS	Mid-Range Chip Select		
MD	Comm-D Message		
MEAS	Measure		
MES	Multisite ELM Subfield		
MHz	Megahertz (10 ⁶)		
MID	Mode S Address		
MOD	Modulator		
MON	Monitor		
MPU	Microprocessing Unit		
ms	Millisecond (10 ⁻³)		
MSD	Most Significant Digit		
MTB	Multiple Threat Bit		
MTL	Minimum Triggering Level		
MU	Comm-U Message		
MUX	Multiplexer		
MV	Comm-V Message		

S		X	
SAM	Serial Access Memory	XCVR	Transceiver
SAT	Select At Test	XMTR	Transmitter
SCOPE	Oscilloscope	XOR	Exclusive OR
SD	Special Designator	XPDR	Transponder
sec	Second(s)		
SL	Sensitivity Level		
SLS	Side Lobe Suppression		
SMENU	Sequence Menu	μF	Microfarad
SNSR	Sensor	μH	Microhenry
SPI	Special Identifier Pulse	μs	Microsecond
SPR	Synchronous Phase Reversal		
SRCS	Static RAM Chip Select		
SROE	Static RAM Output Enable		
SRWE	Static RAM Write Enable		
SRQ	Service Request		
SRS	Segment Request Subfield		
SSR	Secondary Surveillance Radar		
SW	Switch		
SYNC	Synchronization		
T			
TAS	Transmission Acknowledgement Subfield		
TCAS	Traffic Alert and Collision Avoidance System		
TEMP	Temperature		
TMS	Tactical Message Sub-field		
Trig	Trigger		
TTL	Transistor- Transistor Logic		
TX	Transmit		
U			
UART	Universal Asynchronous Receiver-Transmitter		
UCS	Upper Chip Select		
UDS	U-Definition Subfield		
UF	Uplink Format		
UM	Utility Message		
UUT	Unit Under Test		
V			
V	Volts		
V _p	Volts, Peak		
V _{p-p}	Volts, Peak-to-Peak		
VAC	Volts Alternating Current		
VCO	Voltage Controlled Oscillator		
Vdc	Volts Direct Current		
VDS	V-Definition Subfield		
VERS	Version		
VRAM	Video Random Access Memory		
VRC	Vertical Resolution Advisory Complement		
Vrms	Volts Root Mean Square		
VS	Vertical Status		
VSWR	Voltage Standing Wave Ratio		
W			
W	Watts		

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