

**APPENDIX H - ABBREVIATIONS**

<b>A</b>		<b>D</b>	
AA	Announced Address	D/A	Digital to Analog
AC	Altitude Code	DAC	Digital to Analog Converter
AC/ac	Alternating Current	dB	Decibel
ACAS	Airborne Collision Avoidance System	dB <sub>i</sub>	Decibels above isotropic
ACS	Comm-A Capability Subfield	dB <sub>m</sub>	Decibels above one milliwatt
A/D	Analog to Digital	dB W/m <sup>2</sup>	Decibels above one watt per square meter
ADC	Analog to Digital Converter	DCXO	Digitally Compensated Crystal Oscillator
AIS	Comm-A Capability Subfield	DET	Detector
ALT	Altitude	DF	Downlink Format
AMPL	Amplifier	DI	Designator Identification
ANT	Antenna	Div	Division
AP	Address Parity	DMA	Direct Memory Access
AQ	Special Acquisition	DMM	Digital Multimeter
Assy	Assembly	DPCS	Dual Port RAM Chip Select
ATC	Air Traffic Control	DPOE	Dual Port RAM Output Enable
ATC	ATCRBS (on screen)	DPR	Dual Port RAM
ATCRBS	Air Traffic Control Radar Beacon System	DPSK	Differential Phase Shift Key
ATE	Automatic Test Equipment	DR	Downlink Request
ATTEN	Attenuation	DRAM	Dynamic RAM
AUTO	Automatic	DRVR	Driver
AVG	Average	DSP	Digital Signal Processor
	<b>B</b>	DTR	Data Terminal Ready
BATT	Battery		<b>E</b>
BCS	Comm-B Capability Subfield	ECS	Extended Capability Subfield
BDS	B-Definition Subfield	ELM	Extended Length Message
BIT	Built In Test	EOC	End of Cycle
BPF	Bandpass Filter	EPROM	Erasable Programmable Read-Only Memory
BR	Bridge Rectifier	ERP	Effective Radiated Power
BT	Battery	ESB	Encoded Sense Bits
	<b>C</b>	Ext	External
C	Centigrade		<b>F</b>
CA	Transponder Capability	FCC	Federal Communications Commission
CAS	Column Address Strobe	FET	Field Effect Transistor
CFS	Continuation Subfield	FIFO	First In First Out
CHAN	Channel	FPM	Feet Per Minute
CHC	Cancel Horizontal Resolution Advisory Complement	FREQ	Frequency
CLI	Coordination Lock Indicator	FS	Flight Status
CMOS	Complementary Metal-Oxide Semiconductor	Ft	Feet
COMM	Communication		<b>G</b>
COMP	Compensator	GEN	Generator
CONT	Control	GND	Ground
Cont	Continued	GPIO	General Purpose Interface Bus
COMPTR	Comparator		
CTS	Clear to Send		
CVC	Cancel Vertical Resolution Advisory Complement		
CW	Continuous Wave		

**H**

**M**

h Hexadecimal  
HET Heterodyne  
hr Hour  
HRC Horizontal Resolution Advisory Complement  
Hz Hertz

m meters  
MA Comm-A Message  
Max Maximum  
MB Comm-B Message  
MBS Multisite Comm-B Subfield  
MC Comm-C Message  
MCS Mid-Range Chip Select  
MD Comm-D Message  
MEAS Measure  
MES Multisite ELM Subfield  
MHz Megahertz (10<sup>6</sup>)  
MID Mode S Address  
MOD Modulator  
MON Monitor  
MPU Microprocessing Unit  
ms Millisecond (10<sup>-3</sup>)  
MSD Most Significant Digit  
MTB Multiple Threat Bit  
MTL Minimum Triggering Level  
MU Comm-U Message  
MUX Multiplexer  
MV Comm-V Message

**I**

ID Identification (4906 Code)  
IDS Identifier Designators Subfield  
IF Intermediate Frequency  
II Interrogator Identification  
IIS Interrogator Identification Subfield  
INTERR Interrogation  
I/O Input/Output  
ITM Intermode

**K**

KE ELM Control  
kHz Kilohertz (10<sup>3</sup>)  
km Kilometer (10<sup>3</sup>)  
kts Knots (Velocity)

**L**

LC Inductor-Capacitor  
LCA Logic Cell Array  
LCD Liquid Crystal Display  
LCK Coordination Lock Subfield  
LCS Lower Chip Select  
LED Light Emitting Diode  
LO Local Oscillation  
LOAMP Logarithmic Amplifier  
LOG Logarithmic  
LOS Lockout Subfield  
LPF Low-Pass Filter  
LSD Least Significant Digit

N/A Not Applicable  
NC C-Segment Number  
ND D-Segment Number  
nmi Nautical Miles  
NO Number  
ns Nanosecond (10<sup>-9</sup>)

**P**

PAL Programmable Array Logic  
PC Protocol  
PC Printed Circuit  
PCB Printed Circuit Board  
PCS Peripheral Chip Select  
PI Parity/Interrogator Identity  
PLCS Places  
PPG Pulse Power Gate  
PPM Pulse Position Modulation  
PR Reply Probability  
PRF Pulse Repetition Frequency  
PROM Programmable Read-Only Memory  
PSK Phase Shift Keying  
PWM Pulse Width Modulation  
PWR Power

<b>R</b>		<b>U</b>		
RAM	Random Access Memory	UART	Universal Asynchronous Receiver-Transmitter	
RAC	Resolution Advisory Complement	UCS	Upper Chip Select	
RAS	Row Address Strobe	UDS	U-Definition Subfield	
RC	Reply Control	UF	Uplink Format	
RC	Resistor-Capacitor	UM	Utility Message	
RCV	Receive	UUT	Unit Under Test	
RCVR	Receiver			
Ref	Reference	<b>V</b>		
REFLD	Reflected	V	Volts	
RF	Radio Frequency	Vp	Volts, Peak	
RGLTR	Regulator	Vp-p	Volts, Peak-to-Peak	
RI	Reply Information, Air-to-Air	VAC	Volts Alternating Current	
RL	Reply Length	VCO	Voltage Controlled Oscillator	
RMS	Root Mean Square	Vdc	Volts Direct Current	
ROM	Read Only Memory	VDS	V-Definition Subfield	
RR	Reply Request	VERS	Version	
RRS	Reply Request Subfield	VRAM	Video Random Access Memory	
RSS	Reservation Status Subfield	VRC	Vertical Resolution Advisory Complement	
RTCA	Radio Technical Commission for Aeronautics	Vrms	Volts Root Mean Square	
RX	Receive	VS	Vertical Status	
	<b>S</b>		VSWR	Voltage Standing Wave Ratio
SAM	Serial Access Memory		<b>W</b>	
SAT	Select At Test	W	Watts	
SCOPE	Oscilloscope		<b>X</b>	
SD	Special Designator		XCVR	Transceiver
sec	Second(s)		XMTR	Transmitter
SL	Sensitivity Level		XOR	Exclusive OR
SLS	Side Lobe Suppression		XPDR	Transponder
SMENU	Sequence Menu			
SNSR	Sensor			
SPI	Special Identifier Pulse			
SPR	Synchronous Phase Reversal	$\mu$ F		Microfarad
SRCS	Static RAM Chip Select	$\mu$ H		Microhenry
SROE	Static RAM Output Enable	$\mu$ s		Microsecond
SRWE	Static RAM Write Enable			
SRQ	Service Request			
SRS	Segment Request Subfield			
SSR	Secondary Surveillance Radar			
SW	Switch			
SYNC	Synchronization			
	<b>T</b>			
TAS	Transmission Acknowledgement Subfield			
TCAS	Traffic Alert and Collision Avoidance System			
TEMP	Temperature			
TMS	Tactical Message Subfield			
Trig	Trigger			
TTL	Transistor- Transistor Logic			
TX	Transmit			



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