

PART NUMBER
SPEC-PIM2

PIM2 Specifications

Input Characteristics:

- All inputs non-isolated and TTL compatible
- Absolute maximum input voltage = +5.5/-0.3V
- High level input current = 20 μ A
- Low level input current = -400 μ A.

Maximum Count:

1. No concatenation (16 bits/counter): 65536 events.
2. With concatenation (32 bits/counter): over 4 billion events (4,294,967,296) per set of concatenated counters.

Resolution: 16/32 bits (normal/concatenated).

Maximum Counting Speed:

1. Real-time totalizing (read-only mode, no reset): 250,000 pulses/second, or 250kHz.
2. Sampled input count (read and reset counter mode): 1,000,000 pulses/second, or 1MHz.

Gated Input Non-coherence: ± 1 count.

Minimum Input Pulse Width: 50nsec.

Power Requirements: +5V 475mA.

Key Features:

- * Four channel, fully synchronous event counter with channel to channel independent totalizing capability.
- * All counters independently readable and resettable.
- * Four separate gates allow the inputs to be configured as four dual input channels to allow the input events to be gated by external sources.
- * Concatenation of counters is available by means of user-selectable switches to obtain two fully-independent, fully synchronous 32 bit counters.

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| LTR | REVISIONS | APP. | DATE | DRN. | DATE |
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| A | REL. 12/33 | | 5-7-87 | CKD. | DATE |
| | | | | APP. <i>EA</i> | DATE 5/8/87 |
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SPECIFICATIONS

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