SOFT500 SOFTWARE

Host computer software: PC-DOS 1.1 or 2.0, BASICA (IBM).

Apple DOS 3.2 or 3.3, APPLESOFT

Basic (Ap)

System architecture: Real-time, foreground/backround,

interrupt driven

Command families: Analog Input, Analog Output,

Digital Input, Digital Output, Analog and Digital Triggering

Array Control and Memory Management,

Interrupt and Task Control, Statistics and Data Analysis,

Screen Graphics (real-time, non-real

time), Timing and Clock Control.

Cammands: 43 (IBM), 38 (Apple)

Concurrent backround tasks: 64 max (IBM), 20 max (Apple)

Maximum analog sampling rate:

foreground,

31 kHz (IBM), 27 kHz (Apple)

backround,

l kHz (IBM), 500 Hz (Apple)

Timer channels: 3, 16 bit-cascadable

Timer resolution: 1 us

Clock/calendar units: seconds, minutes, hours, date, year

Power-fail battery backup: 90 days

Note: All specifications are typical, at 23°C unless otherwise noted.

LTR	REVISIONS	APP.	DATE	DRN.	DATE		
A	RELEASED 9481		11-1-83	CKD.	DATE	KEITHLE	Keithley Instruments Inc. Cleveland, Ohio 44139
				APP.	DATE		Cievelatiu, Otilo 44 133
-		<u> </u>					PART NUMBER
				SP	ECIFICA"	IONS	SPEC-
							150FT500