KPCI-PIO24

PCI-Bus 24-Channel Parallel Digital I/O Board

Specifications

Symbol	Parameter	Min.	Typ.	Max.	Unit
Logic Inputs and Outputs					
V_{IH}	Voltage, input high	2.0	–	–	V
V_{IL}	Voltage, input low	–	–	0.8	V
I_{IH}	Current, input high, V_{IN} = 2.7V	–	–	±1	µA
I_{IL}	Current, input low, V_{IN} = 0.5V	–	–	±1	µA
I_{OZH}	High impedance output current, V_{OUT} high	–	–	±1	µA
I_{OZL}	High impedance output current, V_{OUT} low	–	–	±1	µA
V_{OH}	Voltage, output high, I_{OH} = –8mA	2.4	3.3	–	V
V_{OH}	Voltage, output high, I_{OH} = –15mA	2.0	3.0	–	V
V_{OL}	Voltage, output low, I_{OL} = 64mA	–	0.3	0.55	V
I_{OL}	Current, output low	–	–	64.0	mA
I_{OH}	Current, output high	–	–	–15	mA
I_{OS}	Short circuit current	–60.0	–120.0	–225.0	mA
I_{OFF}	Input/output power off leakage	–	–	±1	µA
Interrupt Inputs					
I_{IL}	Current, input low	–	–	–100	µA
I_{IH}	Current, input high	–	–	–10	µA
V_{OL}	Voltage, output low, I_{OL} = max	–	0.3	0.5	V
V_{OH}	Voltage, output high, I_{OH} = max	2.4	3.3	–	V
I_{OL}	Current, output low	–	–	16.0	mA
I_{OH}	Current, output high	–	–	–3.2	mA
Power Requirements					
	+5V		400		mA
Environmental					
	Operating temperature range	0		50	°C
	Storage temperature range	–20		+85	°C
	Humidity (non condensing)	0		90	%
Dimension					
	5 × 4.25 × 0.75 (half slot)				inches
	12.7 × 10.8 × 1.9				cm
Weight	4				oz
	116				grams

Board Mapping

Base + Offset 0 × 0	GROUP 0 data	Read/write
Base + Offset 0 × 4	Not Used	
Base + Offset 0 × 8	Not Used	
Base + Offset 0 × C	Not Used	
Base + Offset 0 × 10	CONTROL GROUP 0	Write only
Base + Offset 0 × 14	Not Used	
Base + Offset 0 × 18	Not Used	
Base + Offset 0 × 1C	Not Used	