

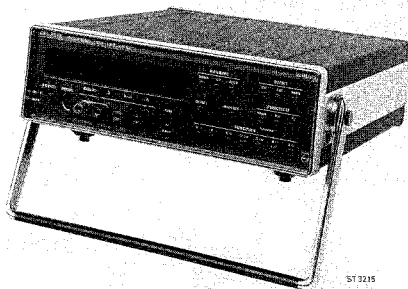
# Automatic RMS Multimeter PM2528

Repair by  
Signature Analysis

Instruction Manual

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830304

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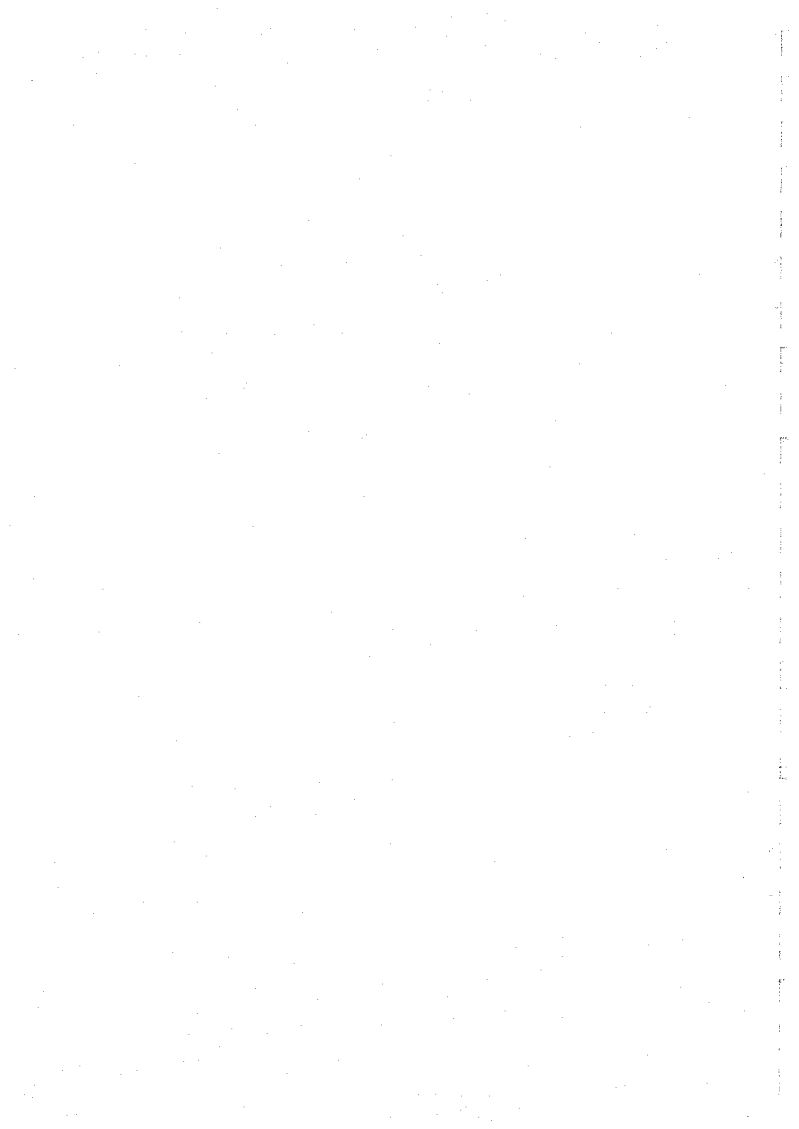
**PHILIPS**



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## REPAIR BY SIGNATURE ANALYSIS

### 1. GENERAL

For repair by signature analysis a testrom has been developed.

This testrom enables the user to test the digital part of the PM2528 by means of signature analysis.

It also enables trouble-shooting at component level in the circuit under test, by comparing the actual signature to the correct one. So the trouble-shooter can trace back the faulty node.

In total there are 13 tests. In the testrom 12 tests have been implemented and one test is made with the help of the free run facility of the PM2528.

### 2. WHY USE A SIGNATURE ANALYSER?

Thanks to the microcomputer in the digital part of the PM2528 the instrument can be very small.

Due to this the bus structure is very complex. Data flow on the bus becomes complex and cannot be analysed with an oscilloscope and voltmeter any longer. Therefore the need to test and service the digital part is acute.

The trouble-shooting involves the detection of circuit failure and the localization of faults at the source.

Finding the fault source in a bus system, such as the bus in the PM2528, with complex feedbacks can be difficult. But opening the feedback paths and using the signature analyser makes fault-finding easy.

### 3. CHECK WITH A SIGNATURE ANALYSER

The signature analyser checks the digital part of the PM2528 by detecting the bit stream at various circuits nodes, and displaying them as hexadecimal signatures.

To do this the PM2528 supplies the signature analyser with three gating signals, or in some cases four:

START, STOP, CLOCK and QUALIFIER.

START signals the beginning of a measurement and prepares the signature analyser to receive information from the data probe. STOP closes does this measurement. In cases of measuring signatures with RC delays, a QUALIFIER has to be used to stop the incoming information for a short period of time. The CLOCK is the system clock of the PM2528 under test to accept the data synchronously.

*NOTE: The active edge of each of these gating signals must be selectable.*

### 4. TYPE OF SIGNATURE ANALYSER TO BE USED?

The signature analyser to be used in combination with the PM2528 should have the following capabilities:

- Selectable edges of; START, STOP, CLOCK and QUALIFIER.
- Signatures in the following sequence:

DIGIT	DISPLAY	DIGIT	DISPLAY
0000	0	1000	8
0001	1	1001	9
0010	2	1010	A
0011	3	1011	C
0100	4	1100	F
0101	5	1101	H
0110	6	1110	P
0111	7	1111	U

## 5. BRIEF DESCRIPTION OF THE TESTS

### FREE RUN TEST

With this test it is possible to measure signatures on the address latches and the data bus of the microcomputer. This is necessary to check if the data can be taken from the tastroom.

#### TEST 0 (Ram and I/O test of the $\mu$ p)

This test checks the internal RAM of the microcomputer. When the RAM is correct, it will also test the I/O parts of the microcomputer. Only then can signatures be measured on the I/O parts databus and ROM.

#### TEST 1 (Visual display/keyboard leds test)

This test fills the display leds and keyboard leds with a certain pattern. In this way a quick display/keyboard test can be made.

#### TEST 2 (Display/keyboard leds test)

This test sends signatures to the display and keyboard. The decoding, the 7-segment leds and the key-leds are tested.

#### TEST 3 (Key test)

This test sends signatures to the keys and reads them back.

#### TEST 4 (Analog control test)

This test activates the I/O expanders and the relays in the analog section.

#### TEST 5 (Synchronous mains/adc start circuit test, no S.A.)

This test checks the following signals:

- Synchronous mains/adc start circuit
- Watchdog
- ADC test switch

The  $\mu$ p reads these signals and if correct an indication will be displayed.

#### TEST 6 (Interrupt controller test no S.A.)

This test checks the following signals:

- Watchdog interrupt
- Interrupt from keyboard
- External start interrupt

The  $\mu$ p reads these signals and if correct an indication will be displayed.

#### TEST 7 (Galvanic separation test)

This test sends signatures to the galvanic separation. Before the interfaces are tested this unit should be tested first.

#### TEST 8 (BCD output PM9292, start $\overline{\text{DVM}}$ test, no S.A.)

This test checks the start  $\overline{\text{DVM}}$  (digital voltmeter) signal. The  $\mu$ p will read this signal and if correct an indication will be displayed.

**TEST 9** (BCD output PM9292 test)

This test checks the complete BCD output.

**TEST 10** (Analog output PM9254 test)

This test checks the complete analog output.

**TEST 11** (IEC-BUS PM9292 test)

This test checks the complete IEC-BUS interface.

**6. HOW TO USE THE DOCUMENTATION AND THE TESTROM**

This service manual should be used in combination with the service manual of the PM2528. The user of the fault-finding procedure in the PM2528 service manual will be directed to a certain part of the PM2528. If the fault-finding procedure does not solve the problem, then select a signature analysis test which is associated with the part of the PM2528.

Begin always with the microcomputer, or with the inputs of the integrated circuits, and try to find the faulty node. Bear in mind that when the faulty node has been found, more than one circuit can cause the faulty signature.

**7. FAULT-FINDING HINTS**

The first thing to do is to get an analysis of what is wrong. If this is not possible get the PM2528 in the free run mode by breaking the bus system between the microcomputer and the ROMs. This is done by removing the ROMs, and using the FREE-RUN simulator.

The microcomputer is now forcing—in some instruction that will cause a repeated increment of the address lines. So at this point the microcomputer is scanning the whole address field.

With START and STOP lines from the signature analyser, both connected to the most significant address bit and triggering on different edges, a measurement will be defined. Connecting the CLOCK line of the signature analyser to the ALE signal of the microcomputer and selecting the edge at which the address is valid, the input probe will get a signature at every address line.

By touching the probe in every test to a logic 1 level, a characteristic signature is obtained. This is the result of shifting ones (logic 1) in for every cycle that occurs while the gate is open. So the characteristic signature can be used to diagnose this condition elsewhere. The logic 0 is always a signature of "0000". Assume there are no bad signatures in the free-run test, then the TESTROM can be placed in the D1810 socket and it is possible to start measuring with test 0.

If an incorrect part can be located with the fault-finding procedure it is recommended to start with the test which is responsible for that fixed part of the PM2528. To do this a special switch has to be used. (Refer to chapter 11.1. at this service manual).

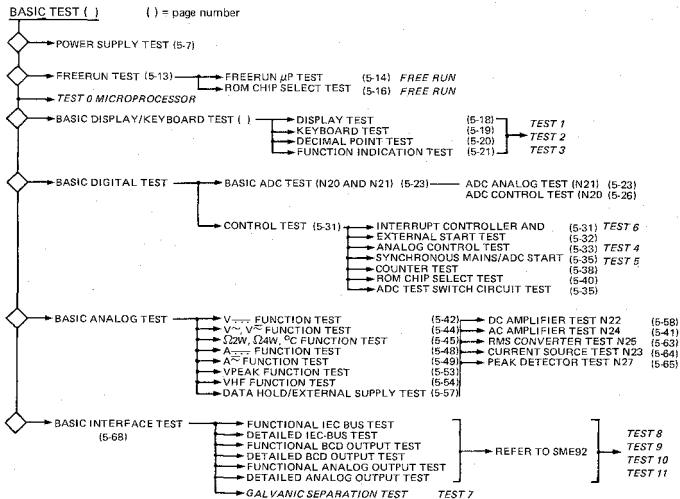
**8. HOW TO ORDER THE SIGNATURE ANALYSIS TESTROM AND THE DOCUMENTATION**

Under ordering number 5322 694 54015 the testrom and the documentation can be ordered from Concern Service.

**9. ADDITIONAL EQUIPMENT TO BE USED**

1. Free run simulator (refer to chapter 5.4. of the PM2528 service manual)  
(Free run)
2. Test switch (refer to chapter 11.1. of this manual)  
(test 0 → 11)
3. Test ROM (ordering number 5322 694 54015)  
(test 0 → 11)
4. Two extender printed circuit boards (ordering number 4822 263 70117)

## 10. FAULT-FINDING FLOW CHART (refer to page 5.4. of the PM2528 service manual)



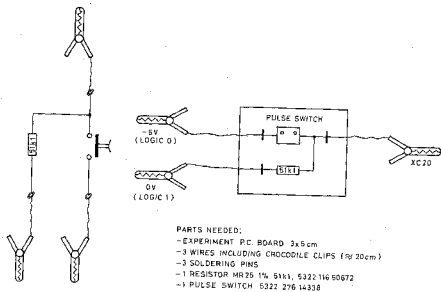
NOTE: THE TEST WRITTEN IN ITALICS ARE ADDITIONAL WHICH CAN BE CALLED WHEN USING THE SPECIAL TEST ROM



## 11. TESTING THE PM2528

### 11.1. Test switch

To switch from one test to another signal XC20 of the microcomputer has to be connected to logic zero (-5V) every time. However the XC20 input of the  $\mu$ p has no pull-up resistor. To ensure correct switching a pull-up simulator with e.g. pulse switch should be used. This may be done in the following way:






Before switching on the tests, connect the test switch to the PM2528.

- XC20 to test pin XC20
- 0V to test pin 0V (logic 1)
- 5V to test pin -5V (logic 0)

Switch on the PM2528 and select the desired test by pushing the pulse switch. Initial the PM2528 will jump to test 0.

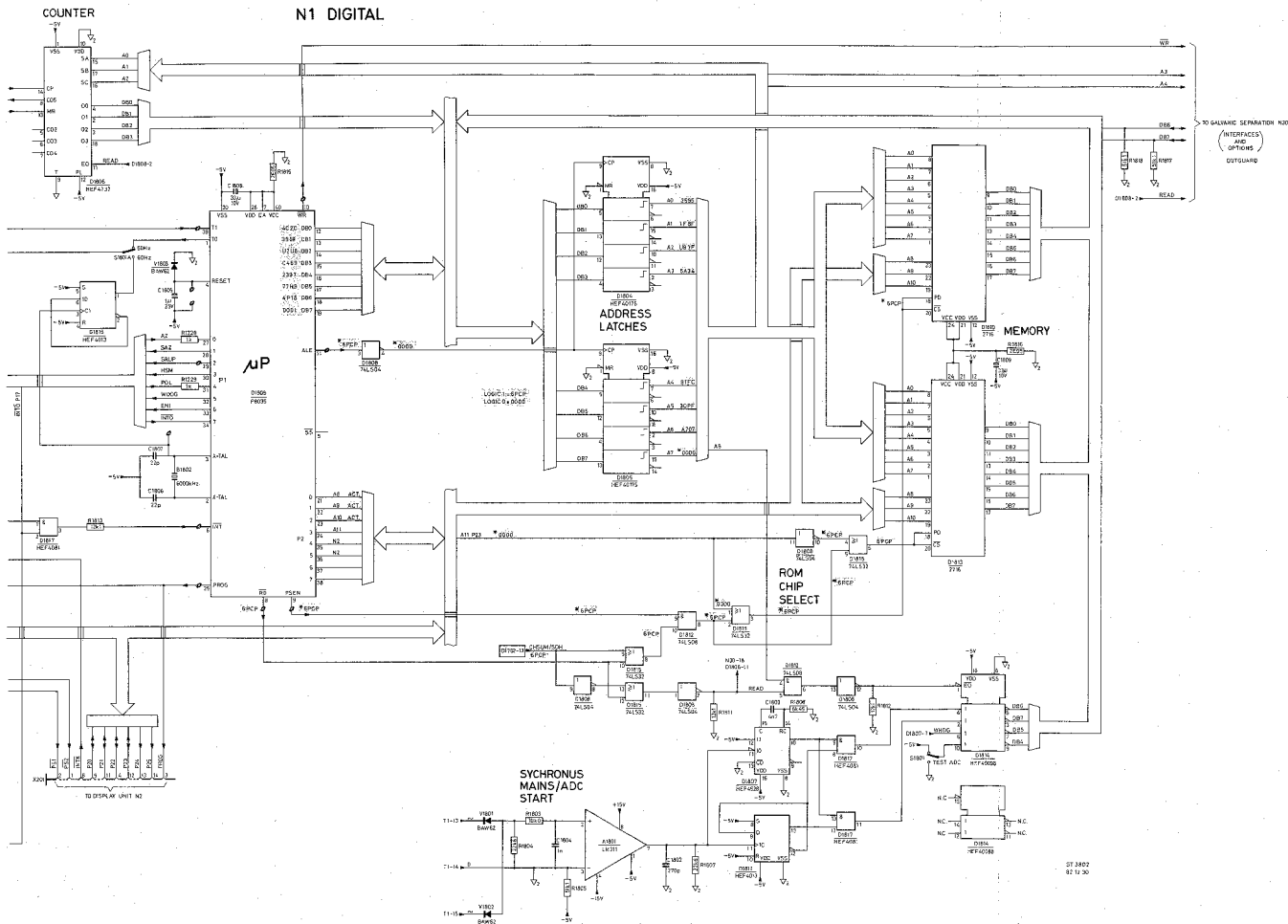
## 11.2. Free run

## Settings signature analyser

Setting	Edge	Test pin	Signal name
START		D1810/1	ROM / A7
STOP		D1810/1	ROM / A7
CLOCK		TP ALE	$\mu$ p/ALE
QUAL	—	—	—
GROUND	—	TP -5V	Logic 0

## Preparations

- PM2528
  - Free run simulator placed in D1810 socket (Both ROMs removed). Refer to 5.4. of the PM2528 service manual.
  - Analog control p.c.b. N20 removed.
  - Galvanic separation p.c.b. and interfaces (ANALOG, BCD, IEC) removed.
- Connect the signature analyser as given in the settings table.
- Switch on the PM2528.



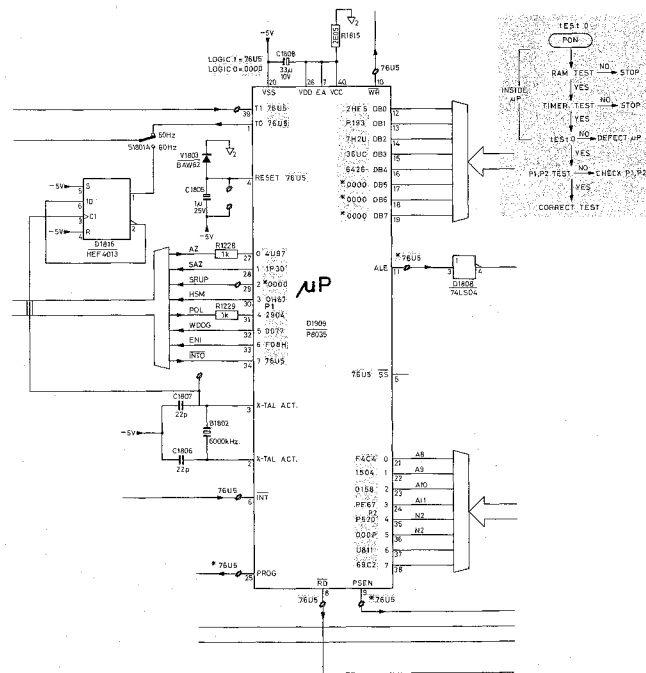
## 11.3. TEST 0 Micro processor

## Settings signature analyser

Setting	Edge	Test pin	Signal name
START	┌	TP RUP	$\mu$ p/P12 (SRUP)
STOP	└	TP RUP	$\mu$ p/P12 (SRUP)
CLOCK	┌	TP ALE	$\mu$ p/ALE
QUAL	—	—	—
GROUND	—	TP -5V	Logic 0

## Preparations

- PM2528 — Testrom placed in D1810 socket (both ROMs removed).  
— Analog control p.c.b. N20 removed.  
— Galvanic separation p.c.b. and interfaces (ANALOG, BCD, IEC) removed.
- Connect the test switch to the PM2528 (refer to 11.1.).
- Connect the signature analyser as given in the settings table.
- Switch on the PM2528 TEST 0 will appear on the display if a part of the  $\mu$ p is correct. Refer to the flow-chart in the circuit diagram.

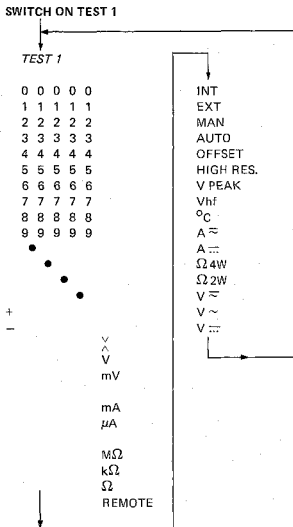


## 11.4. TEST 1 Visual display/keyboard (No S.A.)

## Preparations

- PM2528
  - Testrom placed in D1810 socket (Both ROMs removed).
  - Analog control p.c.b. removed.
  - Galvanic separation p.c.b. and interface (ANALOG, BCD, IEC) removed.
- Connect the test switch to the PM2528 (refer to 11.1).
- Switch on the PM2528 and select TEST 1.

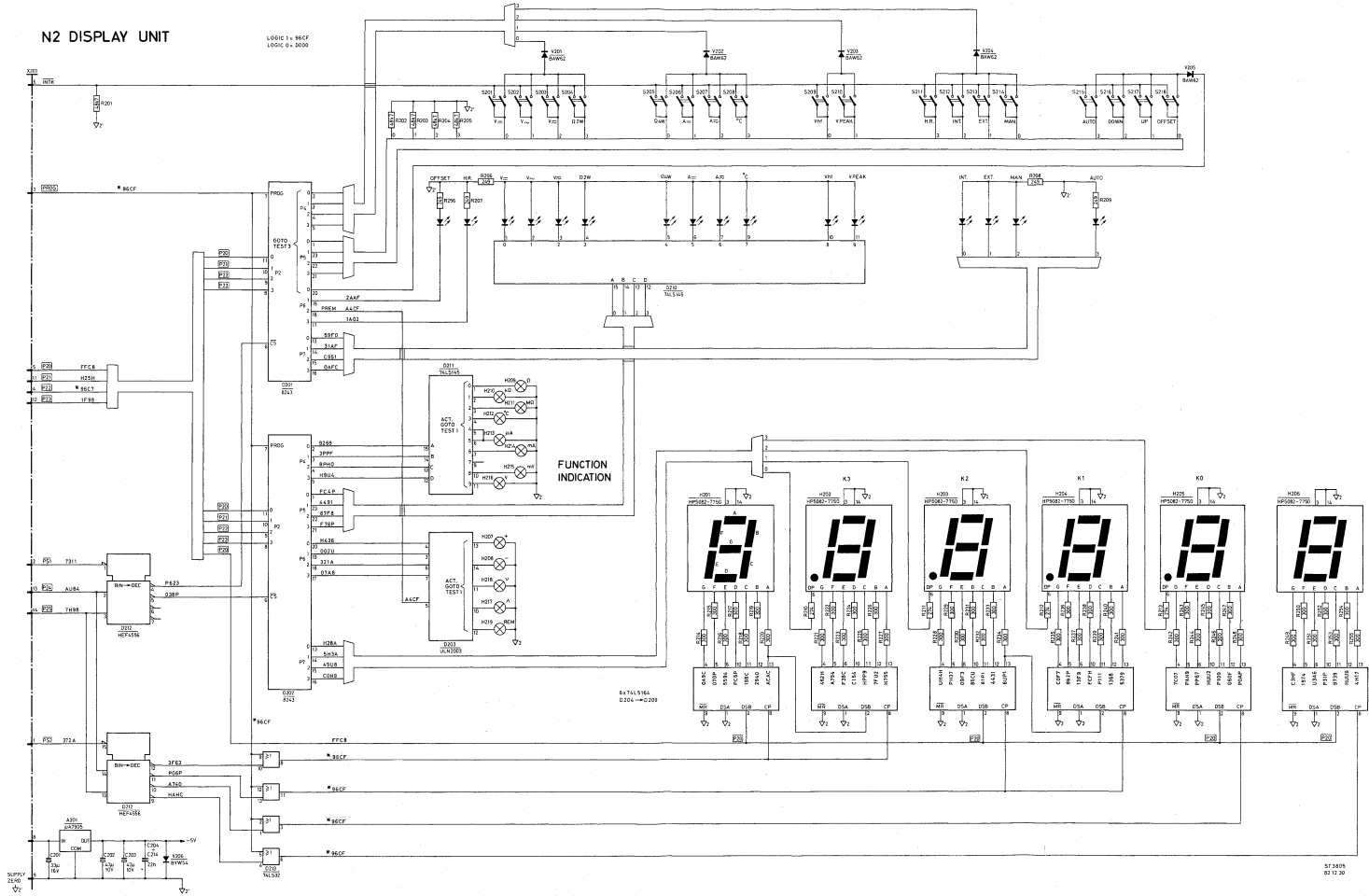
## INDICATION








# N2 DISPLAY UNIT

LOGIC 1, 86CF  
LOGIC 4, 5000



## 11.6. TEST 3 Keys

Settings signature analyser

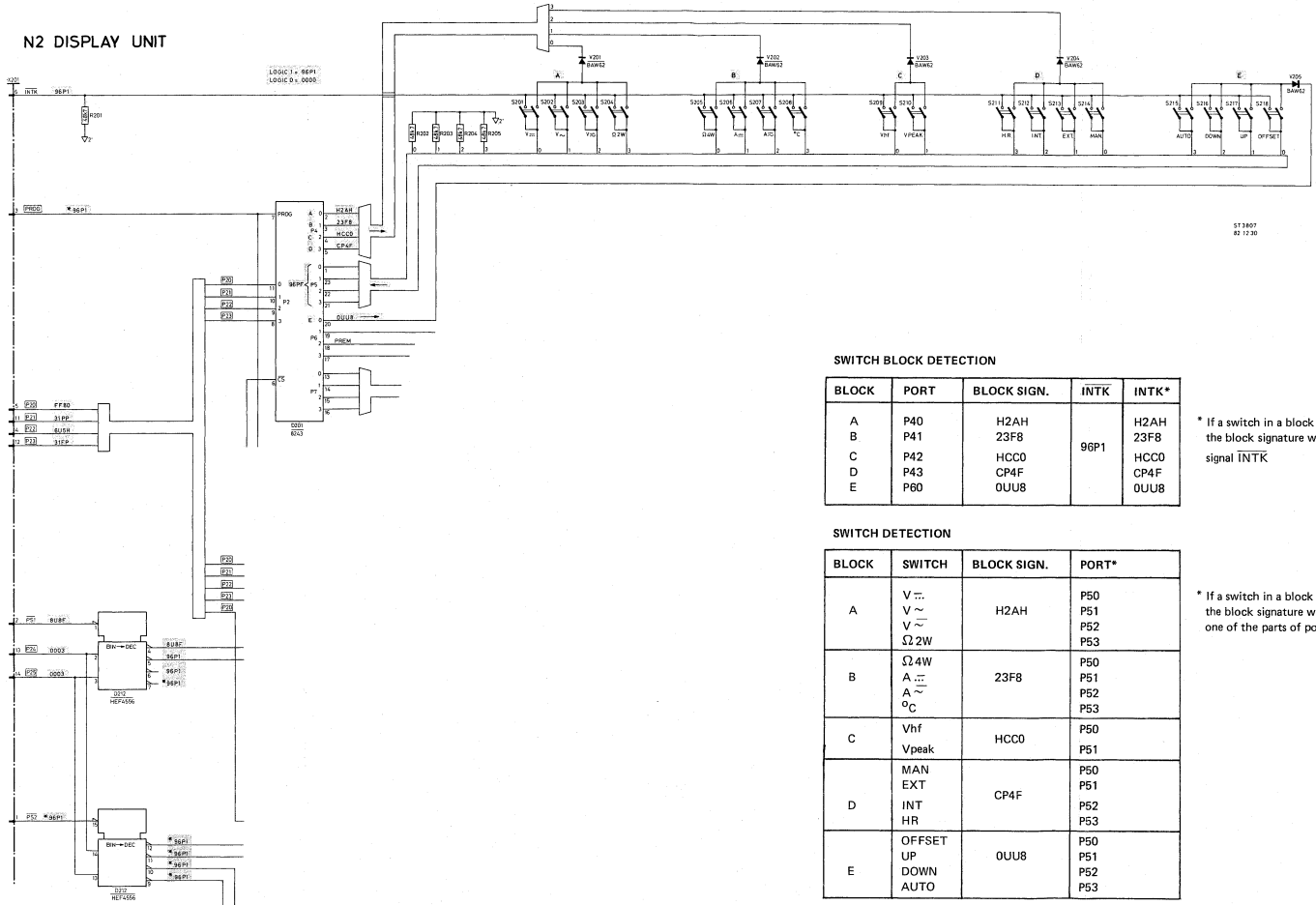
Settings	Edge	Test pin	Signal name
START		TP RUP	$\mu$ P/P12 (SRUP)
STOP		TP RUP	$\mu$ P/P12 (SRUP)
CLOCK		TP ALE	$\mu$ P/ALE
QUAL	—	—	—
GROUND	—	TP -5V	Logic 0

Preparations

- PM2528
  - Testrom placed in D1810 socket (both ROMs removed).
  - Analog control p.c.b. N20 removed.
  - Galvanic separation p.c.b. and interfaces (ANALOG, BCD, IEC) removed.
- Connect the test switch to the PM2528 (refer to 11.1.)
- Connect the signature analyser as given in the settings table.
- Switch on the PM2528 and select TEST 3.



N2 DISPLAY UNIT



SWITCH BLOCK DETECTION

BLOCK	PORT	BLOCK SIGN.	INTK	INTK*
A	P40	H2AH	96P1	H2AH
B	P41	23F8		23F8
C	P42	HCC0		HCC0
D	P43	CP4F		CP4F
E	P60	OUU8		OUU8

\* If a switch in a block is depressed the block signature will appear on signal INTK

SWITCH DETECTION

BLOCK	SWITCH	BLOCK SIGN.	PORT*
A	V ..	H2AH	P50
	V ~		P51
	V ~		P52
	Ω 2W		P53
B	Ω 4W	23F8	P50
	A ..		P51
	A ~		P52
C	Vhf	HCC0	P50
	Vpeak		P51
D	MAN	CP4F	P50
	EXT		P51
	INT		P52
E	HR	OUU8	P53
	OFFSET		P50
	UP		P51
	DOWN		P52
	AUTO		P53

\* If a switch in a block is depressed the block signature will appear on one of the parts of port 5.



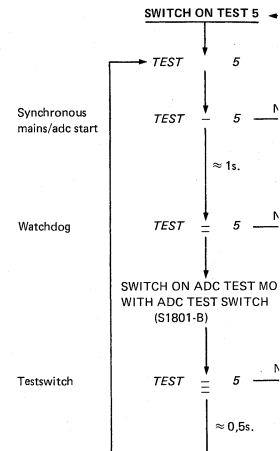




## 11.8. TEST 5 Synchronous mains/ADC start circuit

## Preparations

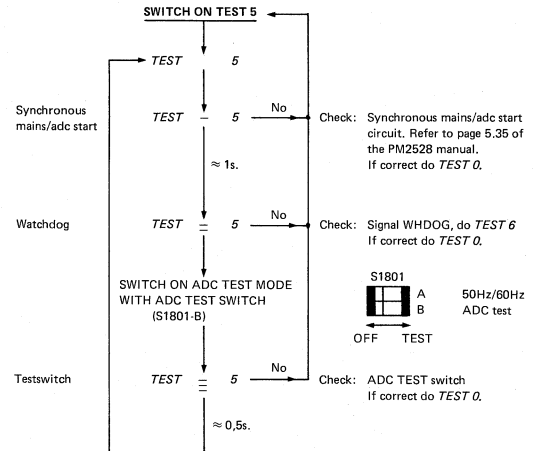
- PM2528
  - Testrom placed in socket
  - Analog control p.c.b.
  - Galvanic separation present
- Connect the test switch to the PM2528 (refer to test manual)
- Switch on the PM2528 and select TEST 5.
- Use a data probe or oscilloscope to measure the test signals.  
Logic 1 = 0V, logic 0 = -5V.

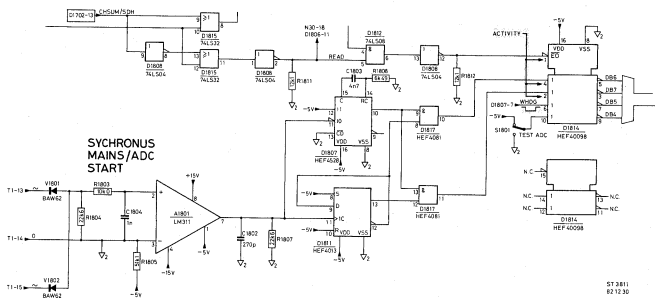


## 11.8. TEST 5 Synchronous mains/ADC start circuit (No S.A.)

## Preparations

- PM2528
  - Testrom placed in socket D1810 (both ROMs removed).
  - Analog control p.c.b. N20 removed.
  - Galvanic separation p.c.b. and interfaces (BCD, ANALOG, IEC) removed.
- Connect the test switch to the PM2528 (refer to 11.1.).
- Switch on the PM2528 and select TEST 5.
- Use a data probe or oscilloscope to measure the state of the signals.  
Logic 1 = 0V, logic 0 = -5V.



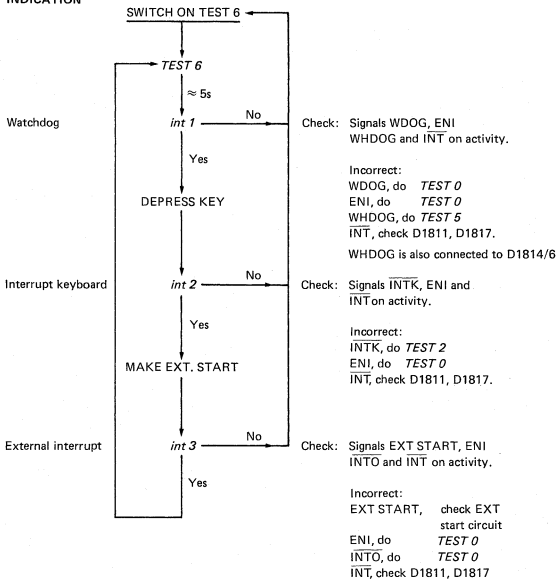


## 11.9. TEST 6 Interrupt controller (No S.A.)

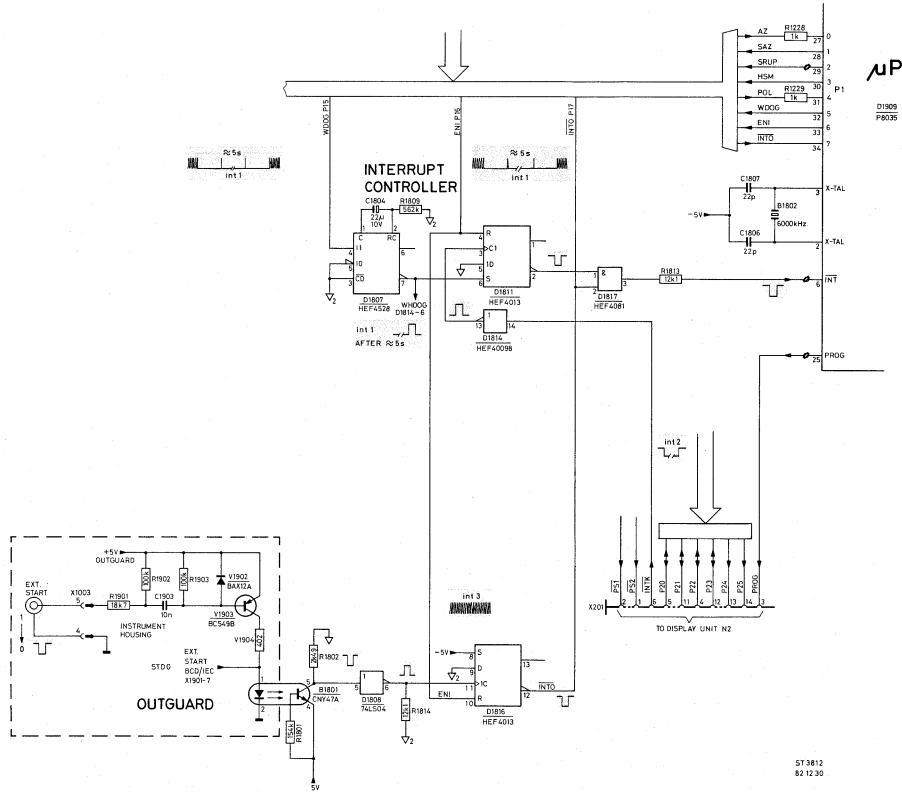
## Preparations

- PM2528
  - Testrom placed in D1810 socket (both ROMs removed).
  - Analog control p.c.b. N20 removed.
  - Galvanic separation p.c.b. and interfaces (BCD, ANALOG, IEC) removed.
- Connect the test switch to the PM2528 (refer to 11.1.).
- Switch on the PM2528 and select TEST 6.
- Use a data probe or oscilloscope to measure the state of the signals.  
Logic 1 = 0V, logic 0 = -5V.

## INDICATION










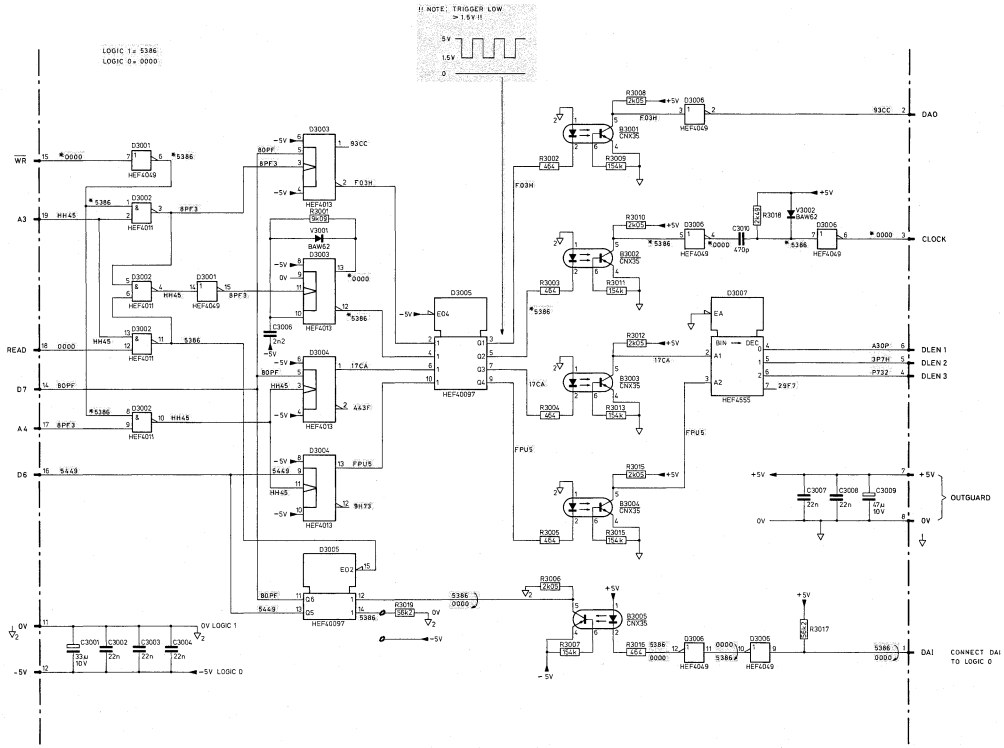
## 11.10. TEST 7 Galvanic separation

## Setting signature analyser

Setting	Edge	Test pin	Signal name
START		TP RUP	$\mu\text{P}/\text{P}12$ (SRUP)
STOP		TP RUP	$\mu\text{P}/\text{P}12$ (SRUP)
CLOCK		TP WR	$\mu\text{P}/\text{WR}$
QUAL	—	—	—
GROUND	—	TP -5V	Logic 0

## Preparations

- PM2528
  - Testrom placed in D1810 socket (both ROMs removed).
  - Analog control p.c.b. N20 removed and replaced by extender p.c.b.
  - Galvanic separation p.c.b. N30 placed on extender p.c.b.
  - Interconnect N30/12 to N30 (Galvanic separation).
- Connect the signature analyser to the PM2528 as given in the settings table.
- Connect the test switch to the PM2528 (refer to 11.1.).
- Switch on the PM2528 and select TEST 7.



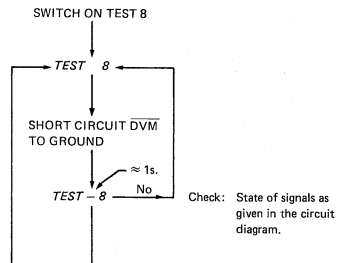
## 11.11. TEST 8 BCD output PM9292, Start DVM (No S.A.)

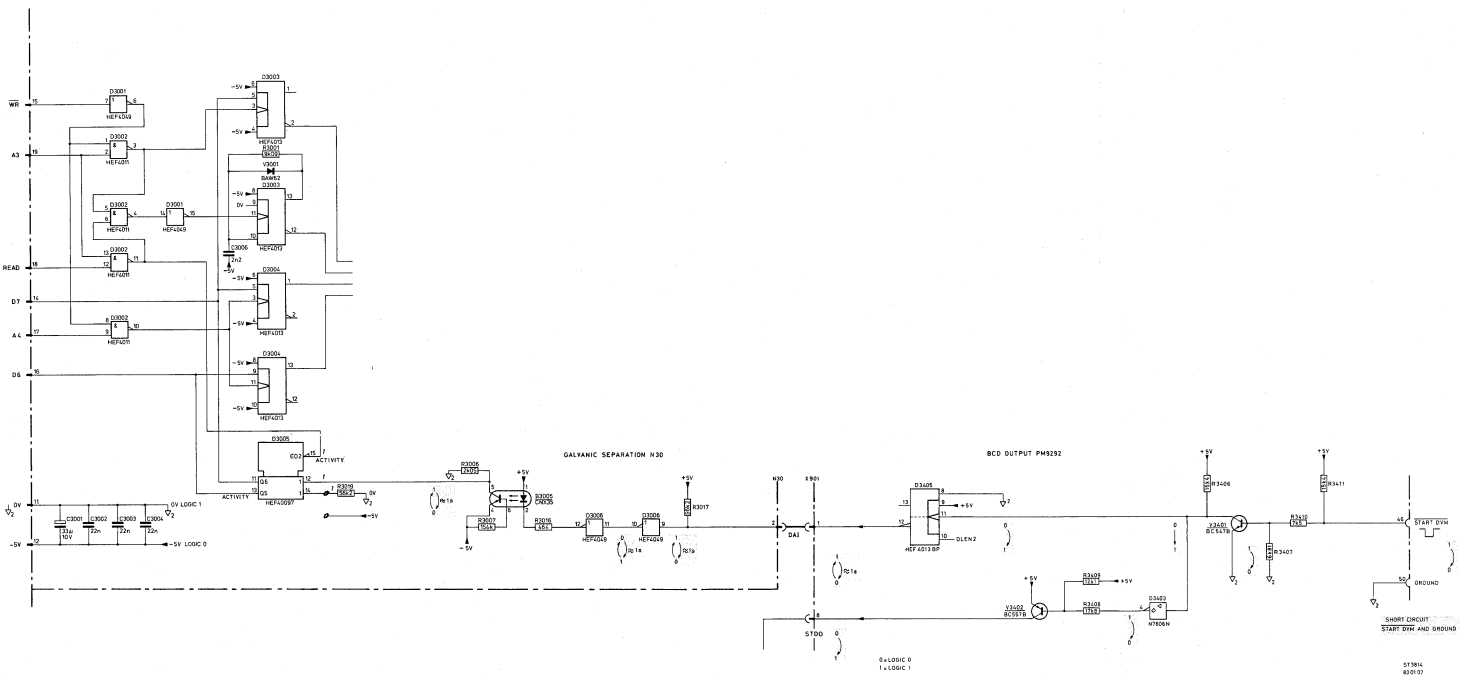
## Preparations

- PM2528
  - Test rom placed in D1810 socket (both ROMs removed).
  - Analog control p.c.b. N20 removed.
  - Galvanic separation p.c.b. N30 placed on extender p.c.b.
  - Interconnect N30/12 to N30/8 (Galvanic separation).
- Connect the test switch to the PM2528 (refer to 11.1.).
- Switch on the PM2528 and select test 8.
- Use a data probe or oscilloscope to measure the state of the signals.  
Logic 0 = -5V, logic 1 = 0V.

## Indication




- If test 8 is switched on TEST 8 will appear on the display.
- Short circuit signal START DVM and ground, on the display a dash will appear between TEST and 8 for about 1 second (TEST - 8). This means that the  $\mu$ p has read the start dvm signal.  
If in-correct measure the state of the signals as given in the circuit diagram.





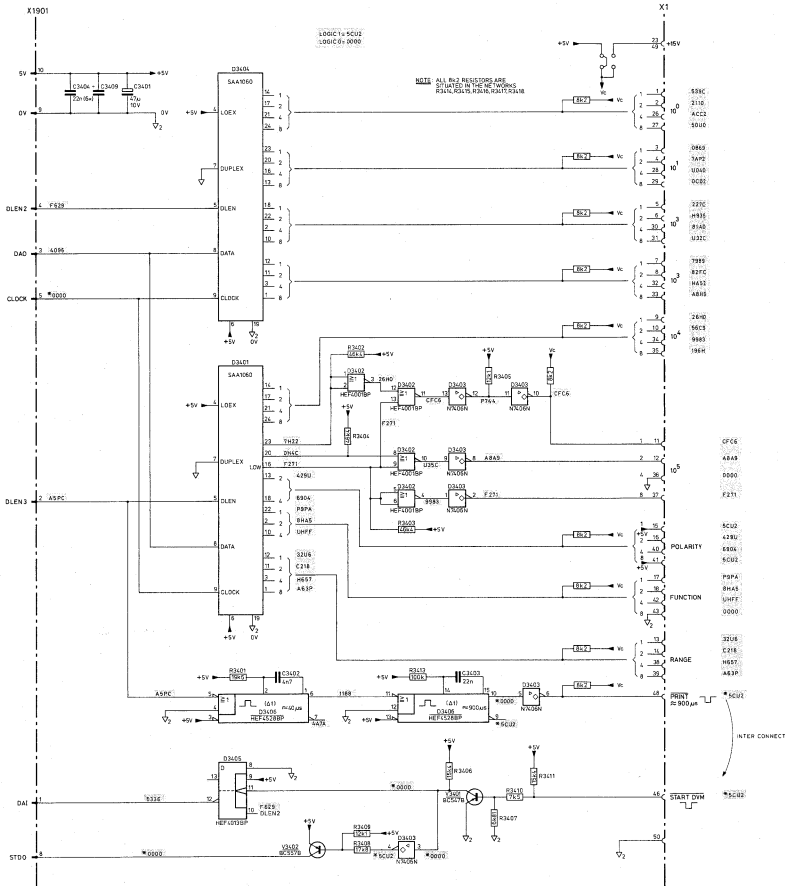
## 11.12. TEST 9 BCD output PM9292

## Setting signature analyser

Setting	Edge	Test pin	Signal name
START		TP RUP	$\mu$ p/P12 (SRUP)
STOP		TP RUP	$\mu$ p/P12 (SRUP)
CLOCK		TP WR	$\mu$ p/WR
QUAL.	—	—	—
GROUND	—	TP -5V	Logic 0

## Preparations




- PM2528
  - Testrom placed in D1810 socket (both ROMs removed).
  - Analog control p.c.b. N20 removed.
  - Galvanic separation p.c.b. N30 placed on extender p.c.b.
  - Interconnect N30/12 to N30/8 (Galvanic separation).
- Connect the signature analyser to the PM2528 as given in the settings table.
- Connect the test switch to the PM2528 (refer to 11.1.).
- Switch on the PM2528 and select TEST 9.



## 11.13. TEST 10 Analog output PM9254

TEST10-A Input part.

Settings signature analyser




Setting	Edge	Test pin	Signal name
START		TP RUP	$\mu$ P/P12 SRUP
STOP		TP RUP	$\mu$ P/P12 SRUP
CLOCK		TP WR	$\mu$ P/WR
QUAL	—	—	—
GROUND	—	TP -5V	Logic 0

Preparations

- PM2528
  - Testrom placed in D1810 socket (both ROMs removed).
  - Analog control p.c.b. N20 removed and replaced by extender p.c.b.
  - Galvanic separation p.c.b. N30 placed on extender p.c.b.
  - Interconnect N30/12 to N30/8 (Galvanic separation).
- PM9254
  - Set digit switch in position C (refer to rear of PM2528).
- Connect the signature analyser to the PM2528 as given in the settings table.
- Connect the test switch to the PM2528 (refer to 11.1.).
- Switch on the PM2528 and select TEST 10.

TEST 10-B Output part (Self running, with own clock).

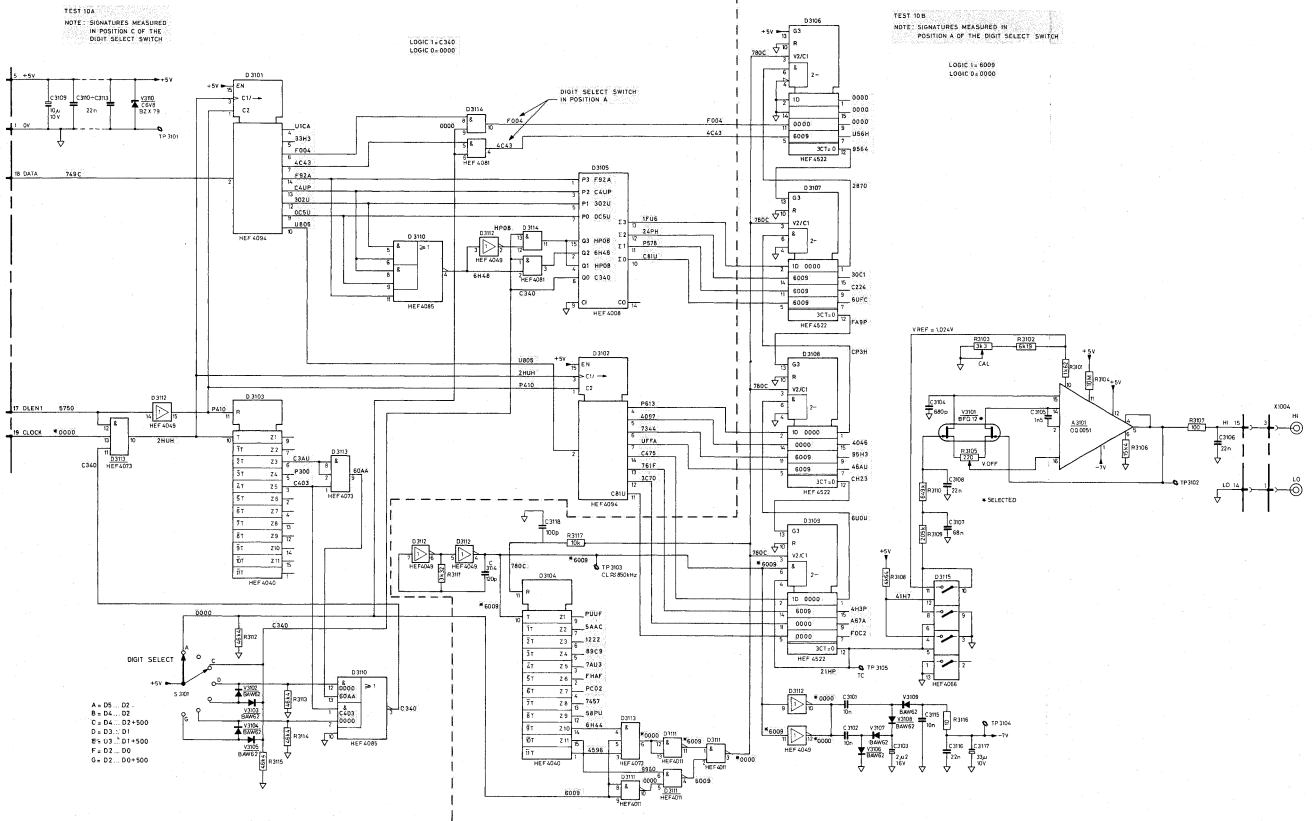
Settings signature analyser

Setting	Edge	Test pin	Signal name
START		D3111/3	Load
STOP		D3111/3	Load
CLOCK		TP3103	Clock
QUAL	—	—	—
GROUND	—	TP -5V	Logic 0

Preparations

- PM2528
  - Refer to test 10A.
- PM9254
  - Set the digit select switch in position A (refer to rear of the PM2528).
- Connect the signature analyser to the PM2528 as given in the settings table.
- Connect the test switch to the PM2528 (refer to 11.1.).
- Switch on the PM2528 and select TEST 10.





TEST 10A  
NOTE: SIGNATURES MEASURED IN POSITION C OF THE DIGIT SELECT SWITCH

LOGIC 1: C340  
LOGIC 0: 0000

TEST 10B  
NOTE: SIGNATURES MEASURED IN POSITION A OF THE DIGIT SELECT SWITCH

LOGIC 1: 6009  
LOGIC 0: 0000

- A = DS -02
- B = DL -02
- C = EN -02/500
- D = D3 -01
- E = I3 -01/500
- F = D2 -00
- G = D2 -00/500

SWITCH DECODER




D310	A	B	C	D	E	F	G
12	0000	0000	0000	C340	C340	0000	0000
13	000A	000A	000A	000A	000A	0000	0000
1	C403	C403	C403	C403	C403	C403	C403
2	0000	0000	0000	0000	C340	C340	C340
3	C340	C340	C340	H37A	H37A	0743	0743

573816  
801007



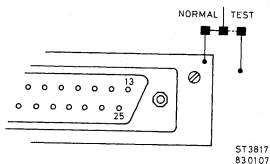
### 11.14. TEST 11 IEC-BUS PM9291 (without address decoder)

Settings signature analyser

Setting	Edge	Test pin	Signal name
START		TP RUP	$\mu$ p/P12 (SRUP)
STOP		TP RUP	$\mu$ p/P12 (SRUP)
CLOCK		N20/12	$\mu$ p/P10 AZ
QUAL.	—	—	—
GROUND	—	TP -5V	Logic 0

Preparations

- PM2528
  - Testrom placed in D1810 socket (both ROMs removed).
  - Analog control p.c.b. N20 removed and replaced by extender p.c.b.
  - Galvanic separation p.c.b. N30 placed on extender p.c.b.
  - Inter-connect N30/12 to N30/8 (Galvanic separation).
- PM9291
  - Jumper on the interface in position test






- Interconnect on the IEC-BUS connector  
signal DAV with EO1,  
signal REN with ground

- Connect the signature analyser to the PM2528 as given in the settings table.
- Connect the test switch to the PM2528 (refer to 11.1.).
- Switch on the PM2528 and select *TEST 11*.

TEST ADDRESS DECODER (Self-running with own clock).

Settings signature analyser

Setting	Edge	Test pin	Signal name
START		D3210/9	red
STOP		D3210/9	red
CLOCK		D3216/15	Cl
QUAL.	—	—	—
GROUND	—	TP -5V	Logic 0

## Preparations

Refer to test 11.

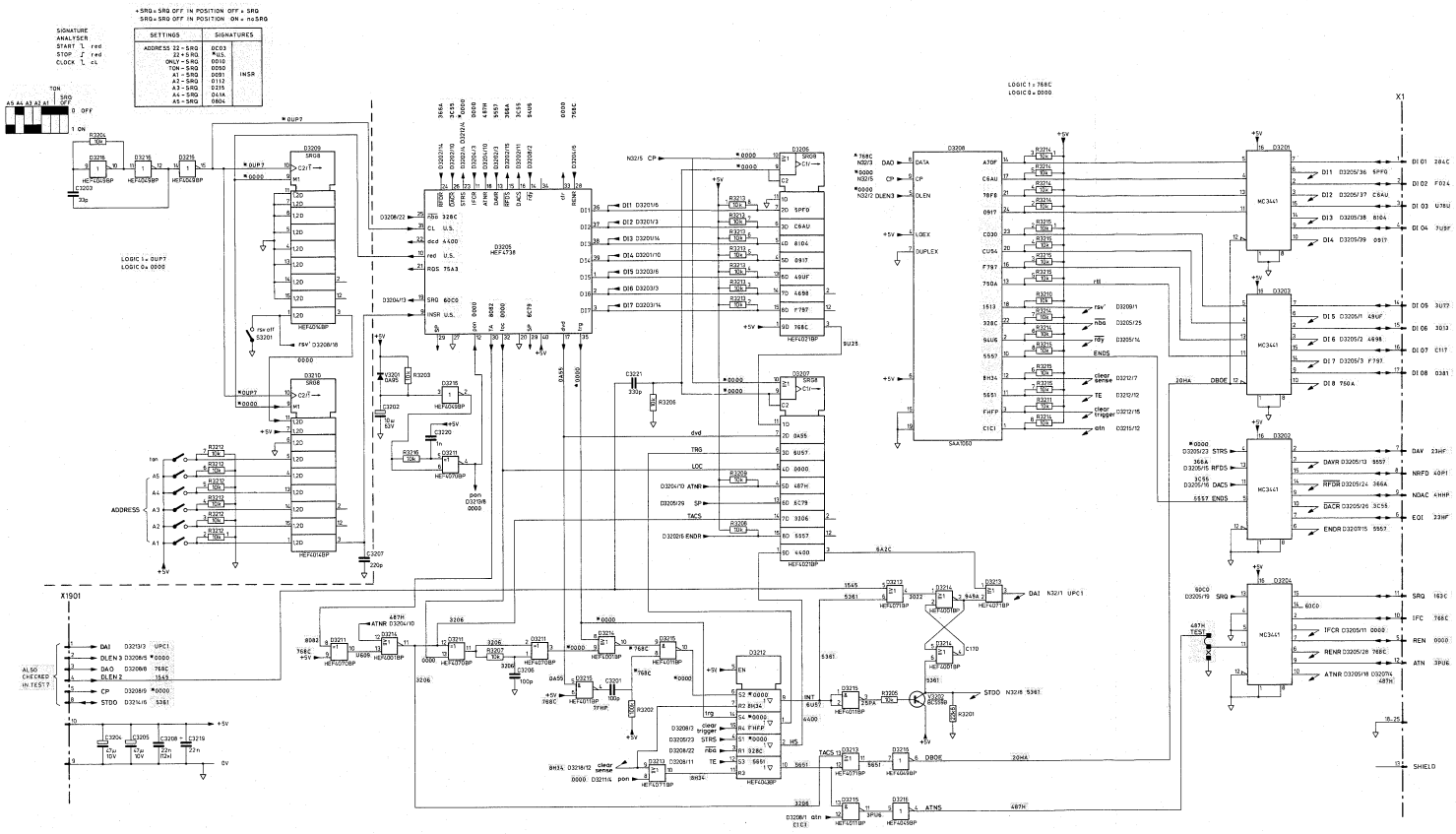
How to measure signal INSR (D3210/3) LOGIC 1 = 0UP7

## Measuring sequence

Settings				Sequence
1	Address	22	- SRQ	0C03
2	Address	22	+ SRQ	US* (Unstable)
3	Only		- SRQ	0010
4	Only	TON	- SRQ	0050
5	Only	A1	- SRQ	0091
6	Only	A2	- SRQ	0112
7	Only	A3	- SRQ	0215
8	Only	A4	- SRQ	041A
9	Only	A5	- SRQ	0804

+SRQ = SRQ off in position off = SRQ

SRQ = SRQ off in position on = SRQ



+SRG-SRG OFF IN POSITION OFF + SRG  
SRG-SRG OFF IN POSITION ON +SRG

ADDRESS	DATA	KEY	SIGNATURE
0000	0000	0000	0000
0001	0001	0001	0001
0002	0002	0002	0002
0003	0003	0003	0003
0004	0004	0004	0004
0005	0005	0005	0005
0006	0006	0006	0006
0007	0007	0007	0007
0008	0008	0008	0008
0009	0009	0009	0009
0010	0010	0010	0010
0011	0011	0011	0011
0012	0012	0012	0012
0013	0013	0013	0013
0014	0014	0014	0014
0015	0015	0015	0015
0016	0016	0016	0016
0017	0017	0017	0017
0018	0018	0018	0018
0019	0019	0019	0019
0020	0020	0020	0020
0021	0021	0021	0021
0022	0022	0022	0022
0023	0023	0023	0023
0024	0024	0024	0024
0025	0025	0025	0025
0026	0026	0026	0026
0027	0027	0027	0027
0028	0028	0028	0028
0029	0029	0029	0029
0030	0030	0030	0030
0031	0031	0031	0031
0032	0032	0032	0032
0033	0033	0033	0033
0034	0034	0034	0034
0035	0035	0035	0035
0036	0036	0036	0036
0037	0037	0037	0037
0038	0038	0038	0038
0039	0039	0039	0039
0040	0040	0040	0040
0041	0041	0041	0041
0042	0042	0042	0042
0043	0043	0043	0043
0044	0044	0044	0044
0045	0045	0045	0045
0046	0046	0046	0046
0047	0047	0047	0047
0048	0048	0048	0048
0049	0049	0049	0049
0050	0050	0050	0050
0051	0051	0051	0051
0052	0052	0052	0052
0053	0053	0053	0053
0054	0054	0054	0054
0055	0055	0055	0055
0056	0056	0056	0056
0057	0057	0057	0057
0058	0058	0058	0058
0059	0059	0059	0059
0060	0060	0060	0060
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0062	0062	0062	0062
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0070	0070	0070	0070
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0075	0075	0075	0075
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0080	0080	0080	0080
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0089	0089	0089	0089
0090	0090	0090	0090
0091	0091	0091	0091
0092	0092	0092	0092
0093	0093	0093	0093
0094	0094	0094	0094
0095	0095	0095	0095
0096	0096	0096	0096
0097	0097	0097	0097
0098	0098	0098	0098
0099	0099	0099	0099
0100	0100	0100	0100