USB 3.1 ENGINEERING CHANGE NOTICE

Title: SKP OS Bytes During Compliance Clarification
Applied to: USB_3_1r1.0_07_31_2013

Brief description of the functional changes:

Correct typographical errors in Table 6-12, which defines the SKP ordered set for SuperSpeed Gen 2.

First, the table refers to values for SKP OS bytes during compliance. However, SKP OS transmission is not allowed in compliance mode. The proposed change resolves this inconsistency. Second, there is a reference to sending a parity bit but this functionality is not defined in USB 3.1. Once again, removing this text fixes this issue.

Benefits as a result of the changes:

Correction to incorrect specifications.

An assessment of the impact to the existing revision and systems that currently conform to the USB specification:

No impact

An analysis of the hardware implications:

None

An analysis of the software implications:

None

An analysis of the compliance testing implications:

None

USB 3.1 ENGINEERING CHANGE NOTICE

Actual Change

(a)From Text (and location): Table 6-12, Section 6.4.2, page 6-20

2*N+1	00-FFh		
		(i)	If prior block was a Data Block: Bit[7] = Even Data Parity Bit[6:0] = LFSR[22:16]
		(ii)	Else: Bit[7] = ~LFSR[22] Bit[6:0] = LFSR[22:16]
2*N+2	00-FFh	(i)	If LTSSM state is Compliance mode: Error_Status[7:0]
		(ii)	Else LFSR[15:8]
2*N+3	00-FFh	(i)	If LTSSM state is Compliance mode: ~Error_Status[7:0]
		(ii)	Else LFSR[7:0]

(a)To Text (and location): Table 6-12, Section 6.4.2, page 6-20

2*N+1	40-BFh	Bit[7] = ~LFSR[22] Bit[6:0] = LFSR[22:16]
2*N+2	00-FFh	LFSR[15:8]
2*N+3	00-FFh	LFSR[7:0]

USB 3.1 ENGINEERING CHANGE NOTICE

(b) From Text (and location): Section 6.4.2, page 6-21

1. The Data parity bit should be even parity for last three symbols in the SKP OS. The parity is a check of the LFSR seed value.

(c) From Text (and location): Section 6.4.2, page 6-21

(no text – item 5 deleted)