

INSTRUCTION MANUAL

Serial Number 701-180

TYPE 7A22 DIFFERENTIAL AMPLIFIER

Tektronix, Inc.

S.W. Millikan Way ● P. O. Box 500 ● Beaverton, Oregon 97005 ● Phone 644-0161 ● Cables: Tektronix
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Abbreviations and symbols used in this manual are based on or taken directly from IEEE Standard 260 "Standard Symbols for Units", MIL STD-12B and other standards of the electronics industry. Change information, if any, is located at the rear of this manual.

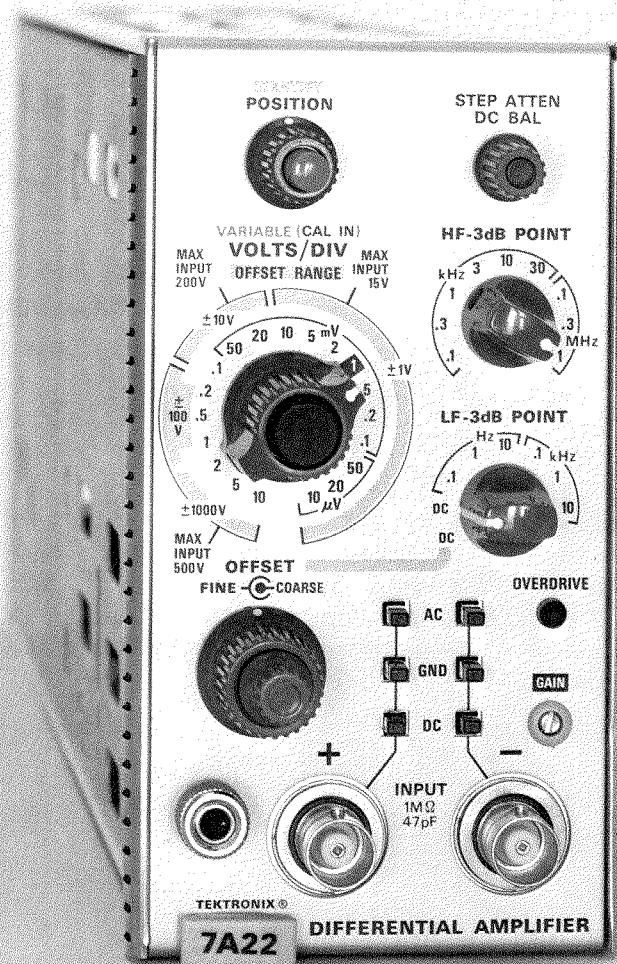


Fig. 1-1. Type 7A22 Differential Amplifier.

SECTION 1

SPECIFICATION

Change information, if any, affecting this section will be found at the rear of the manual.

Introduction

The Type 7A22 Vertical Plug-in is a DC coupled differential amplifier with excellent common-mode rejection characteristics and high gain for low level applications.

The DC Offset capability of the Type 7A22 allows the display of the very small low-frequency signals containing a large DC component, at deflection factors not possible with AC coupling. The vertical deflection factor range of the Type 7A22 is from 10 μ V to 10 V. The high and low frequency -3 dB points can be selected at the front panel, to set the bandwidth of the instrument. Thus, for low frequency applications the signal-to-noise ratio can be improved by restricting the bandwidth of the Type 7A22. The bandwidth selection and excellent drift-with-time characteristics provide measurement capabilities in the biomedical, transducer, and other areas which require stable, low deflection factor, low noise measurements.

The Type 7A22 is designed for use in Tektronix 7000 Series oscilloscopes.

The electrical characteristics described in this section are valid over the stated environmental range for instruments calibrated at an ambient temperature of $+20^{\circ}\text{C}$ to $+30^{\circ}\text{C}$ and after a 5 minute warmup unless otherwise noted.

ELECTRICAL CHARACTERISTICS

Characteristic	Performance Requirements
Deflection Factor (VOLTS/DIV)	
Gain Ratio Accuracy	Within 2% with GAIN adjusted at 1 mV/div
VAR (CAL IN) Range	Continuously variable; extends deflection factor to at least 25 V/div
GAIN	Permits adjustment of deflection factor at 1 mV/DIV for all main-frames
Differential Signal Range (DC OFFSET not used)	
10 μ V/Div to 10 mV/Div	At least ± 1 V
20 mV/Div to 0.1 V/Div	At least ± 10 V
0.2 V/Div to 1 V/Div	At least ± 100 V
2 V/Div to 10 V/Div	At least ± 1000 V

DC OFFSET COARSE Range from Electrical Zero	
10 μ V/Div to 10 mV/Div	At least +1 V to -1 V
20 mV to 0.1 V/Div	At least +10 V to -10 V
0.2 V to 1 V/Div	At least +100 V to -100 V
2 V to 10 V/Div	At least +1000 V to -1000 V
Frequency Response (8 div Reference)	
Overall Frequency Response DC (Direct) Coupled Input	DC to within 10% of 1 MHz at -3 dB
AC (Capacitive) Coupled Input Lower Bandwidth Frequency	2 Hz or less
Bandwidth Limit Accuracy	
HF -3 dB POINT 100 Hz to 1 MHz	9 steps in a 1-3 sequence
Accuracy	Within 10% of selected frequency
LF -3 dB POINT 0.1 Hz to 10 kHz	6 steps in a 1-10 sequence
Accuracy	Within 12% of selected frequency
Recovery Time	10 μ s or less to recover to within 0.5% of zero level after the removal of a + or $-$ test input applied for 1 s. Test signal not to exceed Differential Signal Range. Specified aberration (0.5%) based on test signal amplitude
Common Mode Signal	
10 μ V/Div to 10 mV/Div	At least +10 V and -10 V
20 mV/Div to 0.1 V/Div	At least +100 V and -100 V
0.2 V/Div to 10 V/Div	At least +500 V and -500 V
Common-Mode Rejection Ratio	
DC (Direct) Coupled	See Verification Points on graph, Fig. 1-2.
AC (Capacitive) Coupled	See Verification Points on graph, Fig. 1-2.

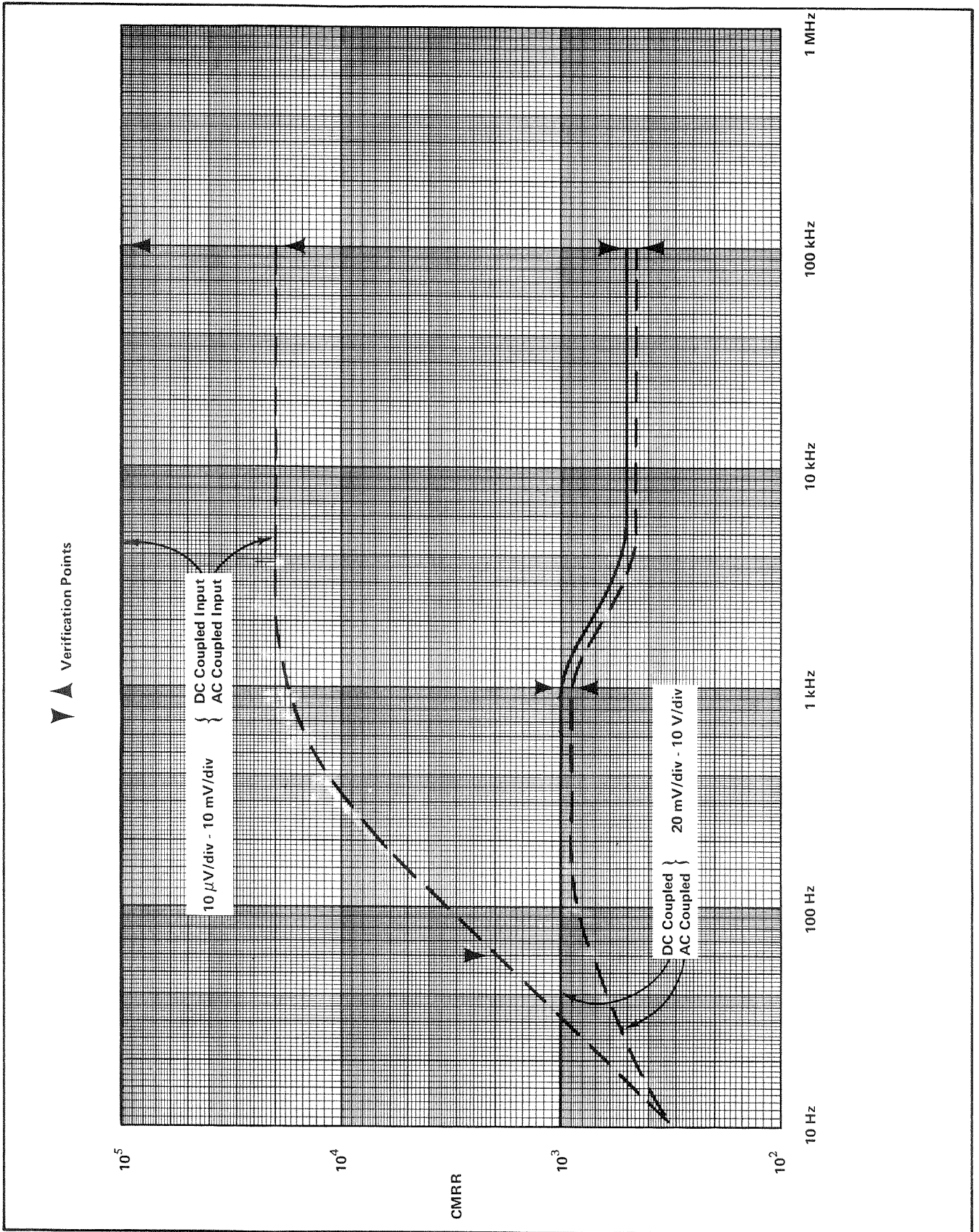


Fig. 1-2. CMRR vs. Frequency for signals not exceeding Common Mode Signal Range.

ELECTRICAL CHARACTERISTICS (cont)

Characteristic	Performance Requirements
Maximum Input Voltage (each input) DC (Direct) Coupled, DC + Peak AC, AC Component 1 MHz or less	
10 μ V/Div to 10 mV/Div	± 15 V
20 mV/Div to 0.1 V/Div	± 200 V
.2 V/Div to 10 V/Div	± 500 V
AC (Capacitive) Coupled Input DC Voltage	± 500 V, each input
AC (Capacitive) Coupled Input DC Rejection	At least $4 \times 10^5 : 1$
Input R and C Resistance	1 M Ω \pm 1%
Capacitance	Approximately 47.0 pF
R & C Product	Within $\pm 1\%$ between all deflection factors
Maximum Input Gate Current	+25°C +50°C
10 μ V/Div to 10 mV/Div	± 20 pA ± 100 pA each input ± 40 pA ± 200 pA Differentially
20 mV/Div to 10 V/Div	± 10 pA ± 20 pA
Display Shift at 10 μ V/Div (AC Coupled)	± 4 div ± 20 div
Variable Balance	0.2 div or less shift with VARIABLE control turned from fully cw to fully ccw position
Displayed Noise (Tangentially Measured)	16 μ V or 0.1 Div (whichever is greater), 1 MHz HF -3 dB POINT, source resistance 25 Ω or less

DC Drift	
Drift with Time (Ambient Temperature and Line Voltage Constant)	
Short Term	5 μ V (P-P) or 0.1 div (whichever is greater) in any minute after 1 hour warmup
Long Term	10 μ V (P-P) or 0.1 div (whichever is greater) in any hour after 1 hour warmup
Drift with Ambient Temperature (Line Voltage Constant)	50 μ V/ $^{\circ}$ C or less
Isolation between + and - Inputs (+ INPUT to an Open - INPUT, - INPUT to an Open + INPUT)	At least 200:1, DC to 1 MHz

ENVIRONMENTAL

PLUG-IN TESTED OUT OF INDICATOR OSCILLOSCOPE

ITEM	CHARACTERISTIC
Altitude Non-operating	To 50,000 feet and -55° C
Transportation	Qualified under National Safe Transit Committee test procedure 1A, Category II

PHYSICAL

ITEM	CHARACTERISTIC
Finish	Front panel is anodized aluminum
Dimensions	$\approx 14\frac{5}{8}$ inch L \times $2\frac{5}{8}$ inch W \times $4\frac{7}{8}$ inch H
Weight	$\approx 2\frac{5}{8}$ lbs

SECTION 2

OPERATING INSTRUCTIONS

Change information, if any, affecting this section will be found at the rear of the manual.

Introduction

This section opens with a brief functional description of the front-panel controls, input overdrive lamp, and input connectors. Following the front-panel description is a familiarization procedure and finally a general discussion of the operation of the Type 7A22.

CONTROLS AND CONNECTORS

INPUT OVERDRIVE Input overdrive indicator lamp turns on to indicate excessive differential drive to the input amplifier stage. Lights when the differential dynamic range between input connectors is exceeded.

VOLTS/DIV Volts per displayed division. Nineteen position switch used to select the calibrated deflection factors.

VARIABLE (CAL IN) Two-position switch activated by the VARIABLE knob to select calibrated or uncalibrated deflection factors. At the IN position, the VARIABLE control is inoperative and the deflection factor is calibrated. When pressed and released, the knob moves outward to activate the VARIABLE control for uncalibrated deflection factors. The uncalibrated position provides continuously variable uncalibrated attenuation between the calibrated deflection factors and extends the deflection factor to at least 25 VOLTS/DIV.

GAIN Screwdriver adjust control to set the CRT display scale factor to agree with the VOLTS/DIV switch indication. Adjusted for proper deflection with the VOLTS/DIV switch set to the 1 mV position.

POSITION The control that vertically positions the trace or display.

IDENTIFY Momentary contact, push-button switch, concentric with POSITION. Will cause the trace, representing the output of the Type 7A22, to move a small amount when pressed. Aids in identifying the Type 7A22 trace when multiple traces are displayed.

HIGH FREQUENCY —3 dB POINT Nine position switch to select the approximate high frequency —3 dB point. The switch positions are: 100 Hz, 300 Hz, 1 kHz, 3 kHz, 10 kHz, 30 kHz, 100 kHz, 300 kHz, and 1 MHz.

LOW FREQUENCY —3 dB POINT

Eight position switch to select DC coupling or the approximate low frequency —3 dB points. The switch positions are: DC OFFSET, DC, .1 Hz, 1 Hz, 10 Hz, 100 Hz, 1 kHz, and 10 kHz.

+ INPUT

Signal input connector. Positive input produces deflection upward (see Fig. 2-1).

— INPUT

Signal input connector. Positive input produces deflection downward (see Fig. 2-1).

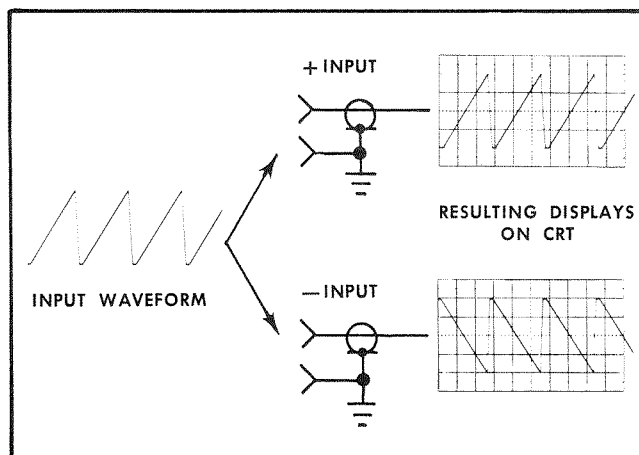


Fig. 2-1. Signals applied to the +INPUT connector produces an upright display, while signals applied to the —INPUT are inverted.

+ AC-GND-DC

A miniature illuminated push-button type switch. The buttons are interlocked so that only one button may be depressed at one time. When the 'AC' button is depressed the signal is coupled through 0.1 μ F to the Input Amplifier and only the varying component of the input signal is amplified.

When the 'DC' button is depressed the signal is coupled directly to the Input Amplifier and the entire input signal, both AC and DC, is amplified. When the 'GND' button (or none of the buttons) is depressed, the signal is coupled through 0.1 μ F and through 1 M Ω to ground. The Amplifier Input is grounded in this condition.

Operating Instructions—Type 7A22

	Each push-button is illuminated from behind when the button is depressed.
— AC-GND-DC	Same function as the + AC-GND-DC switch but applied to the — INPUT.

IMPORTANT

The following two controls are operative only when the LOW FREQUENCY —3 dB POINT switch is set to DC or DC OFFSET.

STEP ATTEN DC BAL (DC MODE ONLY)	Front panel control for DC balancing the amplifier input stage. With no signals applied to the input connectors, the control is adjusted for no trace shift as the VOLTS/DIV switch is moved from the 10 mV position to the 10 μ V position.
DC OFFSET (DC OFFSET mode only)	Coarse and fine controls to provide internal offset bias while maintaining the differential capability. Available range of the offset bias depends upon the settings of the VOLTS/DIV switch, and is indicated by shaded gray bands (OFFSET RANGE) around the VOLTS/DIV switch.
RELEASE LATCH	Gray rectangular knob near the bottom left of the front panel. Pull out to remove Plug-In from Plug-In compartment.

OPERATING INSTRUCTIONS

First-Time Operation

Steps 1 through 5 in the following procedure are intended to help place the trace on the screen quickly and prepare the unit for immediate use. Steps 6 through 8 are used to check the GAIN adjustment. These steps along with those remaining are intended to demonstrate some of the basic functions of the Type 7A22.

1. Insert the unit into the oscilloscope plug-in compartment.

2. Set the Type 7A22 front-panel controls as follows:

VOLTS/DIV	1 mV
VARIABLE	IN (CAL)
POSITION	Midrange
HIGH FREQUENCY —3 dB POINT	1 MHz
LOW FREQ —3 dB POINT	DC
AC-GND-DC (+ INPUT)	GND
AC-GND-DC (— INPUT)	GND
STEP ATTEN DC BAL	Midrange

3. Turn the oscilloscope Intensity control fully counter-clockwise and turn the oscilloscope Power ON. Preset the time-base and triggering controls for a .5 ms sweep rate and automatic triggering.

4. Wait about five minutes for the Type 7A22 and the oscilloscope to warm up.

NOTE

About five minutes is sufficient time for warmup when using the Type 7A22 for short-term DC measurements. For long-term DC measurements using the lower deflection factors, allow at least one hour.

5. Adjust the Intensity control for normal viewing of the trace. The trace should appear near the graticule center.

6. Using the POSITION control, position the trace 2 divisions below graticule center.

CAUTION

If the maximum input voltage rating in the 10 μ V to 10 mV range of the VOLTS/DIV switch is exceeded, the inputs are diode-clamped to fixed voltages of approximately +16.5 volts and/or —16.5 volts and damage to the signal source is possible. If the signal source can supply more than 1/16 A of current, the input protective fuse(s) will open. An open input fuse is indicated by the lighting of the INPUT OVERDRIVE indicator with the input coupling switches set to GND.

7. Apply a 4 mV peak-to-peak calibrator signal through a coaxial cable to the + INPUT connector on the Type 7A22.

8. For DC coupled, single-ended operation, set the + INPUT AC-GND-DC coupling switch to DC. The display should be square waves 4 divisions in amplitude with the bottom of the display at the reference established in step 6.

9. For AC coupled, single-ended operation, reposition the display with the Type 7A22 POSITION control to place the bottom of the display at the graticule center line.

10. Set the + INPUT AC-GND-DC coupling switch to AC and note that the display shifts downward about 2 divisions to its average level.

11. Disconnect the coaxial cable from the + INPUT connector. Connect a dual input connector to the + INPUT and — INPUT connectors. Connect the coaxial cable from the Calibrator to the dual input connector.

12. For AC coupled differential operation, set the —INPUT AC-GND-DC coupling switch to AC. The calibrator signal is now coupled to both inputs as a common-mode signal. A straight line display should be observed, since the common-mode is being rejected.

Operational Adjustments

NOTE

Most of the following checks and adjustments may be made after a 5 minute warmup; however due to the DC drift of the amplifier during subsequent warmup, the STEP ATTEN DC BAL should be re-adjusted for each check or adjustment, and a warmup of at least one hour should be allowed before a final adjustment of the STEP ATTEN DC BAL is attempted. Insure that the oscilloscope used in conjunction with the Type 7A22 is correctly calibrated (refer to the oscilloscope manual) and that the calibrated output voltage is correct.

1. AC ATTEN BAL (internal adjustment). When the LOW FREQ —3 dB POINT switch is used to limit the low frequency response of the Type 7A22, the unit employs AC stabilization. This means that when the switch is set to any position except DC or DC OFFSET, the STEP ATTEN DC BAL and DC OFFSET controls become inoperative. VOLTS/DIV balance is then controlled with the AC ATTEN BAL, an internal adjustment (see Fig. 2-2).

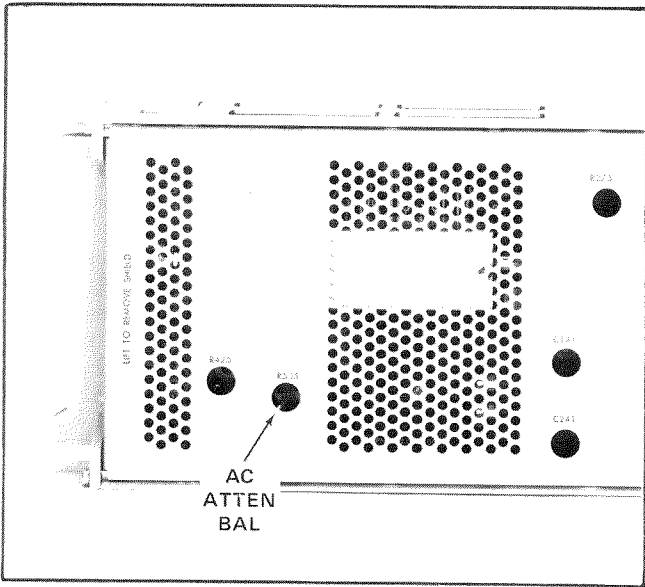


Fig. 2-2. Location of AC ATTEN BAL control.

When transferring the Type 7A22 from one oscilloscope to another, it may be necessary to perform a minor re-adjustment of this control, due to normal power supply variations between oscilloscopes.

NOTE

An unbalance of up to 30 μ V is normal and cannot be reduced by adjusting the AC ATTEN BAL control.

a. With the Type 7A22 inserted into the oscilloscope, remove the left side panel of the oscilloscope and set the Type 7A22 controls as follows:

VOLTS/DIV	1 mV
AC-GND-DC (+INPUT)	GND
AC-GND-DC (-INPUT)	GND
LOW FREQ —3 dB POINT	10 Hz
HIGH FREQ —3 dB POINT	100 Hz

b. Using the POSITION control, position the trace to graticule center.

c. Set the VOLTS/DIV switch to 20 μ V.

d. Adjust the AC ATTEN BAL control, R505, to position the trace to within 1.5 divisions of graticule center. (See note in step 1).

INTERACTION: If this adjustment is made out of sequence, steps 2 and 3 must also be performed.

2. VAR BAL R425. (Internal adjustment).

a. Set the Type 7A22 controls as in step 1-a.

b. Position the trace to graticule center with the POSITION control.

c. With the VAR (CAL IN) in the out position, rotate the VARIABLE control from stop to stop. Check for no movement of trace.

d. Adjust R425 for no movement of trace as the VARIABLE control is rotated from stop to stop. (See Fig. 2-2 for location of adjustment.)

3. STEP ATTEN DC BAL. With zero input voltage and the LF —3 dB POINT selector at DC, if the STEP ATTEN DC BAL is not properly adjusted the CRT trace will shift vertically as the VOLTS/DIV switch is rotated throughout its range. The shift is more noticeable at the most sensitive positions.

a. Set the Type 7A22 front-panel controls as follows:

VOLTS/DIV	10 mV
VARIABLE (CAL IN)	CAL
POSITION	Midrange
HIGH FREQ —3 dB POINT	1 MHz
LOW FREQ —3 dB POINT	DC
AC-GND-DC (+INPUT)	GND
AC-GND-DC (-INPUT)	GND
STEP ATTEN DC BAL	Midrange

b. Using the Type 7A22 POSITION control, position the CRT trace to the center of the graticule.

c. Set the VOLTS/DIV switch to 10 μ V.

d. Return the trace to graticule center by adjusting the STEP ATTEN DC BAL control.

NOTE

The adjustment of the STEP ATTEN DC BAL control should be checked periodically during the use of the instrument. If the Type 7A22 is used DC coupled or in significantly varying ambient temperatures in the 10 μ V/DIV to .1 mV/DIV ranges, the STEP ATTEN DC BAL should be checked quite frequently. It is good practice to check this control and readjust, if necessary, before any critical measurement is made under the above conditions.

4. GAIN

a. Perform steps 1 through 8 in the First-Time Operation Procedure.

b. Adjust the GAIN control for exactly 4 divisions of display.

NOTE

Accuracy of this adjustment is dependent upon the voltage accuracy of the calibration source.

GENERAL OPERATING INFORMATION

Trace Drift

The environment in which the Type 7A22 is operated and the inherent characteristics of the Type 7A22 influence trace drift. Therefore, to determine trace drift for a specific environment refer to the Specification Section. In environment in which the ambient temperature does not vary much (such as an air-conditioned building) the trace drift generally will not exceed $10 \mu\text{V}$ in one hour.

Input Gate Current

When using the $.1 \text{ mV/DIV}$ to $10 \mu\text{V/DIV}$ ranges for measurement with an AC coupled input, for DC measurements where the source impedance is high (in excess of $1 \text{ M}\Omega$) the input gate current should be checked and allowed for, or adjusted to zero. This is particularly desired at high temperatures (above 40°C). Steps 7 and 8 in the Performance Check/Calibration Procedure describe the check and adjust procedures for setting the gate current to zero.

Voltage Measurement

To obtain accurate DC measurements at maximum sensitivity, it is necessary to ground the input and DC balance the amplifier just before making the measurement. This is accomplished by adjusting the STEP ATTEN DC BAL as described under operational adjustment number 3.

When measuring DC voltages, use the largest deflection factor (10 V/DIV) when first connecting the Type 7A22 to an unknown voltage source. Then, if the deflection is too small to make the measurement, switch to a lower deflection factor. If the input stage is overdriven, a large amount of current might flow into the input. See CAUTION after item 6 of First Time Operation.

Where only the AC component of a signal having both AC and DC components is to be measured, use the AC-GND-DC switches to take advantage of the pre-charging circuit incorporated in the unit. The pre-charging circuit permits the coupling capacitor to charge to the DC source voltage when the AC-GND-DC switch is set to GND. Procedure for using this circuit is as follows:

- a. Before connecting the Type 7A22 to a signal containing a DC component, set the AC-GND-DC input coupling switch to GND. Then connect the input to the circuit under test.
- b. Allow about one second for the coupling capacitor to charge.
- b. Set the input coupling switch to AC. The display will remain on the screen and the AC component can be measured in the usual manner.
- d. On completion of the measurement, set the AC-GND-DC switch to GND and short the input connector to ground.

The above procedure should be followed whenever another signal with a different DC level is connected.

CAUTION

If the Type 7A22 is connected to a large DC voltage source without using the pre-charge provision, the peak charging current (into $0.1 \mu\text{F}$

capacitor) will be limited only by the signal source, and this source may be damaged or destroyed.

When a large DC voltage has been applied to the Type 7A22 with the input AC coupled, the input coupling capacitor acquires a charge due to dielectric polarization and acts as a low voltage, high impedance voltage source with a very slowly decaying output voltage. This can offset subsequent AC coupled measurements at other DC voltages and drive the trace off-screen. A period of at least 10 minutes, with input set to GND, should be allowed to assure reasonable recovery from polarization, and a longer period may be necessary for critical measurements. If the input connectors are shorted to ground the depolarization process will require less time.

Signal Input Connectors

When connecting signals to the +INPUT and -INPUT connectors on the Type 7A22, consider the method of coupling that will be used. Ordinary unshielded test leads can sometimes be used to connect the Type 7A22 to a signal source, particularly when a high level, low-frequency signal is monitored at a low impedance point. However when any of these factors are missing, it becomes increasingly important to use shielded signal cables. In all cases, the signal-transporting leads should be kept as short as practical.

When making single-ended input measurements, be sure to establish a common ground between the device under test and the Type 7A22. The shield of a coaxial cable is normally used for this purpose.

In some cases differential measurements require no common ground¹, and therefore are less susceptible to interference by ground loop currents. Some problems with stray magnetic coupling into the signal transporting leads can also be minimized by using a differential rather than single-ended measurement. These considerations are discussed later in this section under Differential Operation.

It is always important to consider the signal-source loading and resulting change in the source operating characteristics due to the signal-transporting leads and the input circuit of the Type 7A22. The circuit at the input connectors can normally be represented by a 1 megohm resistance to ground paralleled by 47 pF . A few feet of shielded cable may increase the parallel capacitance to 100 pF or more. In many cases, the effects of these resistive and capacitive loads may be too great and it may be desirable to minimize them through the use of an attenuator probe.

Attenuator probes not only decrease the resistive and capacitive loading of a signal source, but also extend the measurement range of the Type 7A22 to include substantially higher voltages. Passive attenuator probes having attenuation factors of $10\times$, $100\times$, and $1000\times$, as well as other special-purpose types are available through your Tektronix Field Engineer or Field Office.

Some measurement situations require a high-resistance input to the Type 7A22 with very little source loading or sig-

¹The DC plus peak AC voltages on the test points with respect to the chassis potential of the Type 7A22 should be limited to the levels listed in Section 1 under Maximum Common-mode Input Voltage characteristics. Higher levels will degrade the common-mode rejection ratio and exceed the input voltage rating of the unit.

nal attenuation. In such a situation a passive attenuator probe cannot be used. However, this problem may be solved by using an active probe or the high input impedance provision of the Type 7A22.

High Input Impedance

The high input impedance provision applies only to DC coupled signals which permit the use of 10 mV through 10 μ V positions of the VOLTS/DIV switch, (DC coupled). Since no input attenuator is used at these switch positions, the internal gate return resistor alone establishes the 1 megohm input resistance.

The high input impedance is obtained by unsoldering the wire strap (see Fig. 2-3) between the input line and the internal gate return resistance. The signal source must then provide a DC path for the FET gate current.

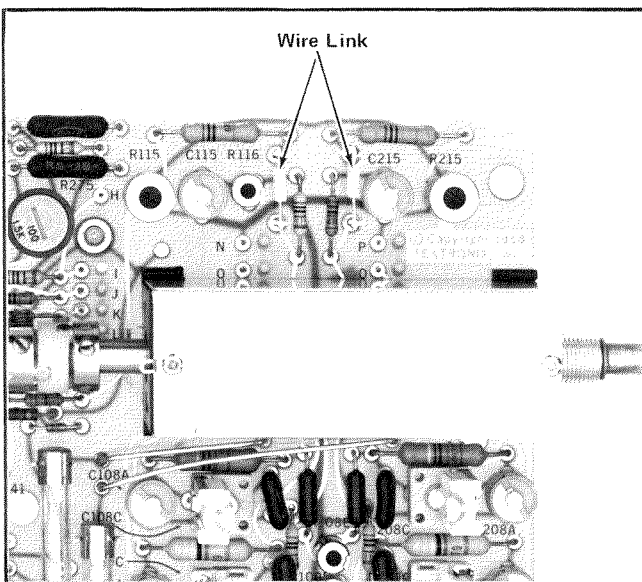


Fig. 2-3. Location of wire strap between input line and internal gate return resistor.

The uncompensated gate current is typically less than 100 picoamperes, but may be several times higher depending upon the operating temperature. The signal-source impedance is therefore an important factor since gate current will produce a DC offset. For example, a 100 picoampere gate current through 10 megohms produces a 1 mV offset; this may result in a significant error where small voltages are of concern.

NOTE

When the wire straps are removed, R111 and R211 are disconnected. The deflection factor in the 20 mV/DIV to 10 V/DIV range will be incorrect.

The high-frequency response will also depend upon the signal-source impedance, since various shunt capacitances between the source and the 7A22 input as well as the 47 pF input capacitance, must charge and discharge through that impedance (see Fig. 2-4).

Display Polarity

Single-ended signals can be applied to either the +INPUT or -INPUT connector. If the +INPUT is chosen, positive-going changes in the input signal will cause the trace to be deflected upward, and negative-going changes will cause the trace to be deflected downward. If the -INPUT is chosen, input-to-display polarity relationship will be reversed as shown previously in Fig. 2-1.

Deflection Factor

The amount of trace deflection produced by a signal is determined by the signal amplitude, the attenuation factor of the probe, the setting of the VOLTS/DIV switch and the setting of the VARIABLE control. The calibrated deflection factors indicated by the VOLTS/DIV switch apply only when the VARIABLE control is pushed "in" to the CAL IN position.

The range of the VARIABLE control is at least 2.5:1. It provides uncalibrated deflection factors covering the full range between the fixed settings of the VOLTS/DIV switch. The control can be set to extend the deflection factor to at least 25 volts/div.

Noise

To reduce noise and obtain a more usable display when the VOLTS/DIV switch is operated in the 10 μ V, 20 μ V, and 50 μ V positions or when the signal source is noisy, it is suggested that the HIGH FREQ -3 dB POINT selector be set to use the lowest bandwidth setting which does not appreciably distort the desired features of the signal under observation. Refer to Fig. 2-6 for the high frequency rolloff for each setting of the HIGH FREQ -3 dB POINT selector.

Bandwidth Selection

In addition to the differential rejection of unwanted signals, many times an undesired signal can be attenuated by varying the bandwidth of the unit. The LOW FREQ -3 dB POINT and HIGH FREQ -3 dB POINT selectors on the front panel of the 7A22 control the low-frequency and high-frequency -3 dB points of the amplifier. The LOW FREQ -3 dB POINT selector provides low-frequency response to DC or to approximate -3 dB points at .1 Hz, 1 Hz, 10 Hz, 100 Hz, 1 kHz, and 10 kHz. Refer to Fig. 2-5. The HIGH FREQ -3 dB POINT selector controls the high-frequency rolloff from 1 MHz to 100 Hz in a 1-3-10 sequence. Beyond the -3 dB points the frequency response falls off at a 6 dB per octave rate. Refer to Fig. 2-6.

Varying the bandwidth of the Type 7A22 is useful, for example, when displaying a low-frequency signal. By reducing the high-frequency response the noise can, in many cases, be considerably reduced without distorting the desired signal (see Fig. 2-7). Likewise, undesired line-frequency signals can be filtered out by restricting the low-frequency response of the unit. When using the LOW FREQ -3 dB POINT and HIGH FREQ -3 dB POINT selectors, care must be taken not to distort non-sinusoidal waveforms by overly restricting the amplifier bandwidth.

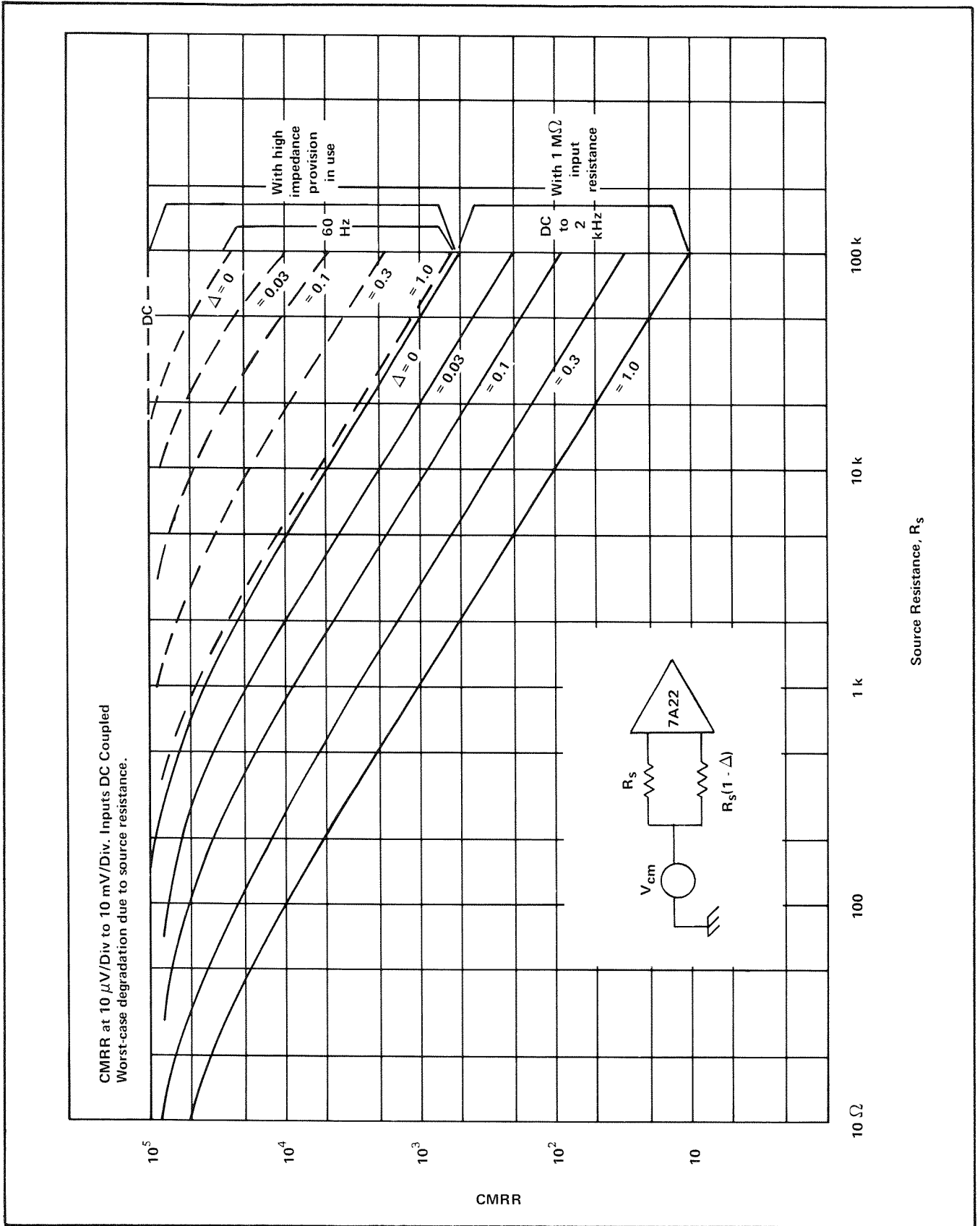
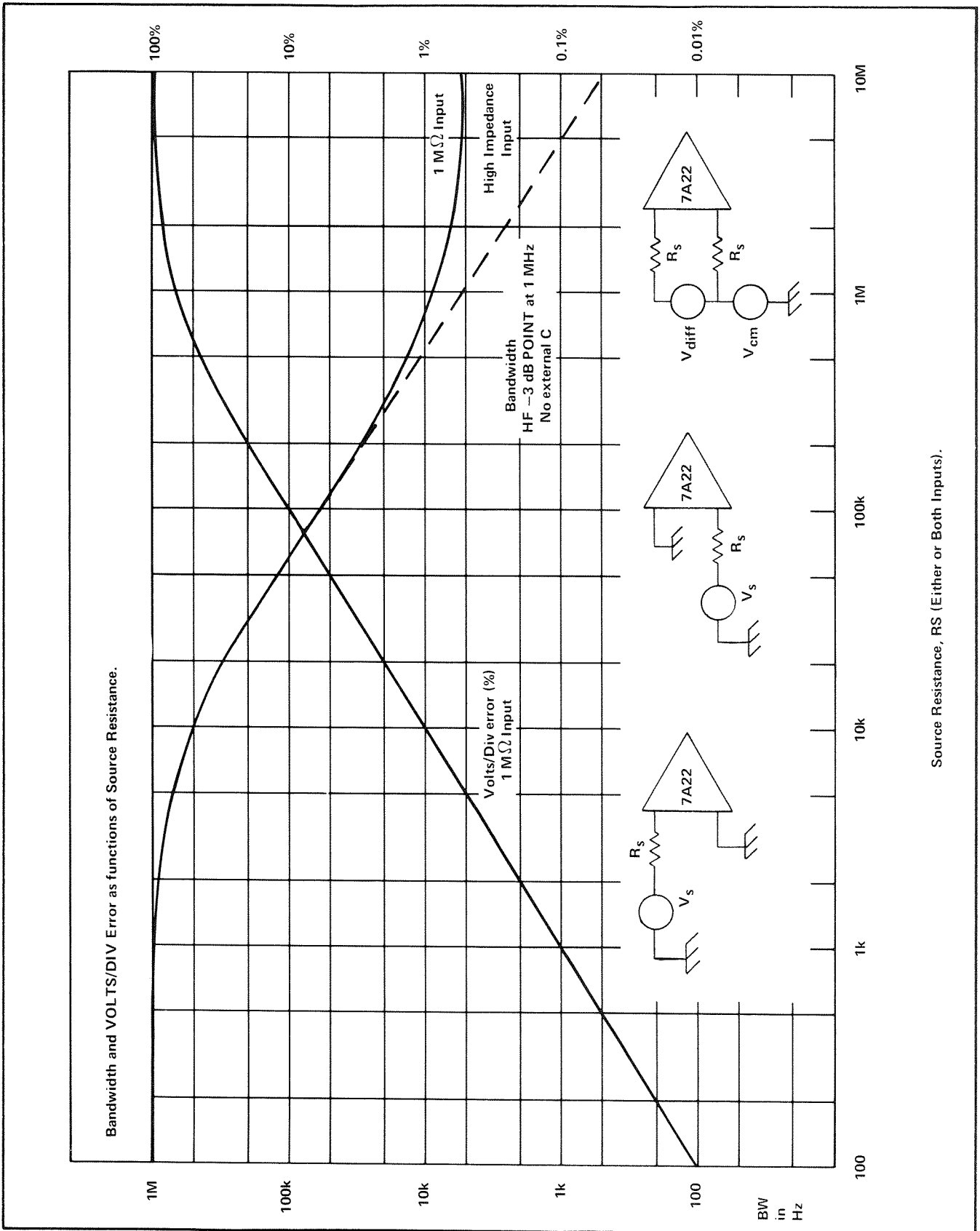


Fig. 2-4A. 7A22 CMRR at 10 μ V/Div to 10 mV/Div. Inputs DC Coupled. Worst-case degradation due to source resistance.



Source Resistance, R_s (Either or Both Inputs).

Fig. 2-4B. 7A22 Bandwidth and VOLTS/DIV Error as functions of Source Resistance.

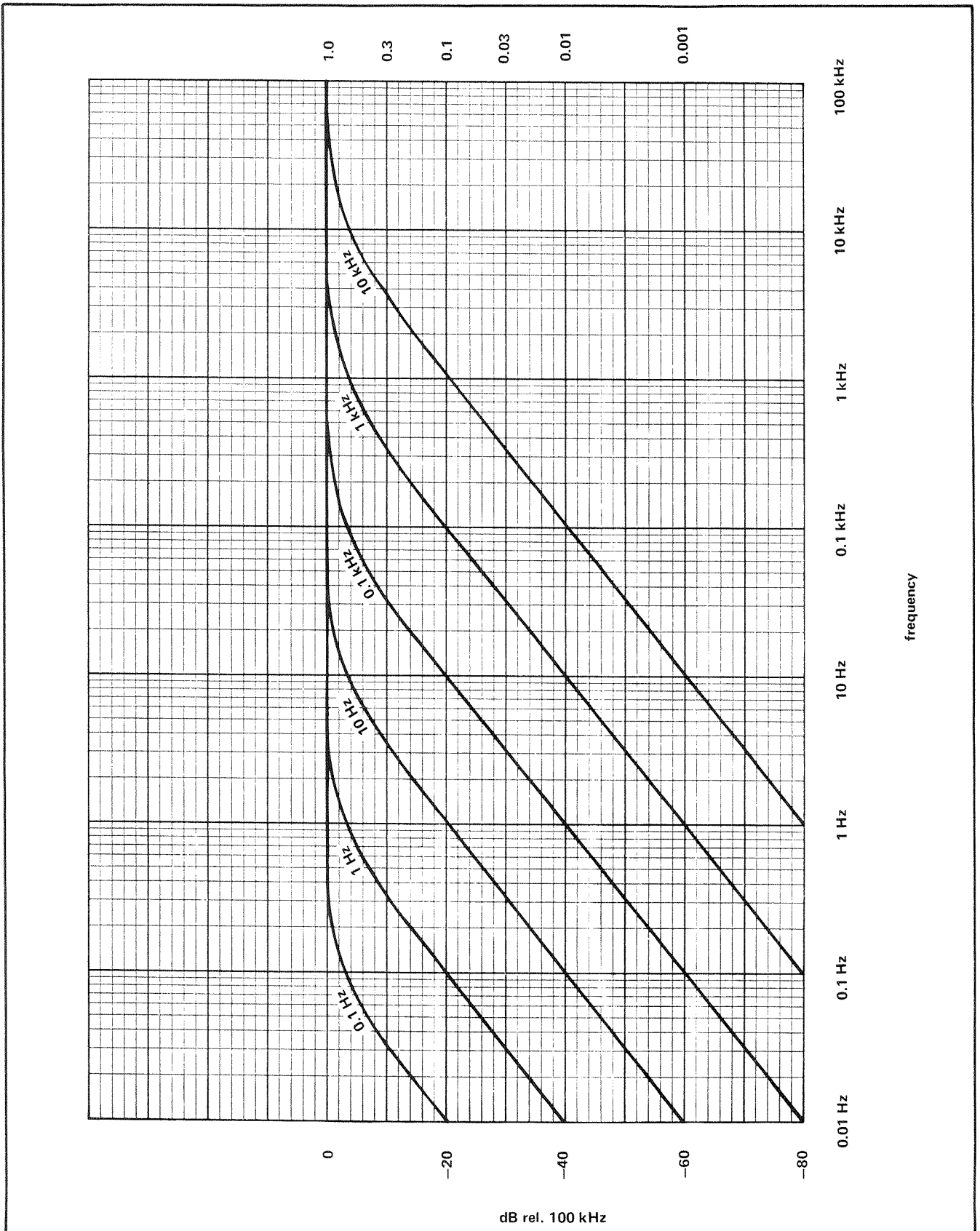


Fig. 2-5. 7A22 FREQUENCY RESPONSE as a function of LF -3 dB POINT SETTING (HF -3 dB POINT at 1 MHz).

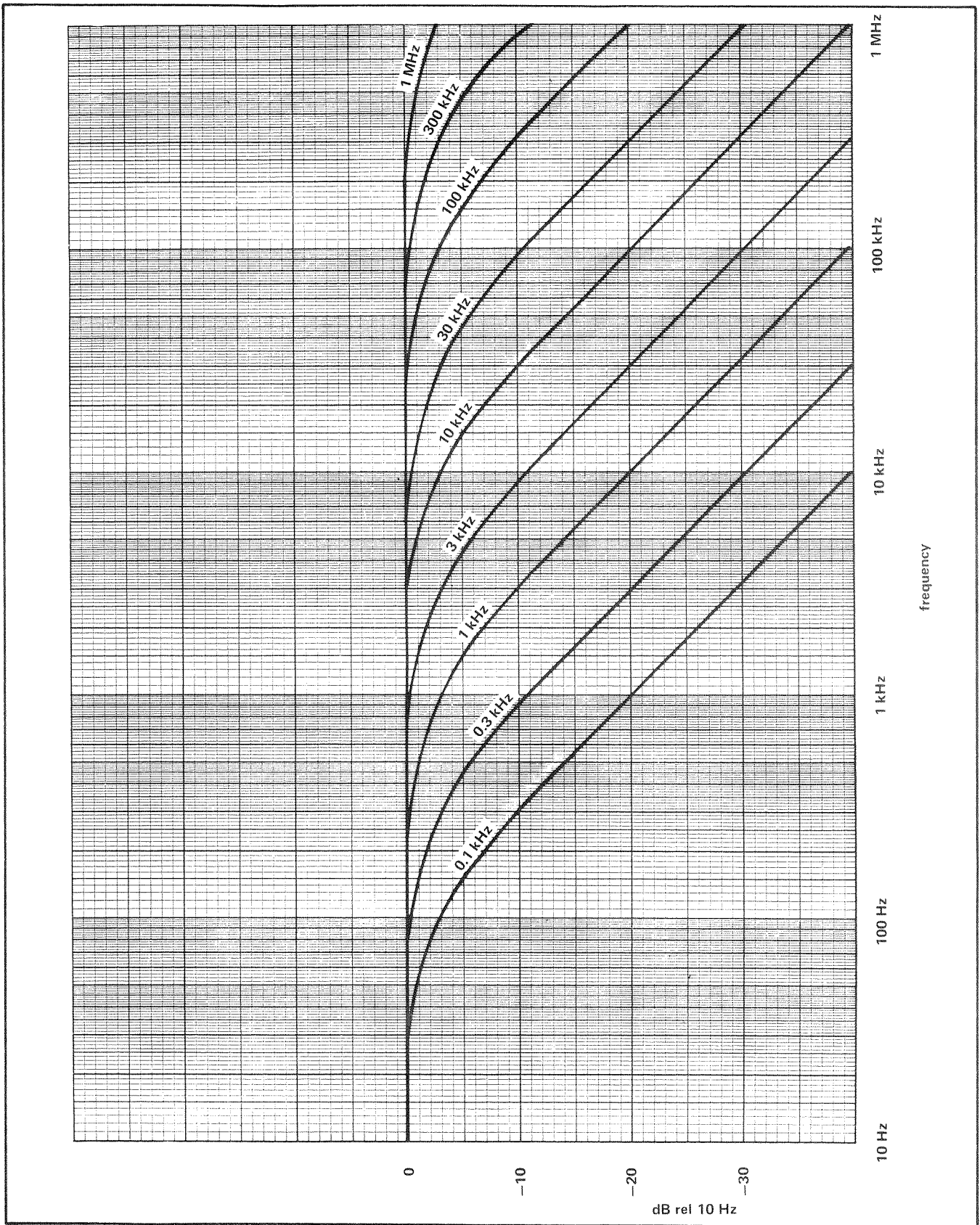


Fig. 2-6. 7A22 FREQUENCY RESPONSE as a function of HF -3 dB POINT settings (LF -3 dB POINT of DC).

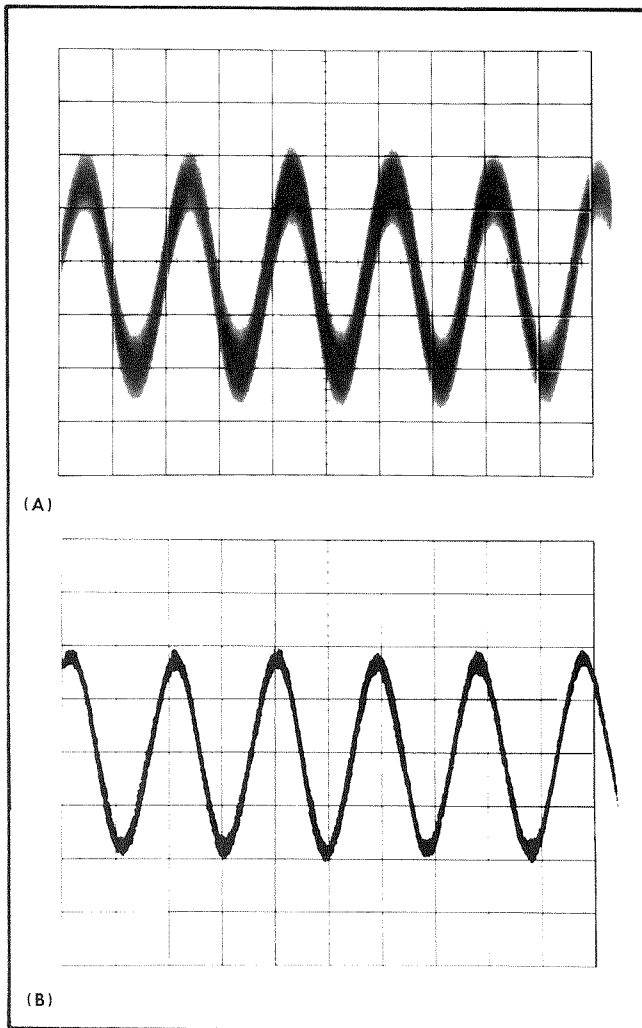


Fig. 2-7. Improving signal-to-noise ratio by setting bandwidth. (A) Lower -3 dB FREQUENCY selector to DC, UPPER -3 dB FREQUENCY 1 MHz. (B) Lower -3 dB FREQUENCY selector to DC, UPPER -3 dB FREQUENCY, 10 kHz.

Voltage Comparison Measurements

Some applications require a set of deflection factors other than the fixed values provided by the VOLTS/DIV switch. One such application is comparison of signal amplitudes by ratio rather than by absolute voltage.

To accomplish this, apply a reference signal to either input of the Type 7A22. Set the VOLTS/DIV switch and VARIABLE control throughout the subsequent comparisons. The settings of the VOLTS/DIV switch can be changed, however, to accommodate large ratios. In doing so, regard the numbers which designate the switch position as ratio factors rather than voltages.

Differential Operation

Differential voltage measurements are made by applying the signals to the +INPUT and -INPUT connectors. Then, both AC-GND-DC switches should be set to the same positions: AC or DC, depending on the method of signal

coupling desired. When using the Type 7A22 for differential operation, only the voltage difference between the two signals is amplified and displayed on the CRT. Common-mode signals (signals that are common in amplitude, frequency and phase) are rejected (see Fig. 2-8).

The Type 7A22 differential input provision may be used to eliminate interfering signals such as AC line-frequency hum. Single-ended measurements often yield unsatisfactory information because of interference resulting from ground-loop currents between the oscilloscope and the device under test. In other cases, it may be desirable to eliminate a DC voltage by means other than the use of a DC-blocking capacitor which would limit low-frequency response. These limitations of single-ended measurements are virtually eliminated in differential measurements.

A differential measurement is made by connecting each of the two inputs to selected points in the test circuit. The input to the amplifier will then be the difference in voltage of the two selected points (see Fig. 2-9).

The ability of the Type 7A22 to reject common-mode signals is indicated by the common-mode rejection ratio (CMRR). This ratio is at least 100,000:1 at the input connectors for the lower deflection factors (10 μ V to 10 mV per division) when signals between DC and 100 kHz are DC-coupled to the inputs. To show the significance of this characteristic, assume that a single-ended input signal consists of an unwanted 60 Hz signal at 1 volt P-P plus a desired signal at 1 mV P-P. If an attempt is made to display the desired signal with the VOLTS/DIV switch set to .2 mV, the 60 Hz signal would produce a deflection equivalent to 5000 div, and thus little useful information about the 1 mV signal could be obtained.

If, however, the same 1 mV signal is monitored differentially so that the 60 Hz signal is common-mode at the inputs, no greater than one part in one hundred thousand of the common-mode signal will appear in the display. Thus, the desired signal produces a display amplitude of 5 div with only .05 div of interference due to the common-mode signal.

There are a number of factors which can degrade common-mode rejection. The principal requirement for maximum rejection is for the common-mode signal to arrive at the input FET gates in precisely the same form. A difference of only 0.01% in the attenuation factors of the input attenuators may reduce the rejection ratio to 10,000:1. Likewise, any difference in source impedance at the two points in the source under test will degrade the rejection ratio. Attenuator probes which do not have an adjustable resistance may reduce the rejection ratio to 100:1 or less.

Outside influences such as electrostatic and magnetic fields can also degrade the performance, particularly when low level signals are involved. Electrostatic interference can be minimized by using shielded signal transporting leads of the same type to the two inputs, and by twisting the leads together throughout most of their length.

Care should be taken to minimize the movement of the signal transporting leads, as any movement of a lead, in the presence of a magnetic field, will tend to induce a signal into that lead. Where an interfering magnetic field cannot be avoided, the pickup loop formed by the two leads should be minimized by taping or twisting them together throughout most of their length. Low-frequency measurements can be similarly protected by using a shielded cable which contains a twisted pair of conductors.

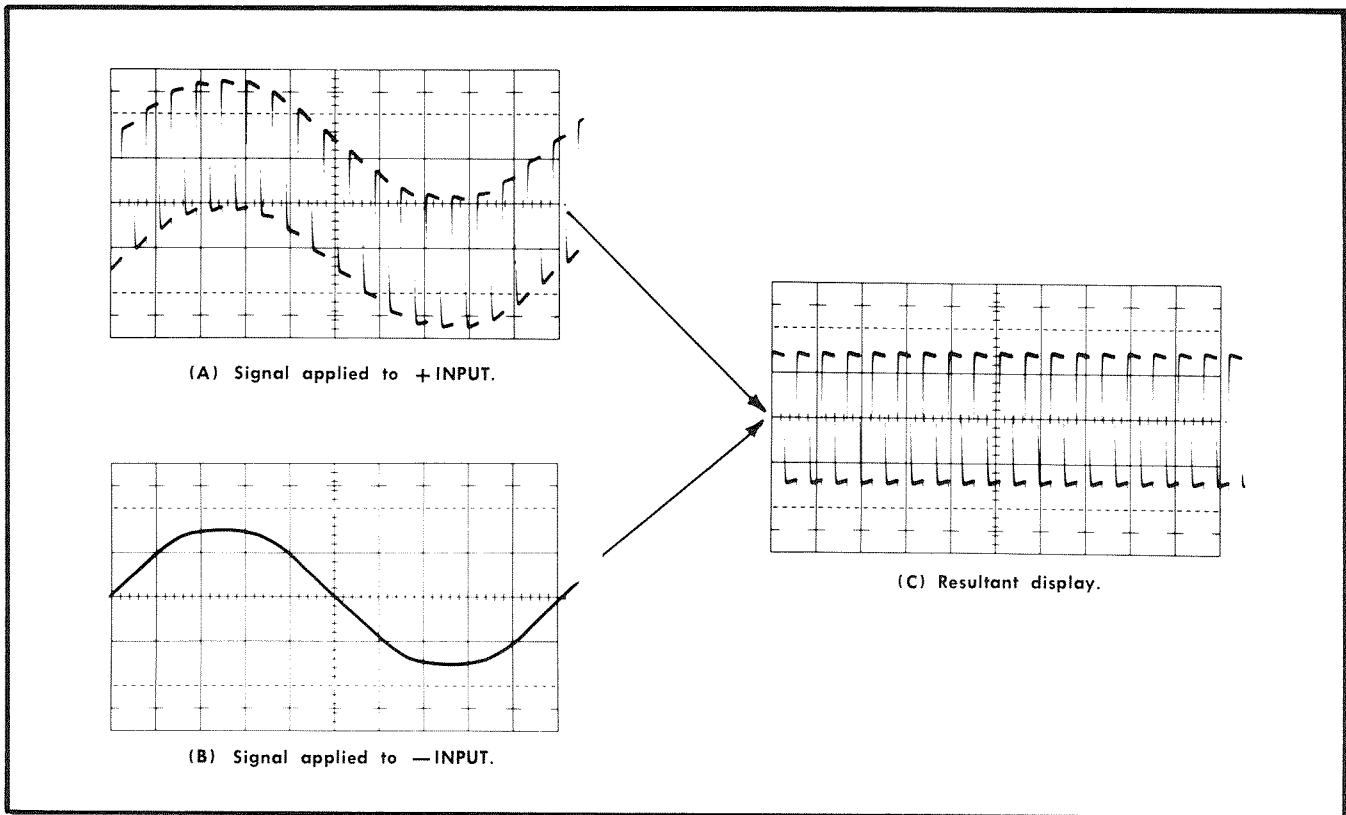


Fig. 2-8. Waveforms showing differential rejection of a common-mode signal. Resultant waveform (c) shows the difference between the two signals.

DC Offset Operation

By using the DC OFFSET controls, it is possible to use the Type 7A22 differentially in a slide-back mode, to observe small signal whose DC potential difference may be considerable. The offset is continuously adjustable from +1 V to -1 V when the VOLTS/DIV switch is in the 10 μ V to 10 mV positions. In the 20 mV, 50 mV and .1 V positions of the VOLTS/DIV switch, the 1 V offset is effectively multiplied by the input attenuator to a range of ± 10 V. Table 2-1 summarizes the effective DC offset voltages internally available for all the VOLTS/DIV switch positions. The table also lists the input attenuator that is switched into the amplifier circuit for the various VOLTS/DIV switch positions.

TABLE 2-1

VOLTS/DIV setting	OFFSET RANGE	ATTENUATOR in
10 μ V to 10 mV	± 1 V	1 \times
20 mV to 0.1 V	± 10 V	10 \times
0.2 V to 1 V	± 100 V	100 \times
2 V to 10 V	± 1000 V ²	1000 \times

²CAUTION— ± 500 volts is the maximum allowable signal voltage of each input.

Using the DC OFFSET functions:

Set the Type 7A22 controls as follows:

VOLTS/DIV	10 V
VARIABLE (CAL IN)	CAL
POSITION	Midrange
AC-GND-DC (+ and - INPUT)	GND
HF -3 dB POINT	1 MHz
LF -3 dB POINT	DC
STEP ATTEN DC BAL	Adjusted for DC balance

1. Position the trace to graticule center (or some other convenient reference line) using the POSITION control.

2. Connect a coaxial cable from the signal source to the +INPUT.

3. Set the +INPUT AC-GND-DC switch to DC and measure the DC level to be offset.

4. Set the VOLTS/DIV to the largest deflection factor in an offset range which will encompass the DC level measured in step 3. See Table 2-1 and front panel color bands.

5. Set the LF -3 dB POINT selector to DC OFFSET.

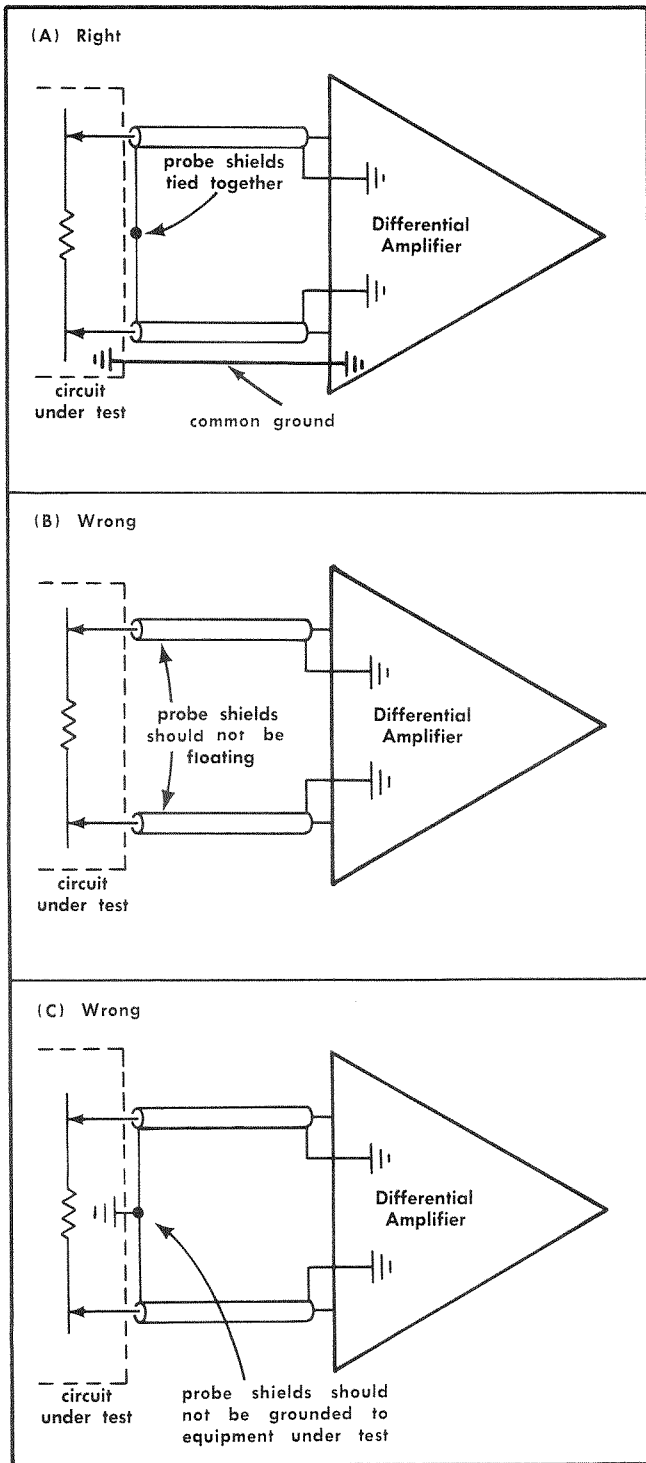


Fig. 2-9. Connecting a differential amplifier across a circuit.

6. Use the COARSE and FINE controls to move the portion of the signal to be observed to the reference line established in step 1.

7. If a different size display is needed, the deflection factor may be changed in the same offset range.

NOTE

If switching into another offset range, the OFFSET controls will need to be readjusted. If switching into a smaller offset range, check that the available range is sufficient to encompass the DC level present (see steps 3 and 4 above).

Input Overdrive Indicator

The INPUT OVERDRIVE indicator turns on when the signal to the input FETs approaches the differential dynamic range of the amplifier. The 7A22 should not be left connected to a circuit if this light is on, as this may mean that a damaging voltage is present.

The INPUT OVERDRIVE indicator serves another important function. If the amplifier is direct-coupled at the input, a DC differential signal could overdrive the input stage and cause a reduction in gain. The small voltages to be measured will not be distorted, but will be reduced in amplitude. As a result, amplitude measurements made under such conditions will not be accurate. The Type 7A22 INPUT OVERDRIVE indicator provides an indication that such a signal is present by lighting before the gain calibration changes by 1%.

If the INPUT OVERDRIVE lamp turns on, there are two possible ways to continue:

1. Switch the LF -3 dB POINT selector to DC OFFSET. DC differential signals may then be balanced out as indicated in Table 2-1.
2. Switch the AC-GND-DC switch to AC. DC differential signals up to 1000 V (either input not to exceed 500 V) may be removed by using AC coupling at the input. This necessarily limits the low frequency response to 1.6 Hz (or 0.16 Hz with a 10X probe).

The INPUT OVERDRIVE lamp is insensitive to common-mode overdrives, and it is possible to overload the Type 7A22 without lighting the input overdrive light.

In summary the overdrive indicator will turn on under the following conditions:

- a. The input signal exceeds the differential dynamic range of the amplifier (see Section 1 for table of dynamic ranges).
- b. An input protective fuse is blown. In this case, the light will remain on even if the AC-GND-DC switches are set to GND.
- c. There is a circuit malfunction.

Readout

If the Type 7A22 is to be used in an oscilloscope having readout capabilities, special probes which correct the readout deflection factor for probe attenuation may be used. Divider probes not having the sensing capability may be used with the instrument, but they will not operate the sensing system, and the deflection factor of the plug-in only (as read on the VOLTS/DIV knob) will be displayed.

The +INPUT and -INPUT connectors have an outer ring which is connected to the readout probe sensing device. This allows the main-frame readout to display the correct deflection factor from the probe tip for any probe attenuation. For

example: if a 10 \times probe is used it will increase the deflection factor, in the readout display, by a factor of 10. Then the actual deflection factor at the probe tip is displayed (see Table 2-2).

TABLE 2-2

TYPE 7A22 Plug In	Main-Frame Displayed Readout			
	VOLTS/DIV Setting	Probe Atten used 1 \times	Probe Atten used 10 \times	Probe Atten used 100 \times
.5 mV	500 μ V	5 mV	50 mV	
50 mV	50 mV	500 mV	5 V	

Trace Identify. With the oscilloscope turned on and a sweep displayed on the CRT, check for approximately 0.2 div of vertical movement of the trace when the IDENTIFY pushbutton is depressed. The vertical scale factor readout associated with the 7A22 will change to the word IDENTIFY.

NOTE

If only one divider probe (or two probes with equal divider ratios) is connected, the deflection factor at the probe tip will be displayed; if probes with different divider ratios are connected (e.g., 10 \times and 100 \times), the readout will display the deflection factor at the tip of the probe with the larger division ratio (100 \times).

NOTE

An incorrect CRT readout will occur in the 10V/div position when using the 100X Readout coded probe (P6009, Tektronix Part No. 010-0264-01). When used in 10V/div setting, reading will show 1V instead of 1KV.

NOTES

Series of horizontal lines for writing notes.

SECTION 3

CIRCUIT DESCRIPTION

Change information, if any, affecting this section will be found at the rear of the manual.

Introduction

A block diagram description covering the general configuration of each circuit in the Type 7A22 is included in this section. Following the block diagram description is a detailed description of each circuit and the functions of specific components.

Simplified drawings are provided where necessary for easier circuit understanding. Complete schematic diagrams are included in the Diagrams section. These should be referred to throughout the detailed circuit description.

The values of resistors on the schematics are in ohms unless otherwise specified. Capacitor values are indicated in the following manner unless otherwise specified: whole numbers indicate that the value is in pF, decimal numbers indicate that the value is in μF . For example, 33 pF and 0.1 μF .

BLOCK DIAGRAM DESCRIPTION (see Block Diagram Pullout preceding schematics)

Input Coupling

A signal applied to the + or —Input connector passes through the input coupling selector switch to the input attenuator circuit. The signals can be AC coupled, DC coupled or disconnected internally. (See Schematic Diagram Number 1).

Input Attenuators

The input attenuators for the + and —inputs are identical and are conventional RC type attenuators. Common resistive elements are adjustable to facilitate matching the — and + attenuators to obtain optimum DC common-mode signal rejection.

The attenuators (Schematic Diagram number 3) are frequency-compensated voltage dividers which provide constant attenuation at all frequencies within the passband of the instrument, while maintaining a constant input time constant (47 μs) for all positions of the VOLTS/DIV switch.

Each attenuator contains an adjustable capacitor to provide correct attenuation at high frequencies, and adjustable shunt capacitance to provide correct input capacitance. The attenuators are used, in conjunction with gain switching in the output amplifier, only in the 20 mV/DIV through 10 V/DIV switch positions. From 10 μV /DIV through 10 mV/DIV, only the output amplifier gain switching is used to set the deflection factor.

Preamp

From the input attenuators, the signal is coupled to the preamp. The preamp consists of two identical feedback amplifiers connected in a differential configuration. The overall differential gain is approximately 15.

The supply voltages for the two amplifiers are obtained from a common power supply which is bootstrapped to the input to improve the common-mode rejection ratio of the preamp.

Each input is equipped with an overdrive protection circuit consisting of fuses and clamping diodes. For deflection factors from 10 μV /DIV through 10 mV/DIV the fuse will open if the current exceeds $\frac{1}{16}$ A (approximately 16.5 volts at the input), preventing damage to the input circuitry.

CAUTION

The input protection circuit clamps the input to a fixed voltage when too large an input signal is applied. In the 10 μV to 10 mV positions there is no resistance through the ATTENUATOR switch. There is a possibility of damage to the signal source, since a very large current surge can flow before the fuse opens.

When the LOW FREQ —3 dB POINT selector is in any position other than DC or DC OFFSET and the Input Coupling switch is in DC, there is no on-screen indication of the DC conditions in the preamp. If the differential dynamic range of the amplifier is exceeded and the amplifier is driven into non-linearity or overdrive, an erroneous display is likely.

An overdrive detector circuit is provided to indicate when the preamp is approaching the limits of its differential dynamic range. A front-panel indicating lamp lights when overdrive occurs.

An offset generator is provided to balance out any current in the preamp resulting from signals containing differential components. Offset (variable) allows the varying component to be amplified, and at the same time maintains the amplifier differential capabilities.

Low Frequency —3 dB Point

The push-pull output of the preamp is coupled through a LOW FREQ —3 dB POINT selector. The selector switches the components of the coupling network in each half of the preamp to select the low frequency —3 dB points (.1 Hz, 1 Hz, 10 Hz, 100 Hz, 1 kHz and 10 kHz). The DC position of the selector bypasses the low frequency selection circuitry and direct-couples the preamp to the Output Amplifier.

Output Amplifier

The signal from the LF -3 dB POINT selector is coupled to the gain-switching section of the Output Amplifier. This section of the Output Amplifier is a pair of feedback amplifiers similar to that of the preamp. The VOLTS/DIV switch changes the amplifier gain.

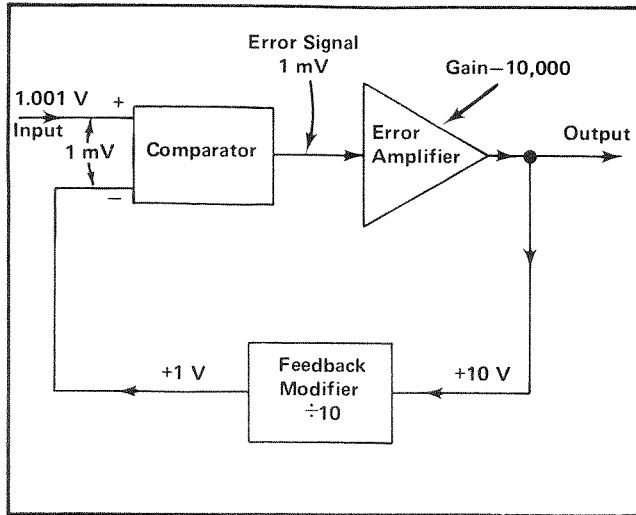


Fig. 3-1. Generalized feedback system showing the relation between input and output.

High Frequency -3 dB Point

The HF -3 dB POINT selector switches capacitance across the output of the Gain Switching Amplifier to set the high frequency -3 dB point at any of 9 frequencies; 100 Hz, 300 Hz, 1 kHz, 3 kHz, 10 kHz, 30 kHz, 100 kHz, 300 kHz, and 1 MHz. The capacitance used in the 1 MHz position is adjustable to set the bandwidth to normal value.

Positioning of the trace, and VARIABLE VOLTS/DIV, are provided in the stage following the HF -3 dB POINT selector.

Overall amplifier gain is adjusted in the signal output stage of the Output Amplifier by adjusting the common emitter resistance.

GENERAL INFORMATION

Feedback Amplifiers

Since the Type 7A22 utilizes several multi-stage feedback amplifiers, a brief review of feedback systems in general is given.

Fig. 3-1 represents a generalized feedback system in which it is desired to produce an output signal accurately and stably related to the input. The arrangement of Fig. 3-1 causes the modified output to be nearly equal to the input. Any difference between these is detected by the comparator, which produces an error signal equal to the difference, and applies this error to the amplifier, which amplifies the error and feeds back a correction to reduce the original error.

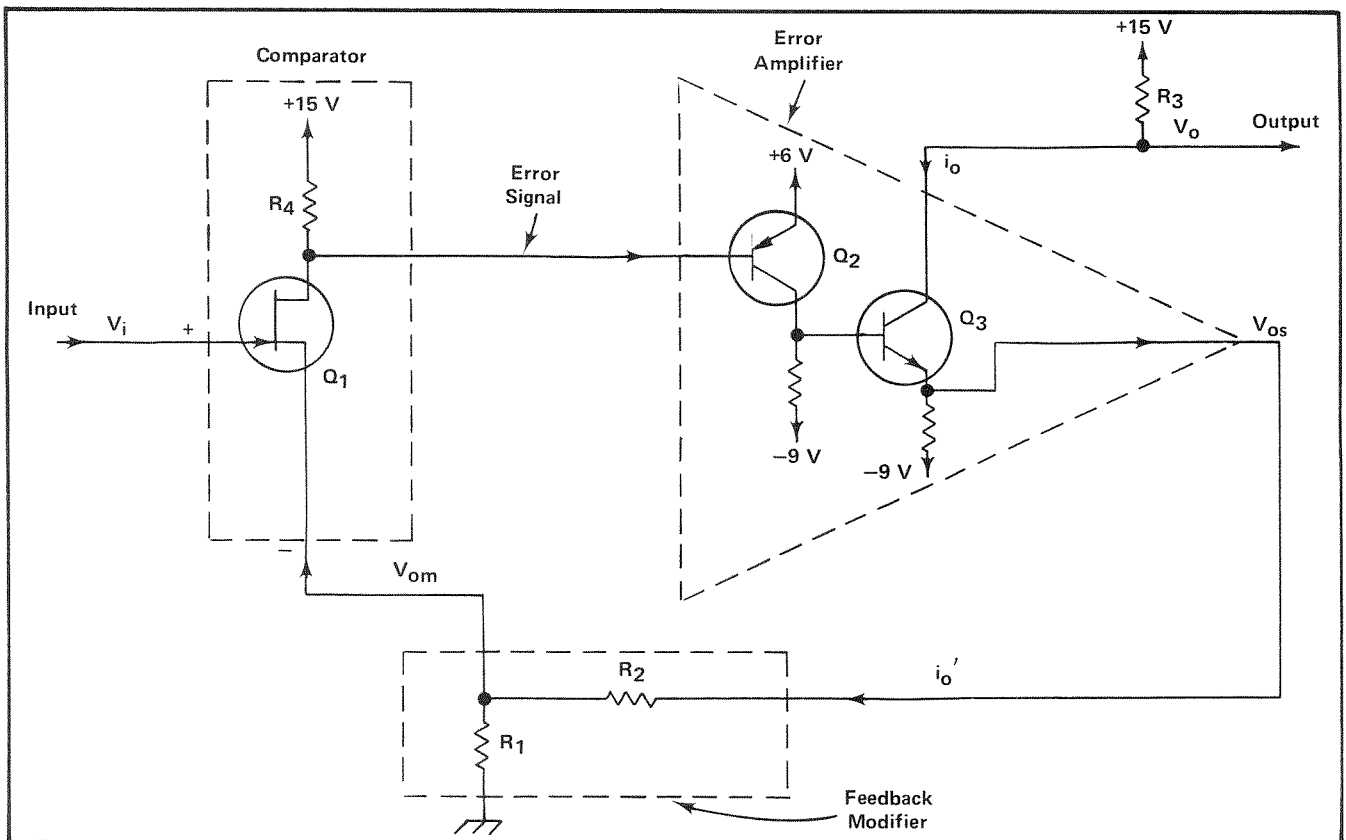


Fig. 3-2. Feedback amplifier (Single-ended version).

The input to the modifier (the system output) is also accurately related to the system input, provided the modifier is constructed of stable components.

Another way of looking at the system is to start at the output and work backward. Specifically, assume an amplifier gain of 10,000 and a feedback modifier which is a 10× divider. Assume a 10 volt output. The modifier output is 1 volt, and the error signal (output ÷ gain) is 1 mV, so that the input is 1.001 volts. In this case, the error between the desired output (10× input) of 10.01 volts and the actual output of 10.00 volts is only 10 mV, or 1 part in 1,000.

In practice the comparator and error amplifier are often combined in a differential amplifier. A single-ended version of the basic configuration used in the Type 7A22 is illustrated in Fig. 3-2, with the basic blocks of Fig. 3-1 identified. The comparator is FET Q₁. Any change in the gate-to-source bias voltage (dictated by the standing current established by R₁ and the supply voltages) will cause a change in drain current, the change being applied as an error signal to the input of the error amplifier.

The error amplifier consists of grounded emitter stage Q₂ driving emitter follower Q₃. The internal output appears at the emitter of Q₃ and is fed back to the comparator input via modifier (voltage divider) R₂, R₁. For this amplifier, the system output, V_{os}, can be determined by:

$$V_{os} = \left(1 + \frac{R_2}{R_1}\right) V_{om}$$

Since V_{om} is approximately equal to the input voltage V_i, then the system gain, $\frac{V_i}{V_{os}}$, is approximately equal to $1 + \frac{R_2}{R_1}$.

The useful output of the amplifier is the Q₃ collector signal current i_o' which flows through R₁ (in addition to the relatively small error current from Q₁). V_{om} = i_oR₁ and since V_{om} is approximately equal to V_i, and i_o is approximately equal to i_o' then i_o is approximately equal to $\frac{V_i}{R_1}$. Thus the output current vs. the input voltage depends primarily on the gain-setting resistor, R₁.

An output voltage can also be obtained by passing i_o through the load resistor, R₃. The overall voltage gain is then $\frac{V_o}{V_i}$, which is approximately equal to $\frac{R_3}{R_1}$.

Differential Configuration

If the lower end of R₁ is connected to the same point in another identical circuit instead of being returned to ground, the result is a differential feedback amplifier with push-pull output, which is the configuration in the Type 7A22. A differential feedback amplifier, such as is used in the 15× preamp, is shown in Fig. 3-3.

From the previous description, the Q_{1a} and Q_{1b} source voltages V_{om} (a and b) follow the input voltages V_{ia} and V_{ib} respectively, hence any differential input voltage, V_i will result in a nearly equal source to source voltage V_{omr}, which in turn is due to an output signal current i_o' = $\frac{V_{om}}{R_{1a} + R_{1b}}$.

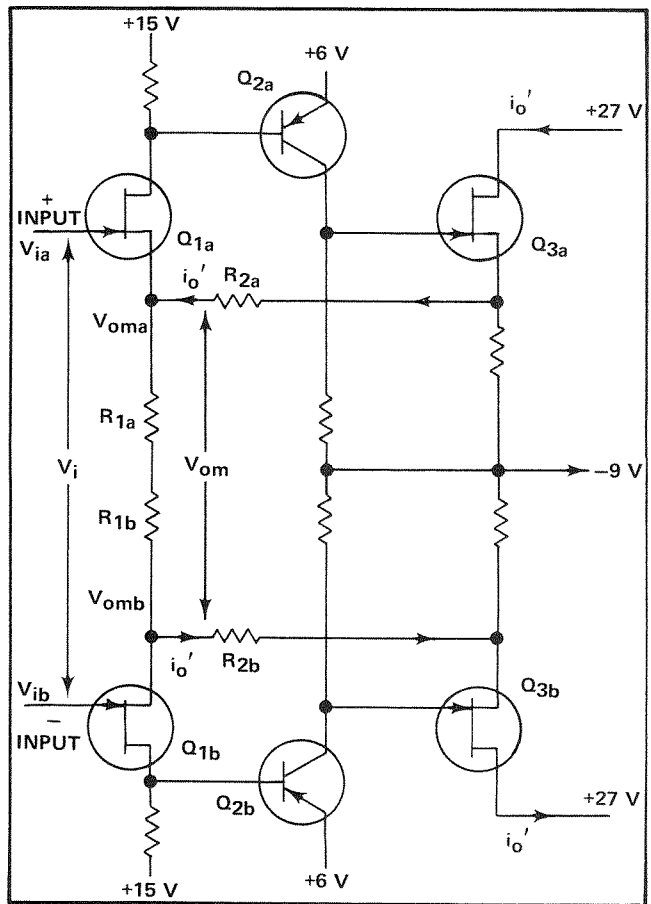


Fig. 3-3. Feedback amplifier (Differential configuration).

$$= \frac{V_i}{R_{1a} + R_{1b}}$$

Note that FETs have been used in Q₃, rather than NPN bipolar as in Fig. 3-2 to avoid loss of signal current from the base lead. The operation of the amplifier remains unchanged.

DETAILED CIRCUIT DESCRIPTION

Input Coupling (See Schematic 1)

Input signals applied to the +input connector can be AC coupled or internally disconnected. When the input coupling switch, S101 (see ×15 preamp diagram), is in the DC position, the input signal is coupled directly to the input attenuator. In the AC position, the AC signal is coupled through coupling capacitor C101, and the DC component is blocked from the input amplifier. The GND position internally connects the gate of the input amplifier to ground. This provides a ground reference for the amplifier without removing the input leads or otherwise disconnecting the input signal.

Resistor R103 allows C101 to be precharged in the GND position so that when S101 is switched to the AC position with a high DC level applied, there is no charging current surge into the input of the amplifier. Excessive loading is also avoided for the circuit under test, since the normal im-

Circuit Description—Type 7A22

pedance of 1 M Ω -47 pF is still seen by the signal source. The —input switch, S201, functions in the same manner as the +input.

Input Attenuators (See Schematic 3)

To produce the vertical deflection factor indicated on the front panel by the VOLTS/DIV switch, the gain of the feedback amplifier in the gain switched amplifier circuit is changed by switching the source resistor (R407) of Q404 A and B (See Schematic 2) for switch positions 10 μ V to 10 mV. For switch positions above 10 mV, input attenuators are switched by S108 into the input circuit of the Type 7A22, in conjunction with the gain switching resistors, R407, to produce the selected deflection factors.

These attenuators are frequency-compensated voltage dividers. For DC and low frequency signals, they are primarily resistive dividers (e.g., R108C, R108D) and the voltage attenuation is determined by the resistance ratio in the circuit. The reactance of the capacitors in the circuit is so high at the lower frequencies that their effect is negligible. However, at higher frequencies, the reactance of the capacitors decreases and the attenuator becomes primarily a capacitive voltage divider (e.g., C108C, C108D).

In addition to providing constant attenuation at all frequencies within the bandwidth of the instrument, the input attenuators are designed to maintain the same input RC characteristics (1 megohm in parallel with 47 pF) for each setting of the VOLTS/DIV switch. Each attenuator contains an adjustable capacitor (e.g. C108C) to provide correct attenuation at high frequencies, and an adjustable shunt capacitor to provide correct input capacitance.

Gate Current Compensation

The leakage current associated with the gates of the input FETs may be as high as 100 pA. This 100 pA of leakage current (through 1 megohm to ground, R111 or R211) will produce an offset of 100 μ V, which at high input sensitivities is not acceptable. To compensate this effect, the gates of the input FETs may be adjusted to zero volts by returning R111, R211 through variable controls R115 and R215 to a slightly negative supply voltage (see Fig. 3-4).

Leakage current associated with the gates of the input FETs and the overdrive protection diodes increases rapidly with temperature, approximately doubling for every 10°C. To compensate this increase, a temperature sensitive input current balancing network is included, using thermistors as the sensing elements.

As the voltage across R111 and R211 increases due to increasing FET gate current at increased temperatures, an equal voltage change is produced in the thermistor compensating circuit, maintaining the FET gate level at zero volts.

The gate current compensation becomes inoperative if the straps are removed for high input impedance operation.

Input Amplifier

The input amplifier circuit in the Type 7A22 is made up of two identical feedback amplifiers connected in a differ-

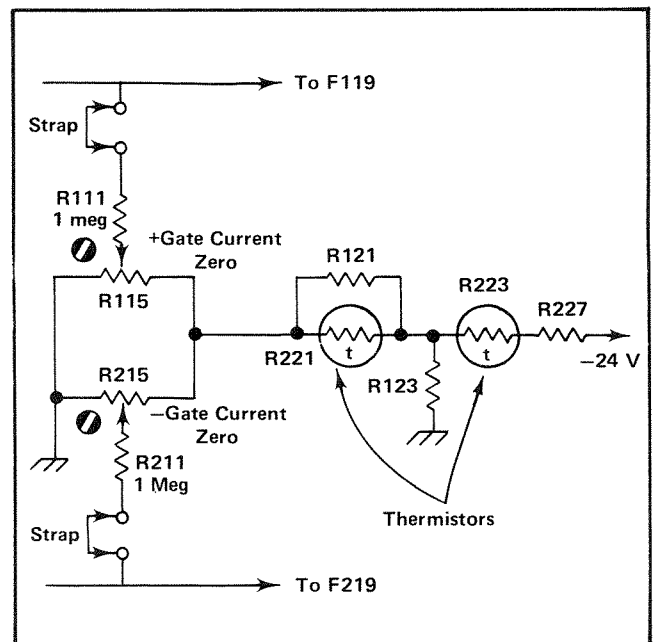


Fig. 3-4. Gate Current Compensation.

ential configuration with a push-pull output. The power supply voltages (except at the output) for each feedback amplifier are obtained from a power supply that is bootstrapped to the common-mode input signal. This improves the CMRR of the amplifier. Input overdrive protection is provided in the input circuit to prevent damage to the semiconductors if a large overdrive is inadvertently applied to the input.

Any amplifier of this type, in which the feedback is taken to the input element, is likely to exhibit a negative resistance component of impedance at higher frequencies. If the input signal source impedance is inductive with a sufficiently high "Q", the amplifier could oscillate. C115, R116, R117 and C215, R217 compensate for this negative input resistance.

×15 Preamp. The feedback amplifiers in the + and — inputs are identical except for circuit numbers, and operate in a coupled differential mode as described previously under feedback amplifiers.

In the actual circuit (see Schematic 1) the + feedback amplifier consists of the comparator Q133A, error amplifier Q144A, Q153, feedback modifier R151, R153 and output load resistor R159. R151 is the gain setting resistor for the amplifier, and the approximate gain of the amplifier can be determined by dividing R159 by R151, $(\frac{R_o}{R_i})$.

Diode CR131 is used for temperature compensation of the base-emitter junctions of Q144A and Q144B. This keeps the total voltage across the input stage current-setting resistors R133, R233 constant with temperature. Diode CR144 connected between base-emitter of Q144A, protects the transistor against reverse base-emitter breakdown. C144 and C244 stabilize the circuit at the higher frequencies.

DC Balance. The DC level at the output of Q153, Q253 is balanced by R258, STEP ATTEN DC BAL. The STEP ATTEN

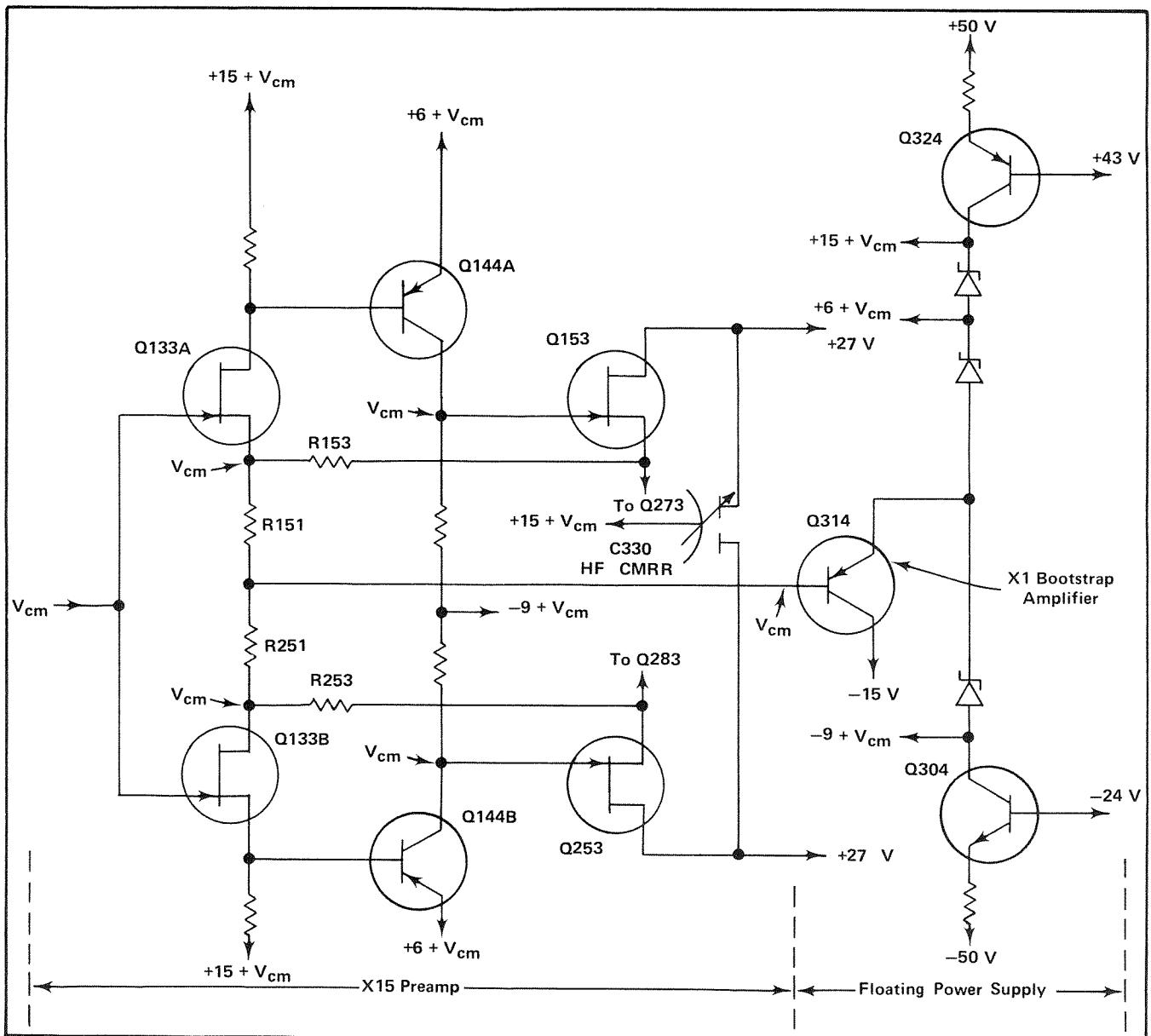


Fig 3-5. Common-mode Rejection using floating power supply.

DC BAL control changes the current through R257, R259, thereby changing the DC balance. It is used to adjust the difference in potential across the output of the $\times 15$ Preamp (pins Z and W) to zero with the input coupling switches at GND and the LOW FREQ -3 dB POINT switch set to DC.

With high frequency common-mode signals the wiring stray capacitance of the $\times 15$ preamp can inject undesirable current into the two output lines at high frequencies. C330 is adjusted to equalize these currents, thus extending the frequency range over which useful CMRR can be obtained.

Floating Power Supply. The supply voltages for the $\times 15$ preamp are obtained from a chain of Zener diode shunt regulators, VR305, VR320 and VR325 connected in series. Current is supplied by two current sources, Q304 and Q324.

C307, C317 and C325 filter out the Zener noise. C315 increases the bootstrap stability at higher frequencies.

Any common-mode changes that occur in the input amplifier, except at the output, are coupled to the power supply through Q314. Q314 is an emitter follower whose gain is maintained very close to unity by the minimum loading presented to its output by the high collector impedance of Q304 and Q324, thus achieving good bootstrap efficiency.

Common Mode Rejection. One of the primary functions of the $\times 15$ preamp is to reject any common-mode component in the input signals, and amplify only the difference. In the extreme case of the inputs tied together and a common voltage applied, the output of the amplifier is ideally zero, and would actually be zero provided that the characteristics

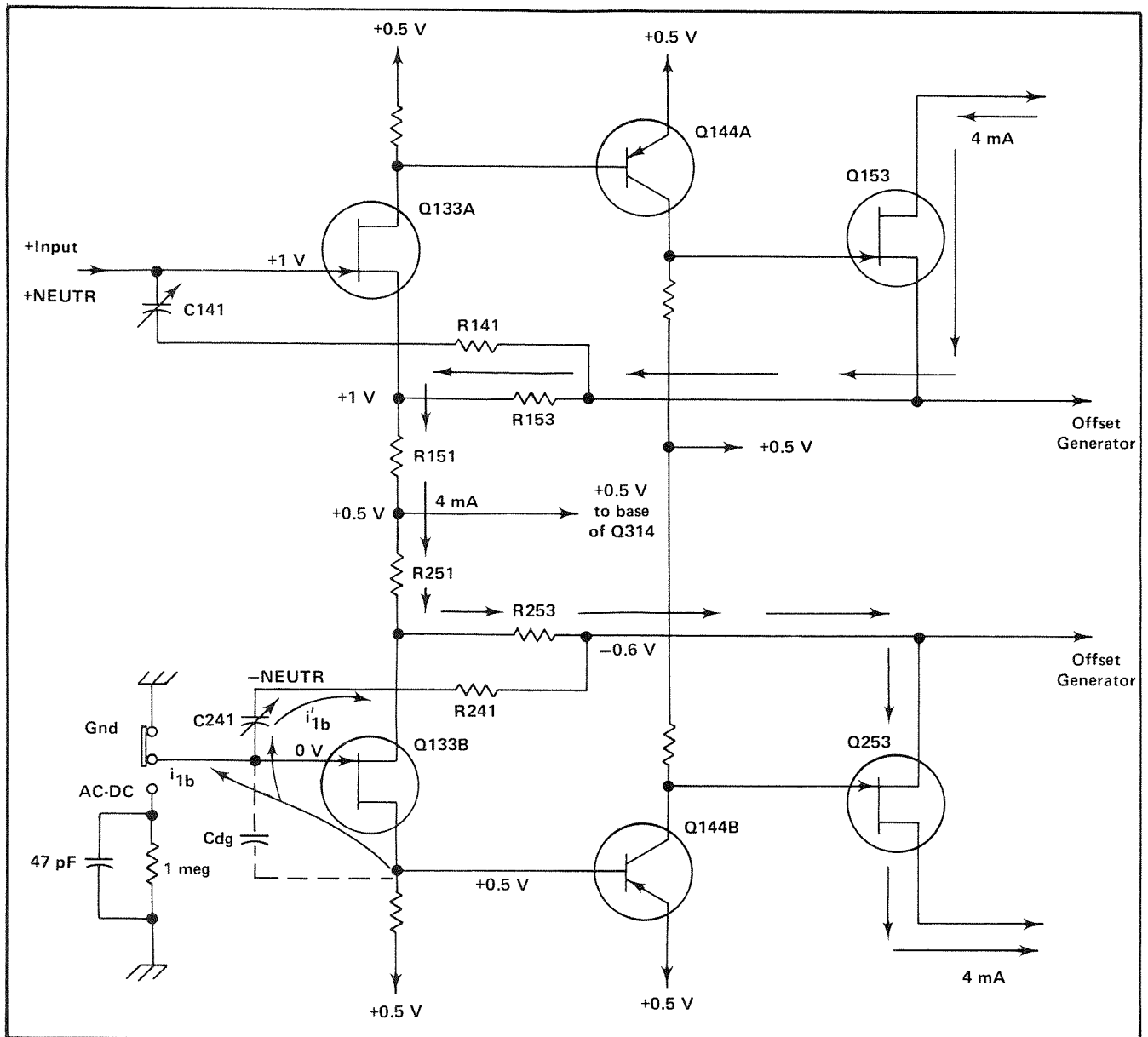


Fig. 3-6. Input cross neutralization.

of all corresponding elements on the two sides of the amplifier (see Fig. 3-5) were exactly matched. In practice any mismatch will cause a differential output. Even with perfect matching, there is still a common-mode output current resulting in an undesirable common-mode signal applied to the subsequent stages of the amplifier (common mode gain).

The floating power supply eliminates these difficulties and improve the CMR. Suppose the input to the bootstrap amplifier is connected to the junction of R151 and R251 (see Fig. 3-5). It can be seen that now the entire power supply and amplifier moves an amount equal to V_{cm} (ie: follows V_{cm}) and that no changes in voltage or current levels occur anywhere within the amplifier as a result of V_{cm} , except for Q153 and Q253 drain to gate voltage. Thus the only mismatch of importance is that of Q153, Q253 amplification

factors, and being in the third stage of the amplifier at a point of relatively large differential signal level, this causes only a small degradation in CMR.

At higher frequencies the inevitable stray capacitances from various points in the $\times 15$ preamp to ground begin to inject significant current into the amplifier as a result of common mode signals. The differential capacitor C330, (Fig. 3-5 or Schematic \diamond) connected from a point in the floating power supply to the two output lines, is used to inject adjustable currents into the output. It can be adjusted to equalize the net output currents resulting from high frequency common mode signals and so extend the range of frequencies over which useful CMRR can be obtained.

Cross Neutralization. The use of a common bootstrap power supply results in an undesirable capacitive coupling

between the two inputs. Consider the effect of applying +1 volt to the +input while keeping the —input at 0 volts (see Fig. 3-6).

The results are (a) an output current of 4 mA, as shown, and (b) a shift of all supply voltages and several other voltage levels by +0.5 V due to the divider action of R151, R251 operating into the bootstrapped power supply system. Specifically, the drain of Q133B also rises +0.5 V and injects a current i_{1b} through the drain to gate capacitance of Q133B and into the —input. If there is any impedance between the —input and ground, i_{1b} will develop a voltage across it which, being applied to the —input subtracts from the original +input and causes an erroneous output (see Fig. 3-7).

Note that the output current flowing through R253 causes its output end (Q253 source) to go to —0.6 V. A capacitor, C241, connected from this point to the —input can be adjusted to divert i_{1b} away from the input line (i_{1b}) and so neutralize the effect of C_{dg} and reduce the —input current to zero. R141 and C141 perform a similar function for the +input.

Input Overdrive Protection. Since the input FETs Q133A and B, being semiconductors can be destroyed by sufficiently large overdrive applied directly to them, some input protection is a necessity. The important components of the protection system used in the Type 7A22 are shown in Fig. 3-8A.

There are two different kinds of overdrive associated with the Type 7A22, (a) single-ended overdrive, with one input grounded, and (b) common-mode overdrive, with approximately equal input voltages.

(a) Assume the —input grounded and a steadily increasing voltage on the +input. The chart in Fig. 3-8B indicates the sequence of events: F119 will open if the + signal source can supply 1/16 amp of current. If this much current is not present, the +input remains at +16.5 V, with the overdrive current flowing to ground as shown in Fig. 3-8.

Now assume the +input grounded and a steadily increasing —voltage on the —input. The sequence of events is shown in Fig. 3-8C.

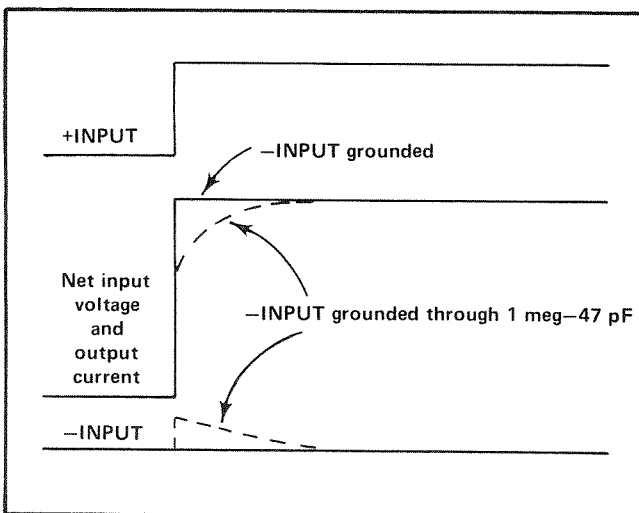


Fig. 3-7. Effects of high impedance to ground in the —INPUT.

(b) For common-mode overdrives the clamping sequence is essentially similar, except the $\times 15$ amplifier does not overload, and the current i_2 does not flow until the input reaches approximately —13 V. The fuse opening current paths are shown in Fig. 3-8A.

For differential overdrive the action is a combination of the + and — single-ended sequences, with the —input supplying i_2 . (In effect a 5 k Ω resistor is connected across the inputs when the differential voltage reaches 3 V). The fuses will not open until one or both inputs reach +16.5 V or —16.5 V.

DC Offset. The purpose of the DC offset system is to allow a differential slideback measurement, i.e.: to buck out small DC components of input signals and allow the amplifier to amplify only the varying components, while keeping the differential capability. This means that when a DC voltage is applied across the inputs, some means must be found to balance out the resultant output current. Fig. 3-9 illustrates how this is done.

Fig. 3-9A shows the standing currents for zero input. Fig. 3-9B shows the currents with a 0.25 V DC input applied to the +input. This would cause an i_o' of 1 mA to flow through R151, R251. However, if this 1 mA is supplied as shown by the offset generator, then no net output current results. Both the Q133A and Q133B source currents and the output standing currents, i_o' , remain at their zero signal value, 2 mA and 5 mA respectively.

Offset Generator. The offset generator produces the adjustable balanced offset current for use in the $\times 15$ preamp. Due to the wide range of the offset system (200,000 div at 10 μ V per div) stable components are used and circuit techniques which minimize drift and noise are employed in the offset generator. A 10 turn coarse and 1 turn fine OFFSET control is used to obtain adequate resolution at low deflection factors.

The functional arrangement of the offset generator is shown in Fig. 3-10. In the reference voltage generator the OFFSET control, R270, taps an adjustable portion (V_{1b}) of the voltage across the reference Zener VR270, and a fixed voltage (V_{1a}) is taken from the voltage divider formed by R271, R279 (approximately 50% of the reference voltage); thus, the difference voltage ($V_{1b} - V_{1a}$) is adjustable over the range of at least —4 V to +4 V.

This adjustable difference voltage is applied to the inputs of a balanced feedback amplifier conceptually similar to that described previously under differential configuration. In amplifier "A", the feedback action forces the —input voltage (also the output voltage) V_{2a} to follow the +input V_{1a} . In a similar manner, V_{2b} follows V_{1b} in amplifier "B". The differential input ($V_{1b} - V_{1a}$) is therefore reproduced across resistors R287, R289. The resultant current, $\frac{V_{2b} - V_{2a}}{R287 + R289}$, which is " i_{offset} ", flows through the FET (Q273, Q283) output stages of the amplifier and out their drains to the $\times 15$ preamp.

When the offset is not in use V_{1b} is switched to the fixed divider (R273, R277) by the OFFSET ON/OFF switch (part of the LF —3 dB POINT switch). V_{1a} is adjustable over a small range with respect to V_{1b} by the COARSE DC BAL control, whose purpose is to adjust out any initial DC unbalance in the $\times 15$ preamp, and to bring its output to zero with zero input.

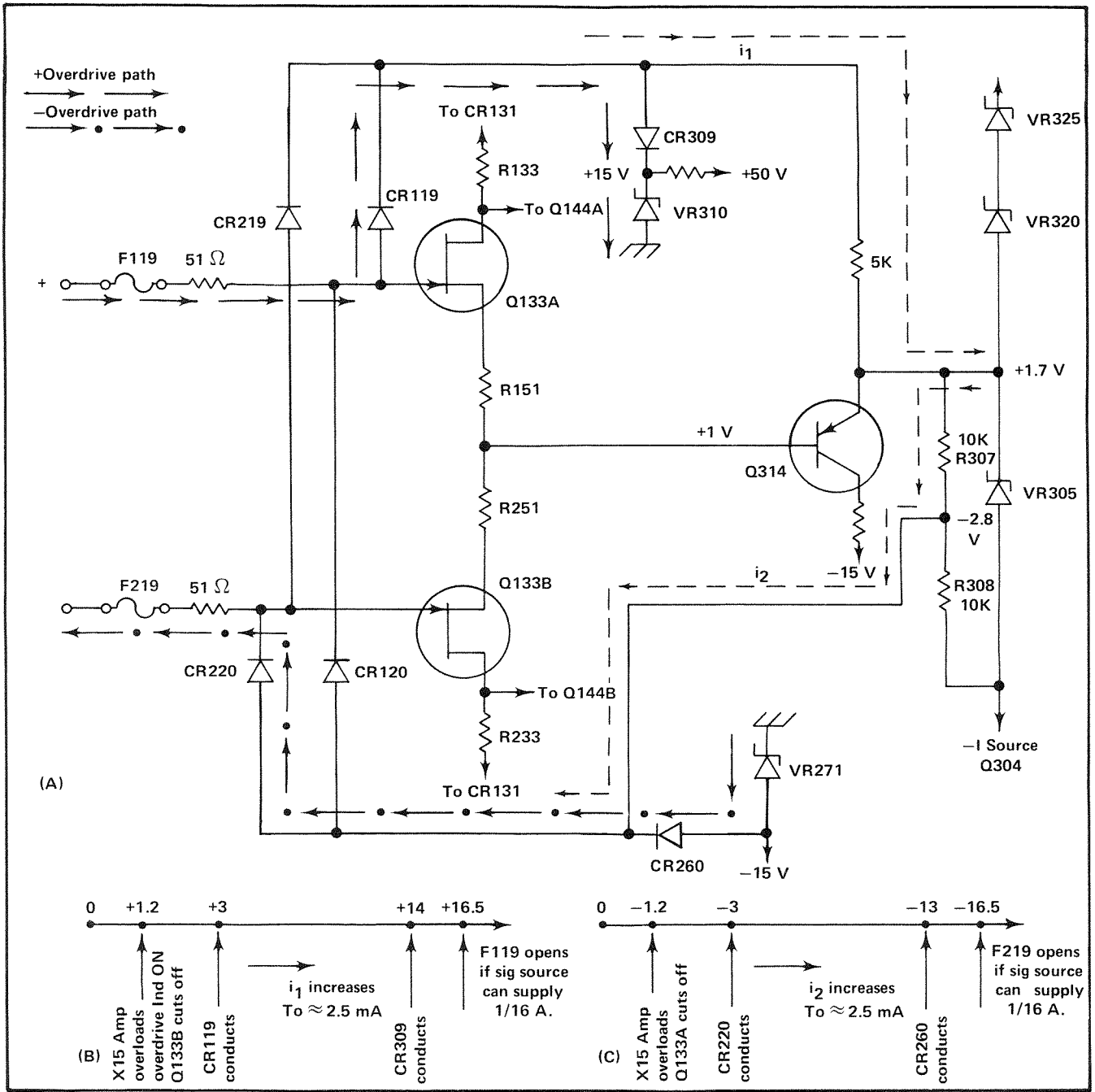


Fig. 3-8. (A) Overdrive Protection System showing current paths. (B, C) Sequence of events leading to excessive overdrive.

In the actual circuit, the reference voltage generator is modified by the addition of a FINE OFFSET control (R265), which changes the effective range of the 10 turn COARSE potentiometer by means of two voltage dividers, one connected to each end of the COARSE control. A capacitor filters out Zener noise from the reference voltage.

The feedback amplifiers A and B are composed of Q264A, Q273 and Q264B, Q283 respectively, with the reference input applied to the emitters of dual transistor Q264 and the feedback to its bases. Current source Q284 supplies operating current for the amplifiers.

Input Overdrive Indicator. When the LF -3 dB POINT selector is in a position other than DC, there is no on-screen indication of the DC conditions in the $\times 15$ preamp, and it may be driven into non-linearity or overload by a DC component, leading to erroneous displays. The input overdrive indicator detects this condition and indicates by means of a warning light that the $\times 15$ preamp is approaching the limits of its dynamic range.

The indicator consists of a threshold detector (CR341, CR343, Q344) and a monostable lamp driver (Q334, Q354). In the quiescent state, while no overdrive exists, CR341,

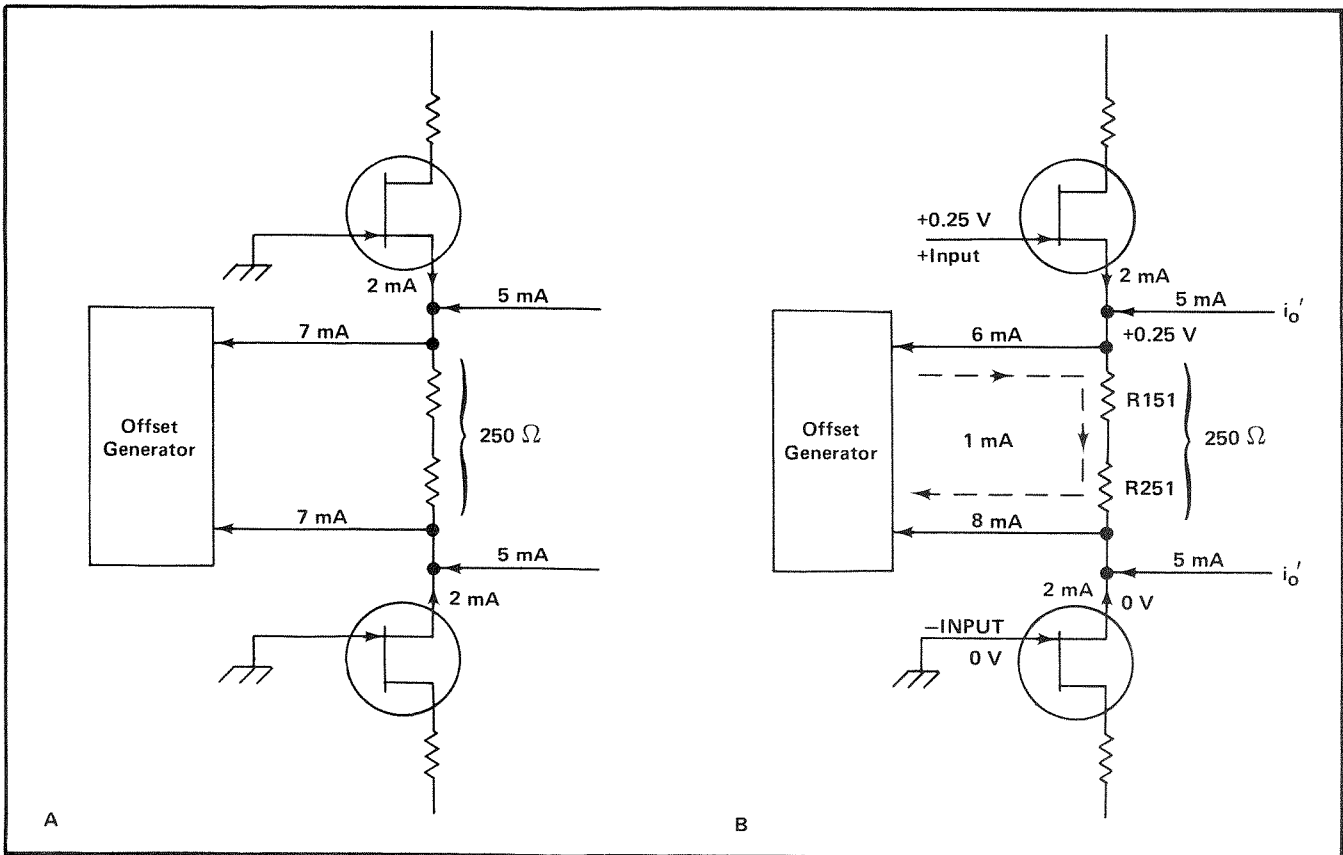


Fig. 3-9. Offset system showing (A) Conditions with zero offset, (B) 0.25 V DC offset.

CR343, Q344, Q334 and Q354 are all cut off. R341 and R343 form a voltage divider to set the emitter of the threshold detector, Q344, at approximately +32 V.

Now suppose one of the $\times 15$ preamp output lines exceeds +33.4 V; CR341 (or CR343) and Q344 collector current turns on Q334 via R347. Q334 collector current turns on Q354 via R333, and the resulting negative going step at Q354 collector turns on the lamp. This causes current in R349 to turn Q334 on harder. If the overdrive is removed immediately, Q344 turns off. However, the current through C349, R349 keeps Q334, Q354 and the lamp on until C349 is charged to its new voltage (approximately 1 second) and the current through R337 exceeds that in R349, R337. This keeps Q334 off for approximately 1 second, even if Q344 receives another overdrive signal. Thus for repetitive overdrives, (up to about 10 kHz), the monostable free-runs and the lamp flashes at approximately a 2 second rate. (Above 10 kHz the high speed AC effectively charges up the stray capacitance and makes the circuit act as if it had a DC overload.) If, however, a DC overload keeps Q344 on, the end of C349 discharge does not turn Q334 off since it is kept on by current through R347. Thus, the lamp stays on until the overdrive is removed.

LF -3 dB POINT Selector. This switch selects the low frequency -3 dB point of the amplifier and has a range of 0.1 Hz to 10 kHz in decade steps. Selection is done by switching the resistor and capacitor of a pair of capacitor couplings in each leg of the amplifier. Fig. 3-11 shows half of the selector, with the switch split up into its functional elements.

For the 100 Hz to 10 kHz positions, C156 is used and resistors R353C, R353B and R353A are switched to the output in the following combinations:

-3 dB frequency	100 Hz	1 kHz	10 kHz
output resistors	R353C	R353 & R353B	R353C & R353A

Whenever R353A or R353B is not on the output side of C156, it is placed across the input, to keep the high frequency load resistance seen by V_{in} constant.

C353A is switched across C156 for the lower 3 ranges, (0.1 Hz, 1 Hz and 10 Hz) and C156 is shorted out for DC coupling. Resistor R157 adds a small increment in gain when C156 is used to compensate for the loss of gain through the capacitive divider formed by C156 and stray capacitance C_s .

When the 0.1 Hz to 10 Hz positions are in use, C353A may be rapidly pre-charged by switching to the 10 Hz position momentarily.

OUTPUT AMPLIFIER (See Schematic 2)

Gain Switching Amplifier

The gain switching amplifier (Q404, Q414, Q424, Q524) is a balanced differential configuration very similar to the $\times 15$ preamp but with a fixed power supply. Gain switching is accomplished by switching R407.

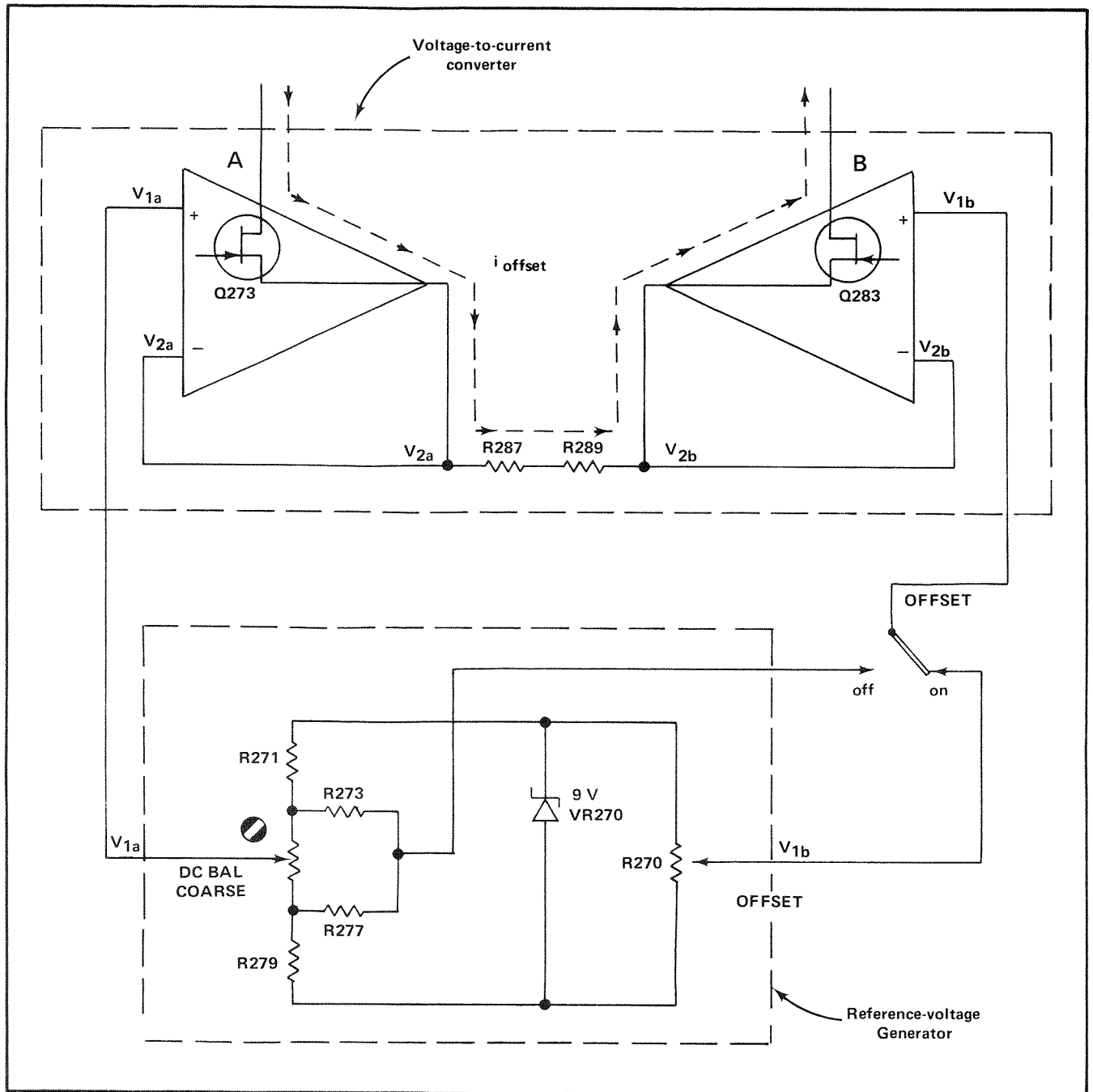


Fig. 3-10. Functional arrangement of the offset generator.

Diodes CR413, CR513 prevent base-emitter reverse breakdown in Q414 under overdrive conditions; CR419, CR519 limit the output current that can flow through Q424, Q524. CR415, CR417 and CR515, CR517 prevent the bases of Q424, Q524 from swinging too far from the emitter, preventing breakdown and ensuring a fast, clean overdrive recovery. The AC STEP ATTEN BAL adjustment (R505) in series with the source of Q404B, develops a small adjustable voltage which removes any initial unbalance in the gate-to-source voltages of Q404 A & B. The AC STEP ATTEN BAL also sets the voltage across

gain-setting resistor R407 to zero when the differential input (gate-to-gate) voltage is zero.

VAR BAL (R425) is used to balance the output currents of Q424 and Q524 with zero input to the amplifier.

The gain switched amplifier has excellent overdrive characteristics so that the full dynamic range of the input amplifier can be used. It is gain-switched over a range of about 1,000:1 with no significant change in bandwidth or any other characteristics.

HF —3 dB POINT Selector. The HF —3 dB POINT selector simply switches different values of capacitance across the output of the gain-switched stage to set the high frequency —3 dB point of the amplifier. The —3 dB point is adjusted in the 1 MHz position by C425.

Variable Output Stage

The variable output stage (Q434, Q534) does not contribute much voltage gain, but it performs a number of other functions. The configuration is a collector loaded, common emitter amplifier with emitter degeneration, the gain (collector signal current vs. input voltage) being determined by the total emitter to emitter resistance. This resistance is adjusted over a 2.5:1 range by the VAR control, R535, which provides a fine control of gain in the uncalibrated position, and interpolates between the steps of the VOLTS/DIV switch.

POSITION control is obtained by feeding adjustable currents into the emitters of Q434, Q534, via resistors R431, R531. This current adds to or subtracts from the signal current developed in the emitter resistors R432, R535, R532, and flows out of the collectors into the signal and trigger output amplifiers.

Signal Output Amplifier

The signal output amplifier (Q444, Q544) is a push-pull common emitter configuration with emitter degeneration to improve the stability and linearity of the circuit. The gain of the stage is determined by the total emitter-to-emitter resistance, which is adjustable by GAIN control (R540) to facilitate calibration of the instrument. The gain of the amplifier is set in the 1 mV position of the VOLTS/DIV switch.

R443, R543 equalize the power in Q444, Q544 under dynamic conditions, thereby eliminating thermal distortion. C443, C543 are high-frequency by-pass capacitors.

Trigger Output Amplifier

The trigger output stage (Q454, Q554) is almost identical to the signal output stage, except the stage gain is not adjustable, so that the output, while nominally the same as the signal output level, can in fact have quite a wide tolerance.

INDICATOR OSCILLOSCOPE READOUT

General

The readout block consists of switching resistors and a probe sensing device. The switching resistors are used to signal to the Indicator Oscilloscope the setting of the VOLTS/DIV switch. R618A, R618B, and R618G select the number 1, 2, or 5 depending upon the combination that is switched in. R618C, R618D, R618L, and the output of the probe sensing device (Q614) select the decimal point (number of zeroes) again depending on the switched-in resistor combination. R618E, R618F, and R618N select the Volts sub-unit, either m (milli), μ (micro), or no sub-unit. R618H and R618K select

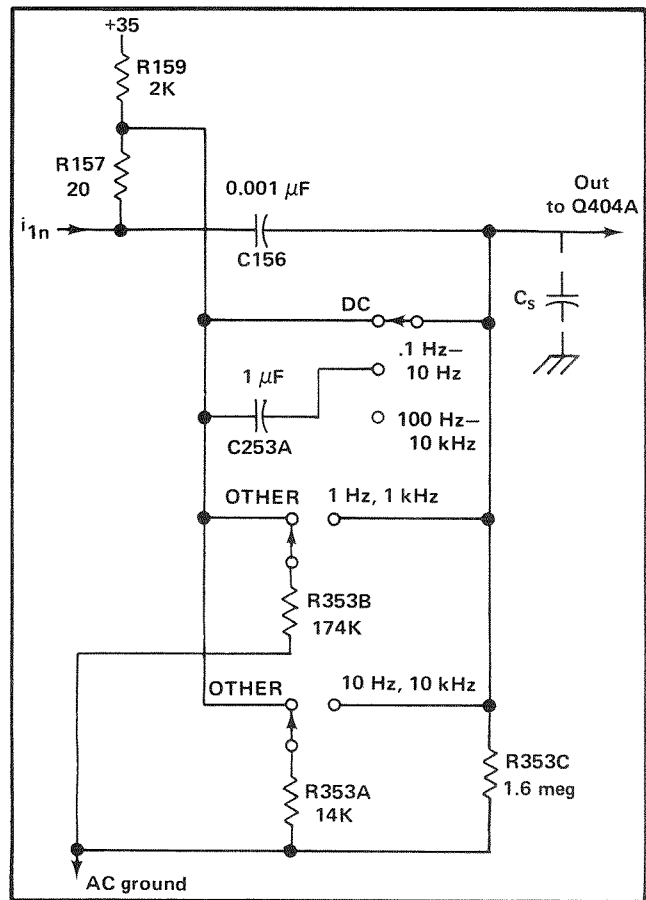


Fig. 3-11. Partial Low Frequency —3 dB Point selector for + Input amplifier.

the symbol V (volts). R618J and R618M select the symbol \gt when the VARIABLE VOLTS/DIV knob is in the uncalibrated position. Refer to the Schematic Diagram of the VOLTS/DIV Switch to find the resistors associated with a particular setting of the VOLTS/DIV switch.

Probe Sensing

The probe sensing device (Q614) identifies the attenuation of the probe connected to the front panel connector, by sensing the amount of current flowing from the current sink through the probe coding resistance, and adjusts the readout display so that the actual probe tip deflection factor is displayed.

The probe connected to the + (or —) INPUT connector forms a voltage divider with R610 (R620) through CR615 to the —15 V supply. This forward biases CR610 (CR620) allowing current to flow through R630, reducing the bias on Q614. The bias voltage, applied to the base of Q614, is set by the probe coding resistance of the divider probe.

When the —15 V clock pulse is applied to interface connector B33, Q614 is interrogated and its collector currents (determined by the base voltage and emitter resistor, R614) is added to the column current through interface connector A37.

Circuit Description—Type 7A22

With a 1× probe (or no probe) connected, Q614 is turned off, and the deflection factor, in the readout display, is determined by the setting of the Readout Switch, S407, (part of the VOLTS/DIV switch) only.

With a 10× probe connected to the input (+ or —), the bias on Q614 will allow 100 μ A of collector current to flow. This increases the deflection factor (in the readout display) by a factor of 10.

When different-attenuation divider probes are connected to the + and — INPUT connectors, the displayed readout will be the probe tip deflection factor of the larger divider. For example: if a 10× probe is connected to the +INPUT and a 100× probe is connected to the —INPUT the readout will display the deflection factor at the tip of the 100× probe. The 100× probe will set the bias on Q614 to allow 200 μ A of collector current which will increase the deflection factor (in the readout display) by a factor of 100.

This means that the Indicator Oscilloscope displayed readout will give the correct deflection factor from the probe tip, for a single probe or two probes having the same attenuation factor, whereas the VOLTS/DIV knob on the plug-in will display only the plug-in deflection factor.

Trace Identify

The TRACE IDENTIFY button, when pressed, does two things:

1. It causes the trace, representing the output of the Type 7A22, to move a small amount by inserting a 2 k Ω resistor, R622, from ground through CR630 to the junction of R632 and R634. This shunts a small amount of the output current causing the trace to move.

2. Forms a voltage divider from the —15 V supply through R630 and R622, placing the base of Q614 at approximately —1.5 V. This low value of bias will cause Q614 to turn on hard (1,000 μ A collector current) when interface connector B33 is interrogated, erasing the readout display and causing the word "IDENTIFY" to appear.

These two actions aid in identifying the Type 7A22 trace when multiple traces are displayed. When the IDENTIFY button is released, the readout is restored to its previous display.

For more specific information on the operation of the system, refer to the appropriate Indicator Oscilloscope manual.

SECTION 4

MAINTENANCE

Change information, if any, affecting this section will be found at the rear of the manual.

Introduction

Information which will aid in keeping the Type 7A22 operating at its peak performance is contained in this section. Cleaning, lubricating and visual inspection hints are included under preventative maintenance. The section on corrective maintenance includes troubleshooting and corrective procedures. Parts identification and soldering techniques are included where necessary.

PREVENTIVE MAINTENANCE

General

The instrument should be cleaned, inspected and recalibrated at regular intervals. The recommended interval for average operating conditions is every 6 months or every 1000 hours of operation, whichever occurs first.

Cleaning the Front Panel

Loose dust may be removed with cloth and a dry paint brush. Water and mild detergents such as Kelite or Spray White may be used.

CAUTION

Avoid the use of chemical agents which might damage the plastics used in this unit. Avoid chemicals such as benzene, toluene, xylene, acetone or similar solvents.

Cleaning the Interior

Cleaning of the interior of the unit should precede calibration, since the cleaning process might alter the settings of the calibration adjustments.

To clean the interior, use low-velocity compressed air to blow off the accumulated dust. High velocity air streams should be avoided to prevent damage to components.

WARNING

Use an eye-shield when cleaning with pressurized air. Hardened dirt can be removed with a paint brush, cotton tipped swab or cloth dampened with a water and mild detergent solution. Avoid the use of chemical cleaning agents that might damage the plastic parts.

Visual Inspection

The unit should be inspected occasionally for such defects as poor connections, broken or damaged circuit boards, improperly seated transistors and heat-damaged parts. The remedy for most visible defects is obvious. However, damage from overheating is usually a symptom of less obvious trouble; and unless the cause is determined before parts are replaced the damage may be repeated.

Transistor Checks

Periodic preventive maintenance checks on the transistors used in the unit are not recommended. The circuits within the unit generally provide the most satisfactory means of checking transistors usability. Performance of the circuits is thoroughly checked during recalibration, and substandard transistors will usually be detected at that time.

Calibration

To insure accurate measurements, the Type 7A22 calibration should be checked after each 1000 hours of operation or every six months if used intermittently. Complete calibration instructions are contained in Section 5.

The calibrated procedure can be helpful in isolating major troubles in the unit. Moreover, minor troubles not apparent during regular operation may be revealed and corrected during calibration.

CORRECTIVE MAINTENANCE

General

Replacement of some parts in the unit should be done by following a definite procedure. Some procedures, such as soldering and replacing components on the circuit boards, are outlined in this portion of the manual.

Many electrical components are mounted in a particular way to reduce or control stray capacitance and inductance. When selecting replacement parts, it is important to remember that the physical size and shape of a component may affect its performance at high frequencies. When a repair is made, calibration and performance of the relevant portions of the circuit should be checked. Refer to Table 4-1 and to the Performance Check/Calibration procedure in Section 5 and perform the applicable steps.

TABLE 4-1

Areas of Repair	Checks and Adjustments Affected	Section 5 Calibration Step
Input Coupling switches and cables	Input R and C, Attenuator compensation, CMRR, LF response, Gain	6, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22
Input Attenuators	1× input C, Cross Neutralization, DC BAL, CMRR Common-Mode dynamic range, Differential dynamic range, Offset range, Noise, Input protection system	3, 6, 10, 11, 12, 13, 14, 20, 22, 23, 24
Preamp	Gain, 1 MHz freq. response, Overdrive indicator	
LF —3 dB POINT	Gain, LF freq. response, 1 MHz freq. response	6, 20, 21
Overdrive Indicator	Dynamic range	23, 24
Gain Switched Amp	Gain, Deflection factor tracking, AC Atten Bal, HF —3 dB POINT, 1 MHz freq. response, Overdrive recovery Variable Balance	1, 2, 6, 8, 20, 26
Offset Generator	Offset Range, DC Bal, CMRR	3, 10, 22
Output Amplifier	HF —3 dB POINTS, Var balance, Variable range, Gain	2, 6, 7, 20

Obtaining Replacement Parts

Standard Parts. All electrical and mechanical part replacements for the Type 7A22 can be obtained through your local Tektronix Field Office or representative. However, many of the standard electronic components can be obtained locally in less time than is required to order them from Tektronix, Inc. Before purchasing or ordering replacement parts, check the parts lists for value, tolerance, rating and description.

NOTE

When selecting replacement parts, it is important to remember that the physical size and shape of the component may affect its performance in the instrument, particularly at high frequencies. All replacement parts should be direct replacements unless it is known that a different component will not adversely affect instrument performance.

Special Parts. Some parts are manufactured or selected by Tektronix to satisfy particular requirements, or are manufactured for Tektronix to our specifications. These and most

mechanical parts should be ordered through your Tektronix Field Engineer or Field Office. See Parts Ordering Information and Special Notes and Symbols on the page immediately preceding Section 6.

Soldering Techniques

Circuit Boards. Use ordinary 60/40 solder and a 35- to 40-watt pencil type soldering iron on the circuit boards. The tip of the iron should be clean and properly tinned for best heat transfer to the solder joint. A higher wattage soldering iron may separate the etched wiring from the base material.

The following technique should be used to replace a component on the circuit board. Most components can be replaced without removing the board from the unit.

1. Grip the component lead with long-nosed pliers. Touch the soldering iron to the lead at the solder connection. Do not touch the soldering iron tip directly on the board, as it may damage the board.

2. When the solder begins to melt, pull the lead out gently. This should leave a clean hole in the board. If not, the hole can be cleaned by reheating the solder and placing a sharp object, such as a toothpick or pointed tool, into the hole to clean it out.

3. Bend the leads of the new component to fit the holes in the board. Cut the leads of the new component to the same length as those of the old component. Insert the leads into the board until the component is firmly seated against the board, or as positioned originally. If it does not seat properly, heat the joint, and gently press the component into place.

4. Apply the iron and a small amount of solder to the connection to make a firm solder joint. To protect heat-sensitive components, hold the lead between the component body and the solder joint with a pair of long-nose pliers or other heat sink (see Fig. 4-1).

5. Clip the excess lead that protrudes through the board.

6. Clean the area around the soldered connection with flux-remover solvent to maintain good environmental characteristics and appearance. Be careful not to remove information printed on the board.

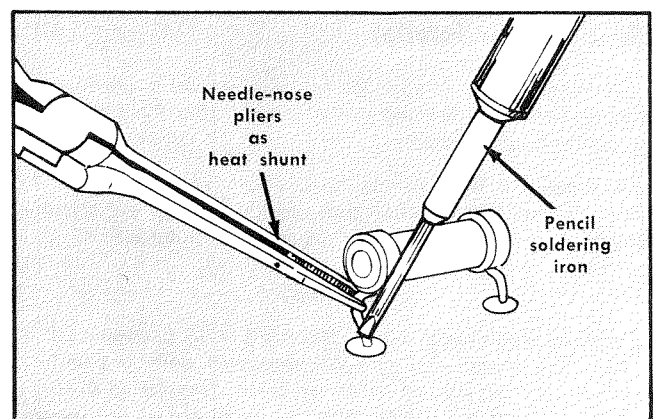


Fig. 4-1. Removing or replacing component on circuit board.

CAUTION

Silk-screen lettering dissolves when contacted by flux-remover.

Metal Terminals. When soldering metal terminals (inter-connecting plug pins, switch terminals, potentiometers, etc.) ordinary 60/40 solder can be used. The soldering iron should have a 40- to 75-watt rating with a $\frac{1}{8}$ inch wide chisel-shaped tip.

Observe the following precautions when soldering to metal terminals:

1. Apply heat only long enough to make the solder flow freely.
2. Apply only enough solder to form a solid connection; excess solder may impair the function of the part.
3. If a wire extends beyond the solder joint, clip the excess close to the joint.
4. Clean the flux from the solder joint with a flux-remover solvent to maintain good environmental characteristics and appearance.

Specific Component Replacement Information**WARNING**

Disconnect the instrument from the power source before removing or replacing components.

The pushbutton switches are not repairable and should be replaced if defective. Components which are mounted on the circuit board associated with the pushbutton switch can be replaced using the normal replacement procedure. See the information under Light-Bulb Replacement for bulb replacement. Use the following procedures to replace the +INPUT and -INPUT pushbutton switches.

1. AC-GND-DC (+INPUT) Switch

- a. Set the front panel controls as follows:

POSITION	Midrange
STEP ATTN DC BAL	Midrange
VOLTS/DIV	10 V
HF -3 dB POINT	1 MHz
LF -3 dB POINT	DC OFFSET
DC OFFSET (FINE)	Midrange
DC OFFSET (COARSE)	5 turns from either extreme
- b. Remove all front panel knobs.
- c. Remove the GND binding post assembly.
- d. Remove the nut and washer from the POSITION and DC OFFSET shafts.
- e. Remove the plastic bushing from the VOLTS/DIV shaft.
- f. Remove the front panel overlay.
- g. Without unsoldering any leads, carefully push the dual DC OFFSET potentiometers out of the front panel and lift them out of the way.
- h. Unsolder and unplug the connecting leads from the AC-GND-DC switch.
- i. Remove four corner screws and remove the front sub-panel.

- j. Remove the two screws securing the AC-GND-DC switch to the front sub-panel and remove the switch.
- k. Replace by reversing the above procedure.

2. AC-GND-DC (-INPUT) Switch

- a. Perform step 1, parts a through f, of Specific Component Replacement.
- b. Loosen the setscrew on the collar connecting the GAIN shaft to the pot coupler and pull the shaft out of the front panel.
- c. Unsolder and unplug the connecting leads from the AC-GND-DC switch.
- d. Remove the four corner screws and remove the front sub-panel.
- e. Remove the two screws securing the switch to the front panel.
- f. Remove the switch.
- g. Replace by reversing the above procedure.

3. Light-Bulb Replacement, +INPUT and -INPUT Push-Button Switches

Two types of pushbutton switches which have different light designs are in use. One switch design uses a metal cover over the light bulb(s) on the back of the switch; the other type does not. Either or both types of switches may be used in the 7A22.

To replace light bulbs in the pushbutton switches, use the following procedure:

- a. Remove the applicable INPUT switch as described in steps 1 and 2.
- b. On switches with the metal cover, remove the screw holding the cover and remove the cover.
- c. Clip off the bulb leads near the bulb body.
- d. Remove the leads from the circuit board.
- e. Remove the excess solder from the circuit board with a vacuum-type desoldering tool.
- f. Solder the new bulb to the circuit board (replace cover).
- g. Replace the switch in the instrument.

4. Bandwidth Circuit Board

- a. Set the LF and HF -3 dB POINT Selectors to either extreme.
- b. Loosen the setscrews securing the LF -3 dB POINT shaft to the collar on the cam switch. Pull the shaft forward until it clears the collar.
- c. Loosen the setscrews securing the HF -3 dB POINT shaft to the collar on the cam switch. Pull the shaft forward until it clears the bandwidth board.
- d. Loosen the setscrew securing the GAIN shaft to the collar on R540. Pull the shaft forward until it clears the bandwidth circuit board. Loosen the setscrew securing the collar to R540 and remove the collar.
- e. Loosen the four circuit board retaining screws and lift out the Bandwidth Circuit Board.
- f. Unclip the leads from solderless connectors A, AN, AP, AO, and D.
- g. Replace by reversing the above procedure.

CAUTION

Repair of cam-type switches should be undertaken only by experienced personnel. Switch alignment and spring tension of the contacts must be carefully maintained for proper operation of the switch. For assistance in maintenance of the cam-type switches, contact your local Tektronix Field Office or representative.

5. Cam-Type Switches

NOTE

A cam-type switch repair kit including necessary tools, instructions and replacement contacts is available from Tektronix, Inc. Order Tektronix Part No. 040-0541-00.

The cam-type switch consists of a rotating cam, which is rotated by a front-panel knob, and a set of contacts mounted on the adjacent circuit board. These switch contacts are actuated by lobes on the cam. The VOLTS/DIV, LF —3 dB POINT and HF —3 dB POINT cam-type switches can be disassembled for inspection, cleaning, repair or replacement, as follows:

A. LF —3 dB POINT and HF —3 dB POINT Switches

1. Remove the Bandwidth board as described in step 4.
2. Remove the two screws which hold the metal covers in place. (The front switch on the Bandwidth board is the LF —3 dB POINT Selector and the rear switch is the HF —3 dB POINT Selector). The switches are now open for cleaning or inspection.
3. To completely remove either of the two switches from the board, remove the four screws (from the back side of the board) which hold the cam assembly to the circuit board.
4. To remove the cam from the front support block, remove the retaining ring from the shaft on the front of the switch and slide the cam out of the support block. Be careful not to lose the small detent roller (between detent and detent spring).
5. To replace defective switch contacts, unsolder the damaged contact and clean solder from the hole in the circuit board. Position the new contact in the hole in proper alignment relative to the other switch contacts and with the mating area on the circuit board (alignment tool provided in switch repair kit). Solder the new contact into place; be sure that the spring end of the contact has adequate clearance from the circuit board.
6. To re-install the switch assembly, reverse the above procedure.

B. VOLTS/DIV Switch

1. Remove the Bandwidth board as described in step 4.
2. Remove the two screws which hold each of the metal switch covers in place.
3. Push the VARIABLE (CAL IN) to the 'IN' position.

4. Loosen the set screw which secures the VARIABLE shaft to the VARIABLE control assembly.

5. Pull the VARIABLE shaft through the front of the instrument.

6. Remove the 4 screws (from the rear side of the board) which hold the rear cam assembly to the circuit board.

7. Remove the rear cam assembly.

To remove the front cam-switch assembly, perform the preceding steps and then proceed as follows:

1. Remove the VOLTS/DIV knob.
2. Loosen the 1/2-inch hex bushing nut which secures the front support block.
3. Remove the plastic bushing from front of panel.
4. Remove the retaining ring from the shaft at the front of the switch.
5. Remove the 4 screws which secure the support blocks to the board.
6. Loosen the 2 set screws in the half of the flexible coupling and remove coupling.
7. Remove the rear support block by carefully lifting the block away from the circuit board far enough to clear the alignment projection on the bottom of the block. Carefully slide the rear block toward the rear (off the shaft).
8. Slide the cam shaft carefully (keep cam lobes clear of contacts) out of the front support block and bushing assembly.
9. To re-assemble, reverse the above procedure.

6. Interconnecting Pins

Interconnecting pins are used on the Type 7A22 to interconnect circuit boards. When interconnection is made at a circuit board, the pin is soldered into the board. Two types of mating connectors are used for these interconnecting pins. If the mating connector is mounted on a plug-on circuit board, a special socket is soldered into the board. If the mating connector is on the end of a lead, an end-lead pin connector (which mates with the interconnecting pin) is used. The following information provides the replacement procedures for the types of interconnecting methods.

A. CIRCUIT-BOARD PINS

NOTE

A pin replacement kit including necessary tools, instructions and replacement pins is available from Tektronix, Inc. Order Tektronix Part No. 040-0542-00.

To replace a pin which is mounted on a circuit board, first disconnect any pin connectors. Then unsolder the damaged pin and pull it out of the circuit board with a pair of pliers. Be careful not to damage the wiring on the board with too much heat. Ream out the hole in the circuit board with a 0.031-inch drill. Then remove the ferrule from the new interconnecting pin and press the new pin into the hole in

the circuit board. Position the pin in the same manner as the old pin. Then, solder the pin on both sides of the circuit board. If the old pin was bent at an angle to mate with a connector, bend the new pin to match the associated pins.

B. CIRCUIT-BOARD PIN SOCKETS

The pin sockets on the circuit boards are soldered to the rear of the board. To replace one of these sockets, first unsolder the pin (use a vacuum-type desoldering tool to remove excess solder). Then straighten the tabs on the socket and remove it from the hole in the circuit board. Place the new socket in the circuit board hole and press the tabs down against the board. Solder the tabs of the socket to the circuit board; be careful not to get solder into the socket.

NOTE

The spring tension of the pin sockets ensures a good connection between the circuit board and the pin. This spring tension can be destroyed by using the pin sockets as connecting points for spring-loaded probe tips, alligator clips, etc.

C. END-LEAD PIN CONNECTORS

The pin connectors used to connect the wires to the interconnecting pins are clamped to the ends of the associated leads. To replace damaged end-lead pin connectors, remove the old pin connector from the end of the lead and clamp the replacement connector to the lead.

Some of the pin connectors are grouped together and mounted in a plastic holder; the overall result is that these connectors are removed and re-installed as a multi-pin connector when it is replaced. An arrow is stamped on the circuit board and a matching arrow is molded into the plastic housing of the multi-pin connector. Be sure these arrows are aligned as the multi-pin connector is replaced. If the individual end-lead pin connectors are removed from the plastic holder, note the color of the individual wires for replacement.

TROUBLESHOOTING

Introduction

The following information is provided to facilitate troubleshooting of the Type 7A22. Information contained in other sections of this manual should be used along with the following information to aid in locating the defective component. An understanding of the circuit operation is very helpful in locating troubles. See the Circuit Description Section for complete information.

General

If trouble occurs in the Type 7A22, the following procedure is recommended to accomplish rapid and effective repairs.

1. Check that the plug-in unit and the oscilloscope connectors are not damaged, and that the plug-in unit is properly inserted.

2. Inspect the front panels of the Type 7A22 and indicator oscilloscope to be sure that the trouble is not from an incorrect control setting.

3. Insure that the indicator oscilloscope is not at fault by inserting a known-good plug-in unit and checking its operation.

4. Determine all trouble symptoms.

5. Perform a visual inspection of the Type 7A22.

6. Repair or replace obviously defective parts.

7. Troubleshoot the Type 7A22 as necessary.

8. Recalibrate the Type 7A22.

Indicator Oscilloscope

The quickest check of the oscilloscope is to use it with a good plug-in unit, or check the questionable plug-in with another oscilloscope. Refer to the oscilloscope manual to verify proper operation of the oscilloscope.

Operating Procedures and Control Settings

Refer to the Operating Instructions Section of this manual to verify operating procedures and front panel control settings of the Type 7A22.

Trouble Symptoms

The Type 7A22 response to all front panel controls should be observed. The first-time operation in Section 2 or the Performance Check in Section 5 may be used for this purpose. All trouble symptoms should be evaluated and compared against each other. A casualty will often create a combination of symptoms that, when considered together, will pinpoint the trouble.

Visual Inspection

In physically examining the Type 7A22, take special note of the area indicated by evaluation of symptoms. Look for loose or broken connections, improperly seated transistors and burned or otherwise damaged parts. Repair or replace all obviously defective components.

Calibration Check

Troubles can frequently be located and corrected by recalibrating the instrument. Unless the casualty has definitely been isolated to a specific circuit, it is recommended that the calibration procedure contained in Section 5 be performed to provide a logical circuit troubleshooting sequence.

DETAILED TROUBLESHOOTING

General

If the casualty has not been disclosed and corrected through the procedure outlined, a detailed troubleshooting

Maintenance—Type 7A22

analysis will have to be performed. The Circuit Description Section, the Schematic Diagrams, and the troubleshooting aids contained in this section are designed to expedite troubleshooting.

The Circuit Description Section provides a fundamental understanding of circuit operation and is referred to the Schematic Diagrams. The Schematic Diagrams contain voltage and resistance values and signal waveforms. The specified operating conditions should be duplicated before making voltage or waveform comparisons.

NOTE

Voltages and waveforms may vary slightly between instruments. Those given in the schematics should be checked against each instrument while it is operating properly. Deviations should be noted on the schematics for later reference.

Test Equipment Recommended for Troubleshooting

The test equipment listed here should suffice for most troubleshooting jobs. Test equipment required for calibration is listed in the Calibration Section.

- High Impedance Voltmeter (20,000 Ω /V DC or greater)
- Ohmmeter (2 mA or less current on the $\times 1$ k Ω scale)
- Test Oscilloscope and Probes
- Flexible Plug-in Extension Cable
- Dynamic Transistor Tester

DC Balance Check

A properly operating oscilloscope will have its trace centered vertically on the CRT only when the Type 7A22 has a balanced output. The Type 7A22 is a balanced amplifier connected in a differential configuration, with the +INPUT circuit being electrically identical to the -INPUT circuit. With no signal or comparison voltage applied to the FET gates, any point in the +INPUT circuit should have a potential equal to an identical point in the -INPUT circuit.

If the CRT trace is deflected as a result of a Type 7A22 problem, unbalances will exist between the two circuits. The unbalance can be detected by connecting a high impedance voltmeter between identical points in the two circuits.

An aid to this process is to short together the inputs of the stage being checked as shown in this example.

Stage	Devices	Short Together
Signal Output	Q444, Q544	Q444, Q544 bases
Trigger Output	Q454, Q554	Q454, Q554 bases
Variable Stage	Q434, Q534	Q434, Q534 bases
Gain Switching Amplifier	Q404A, Q404B, Q414A, Q414B, Q424, Q524	Front ends of R401 and R501
Input Amplifier		AC-GND-DC switches to GND

If the output balance is checked first and is in error, work toward the front until an unbalance no longer exists. This localizes the trouble to the circuitry between the points which

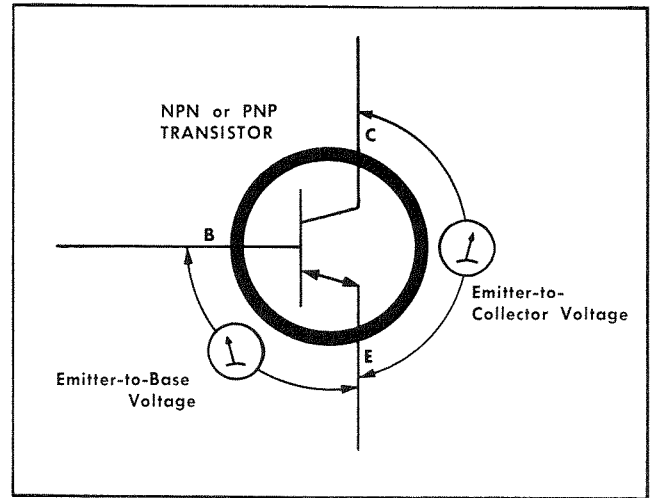


Fig. 4-2. Transistor junction-voltage measurements.

are balanced and the points which are unbalanced. The individual components must then be checked.

Troubleshooting by Direct Replacement

Semi-conductor failures account for the majority of electronic equipment troubles. The ease of replacing transistors often makes substitution the most practical means of repair. If this method is used, these guide lines should be followed:

Determine that the circuit is safe for the substitute component.

Use only substitute components that are known to be good.

Remove the plug-in from the oscilloscope before substituting components, to protect both you and the equipment.

Be sure components are inserted properly.

Check operation after each component is replaced.

Return good components to their original sockets.

Check calibration after a bad component has been replaced (see Table 4-1).

Component Checks

Transistors

The best means of checking a transistor is by using a transistor curve display instrument such as the Tektronix Type 576. If a transistor checker is not readily available, a defective transistor can be located by signal tracing, by making in-circuit voltage checks, by measuring the transistor resistances or by the substitution method previously described.

When troubleshooting using a voltmeter, measure the emitter-to-base and emitter-to-collector voltages to determine whether the voltages are consistent with normal circuit voltages. Voltages across a transistor vary with the device and its circuit function. Some of these voltages are predictable. The base-emitter voltage of a conducting germanium transistor will normally be approximately 0.2 V and that of a silicon transistor will normally be approximately 0.6 V. The col-

lector-emitter voltage will vary with the circuit and circuit conditions but it should always exceed 0.5 V. The best way of checking these devices is by connecting a voltmeter across the junction, using a sensitive voltmeter setting (see Fig. 4-2).

An ohmmeter can be used to check a transistor if the ohmmeter's voltage source and current are kept within safe limits. 1.5 V and 2 mA are generally acceptable. Selecting the $\times 1$ k Ω scale on most ohmmeters will provide voltage and current below these values.

Table 4-2 contains the normal values of resistance to expect when making an ohmmeter check of an otherwise unconnected transistor.

Fig. 4-3 shows the transistor base and socket arrangements used in this instrument.

TABLE 4-2

Transistor Resistance Checks

Ohmmeter ¹ Connections	Resistance Reading That Can be Expected Using the R \times 1 k Range
Emitter-Collector	High readings both ways
Emitter-Base	High reading one way, low reading the other way
Base-Collector	High reading one way, low reading the other way

¹Test prods from the ohmmeter are first connected to the transistor leads and then the test lead connections are reversed. Thus, the effects of the polarity reversal of the voltage applied from the ohmmeter to the transistor can be observed.

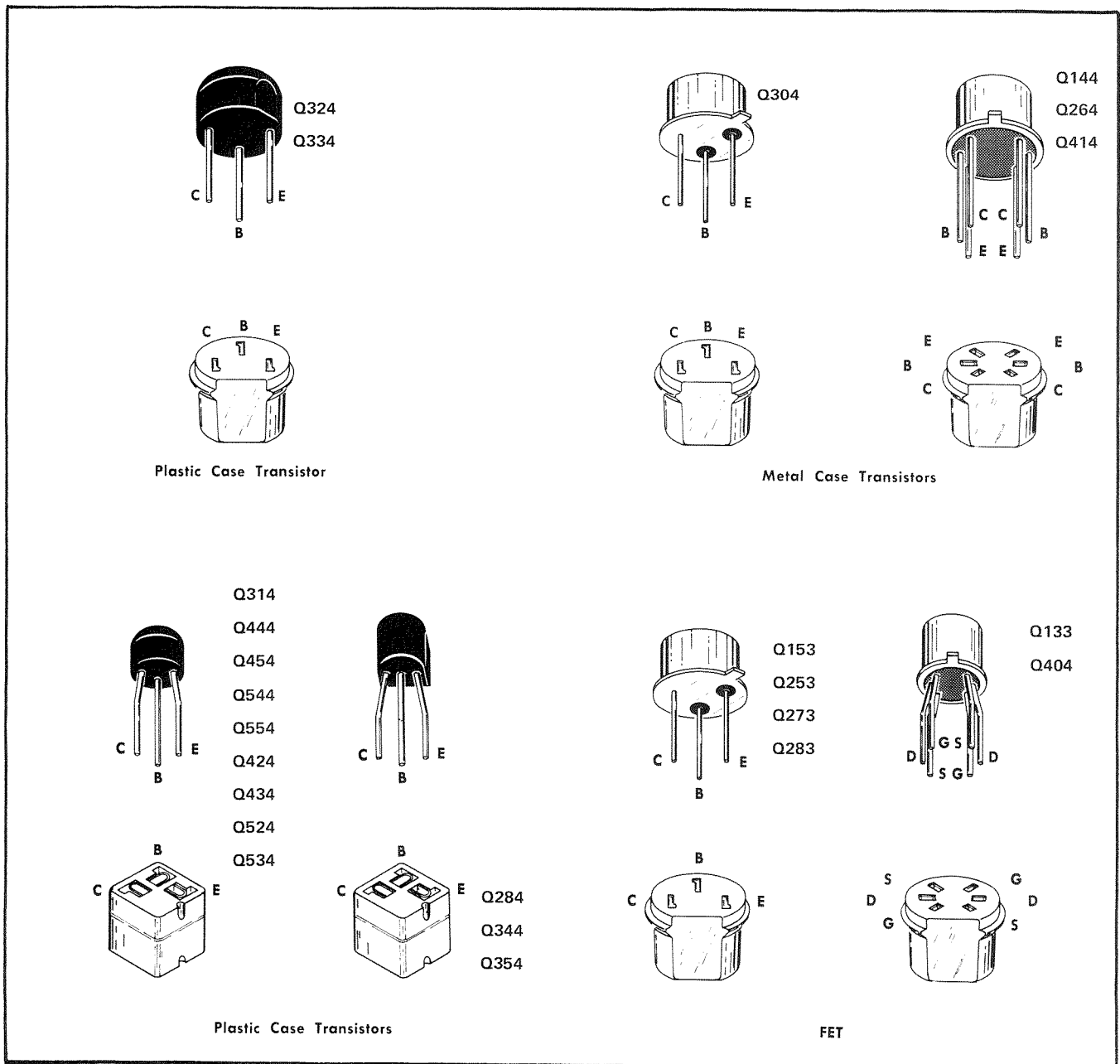


Fig. 4-3. Transistor base pin and socket arrangement.

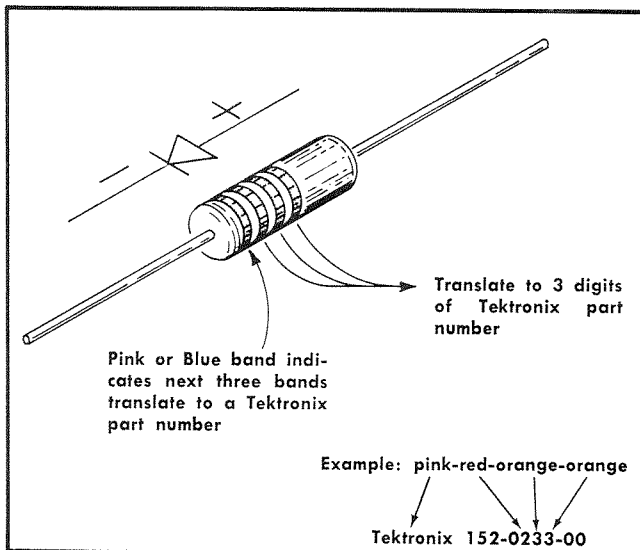


Fig. 4-4. Diode polarity and color code.

Diodes

A diode can be checked for an open or for a short circuit by measuring the resistance between terminals with an ohmmeter set to the R X 1k scale. The diode resistance should be very high in one direction and very low when the meter leads are reversed. Do not check tunnel diodes or back diodes with an ohmmeter.

Some diodes used in the Type 7A22 are color coded to identify the diode type. The cathode end of each glass-encased diode is indicated by a stripe, a series of stripes or a dot. For most diodes with a series of stripes, the first stripe (either pink or blue) indicates a Tektronix part and the next three stripes indicate the three significant figures of the Tektronix Part Number. Example: a diode color coded blue-brown-gray-green indicates a diode with Tektronix Part No. 152-0185-00. The cathode and anode of a metal-encased diode can be identified by the diode symbol marked on the body. See Fig. 4-4, DIODE POLARITY AND COLOR CODES.

Resistors

The types and accuracies of resistors found in this instrument vary in accordance with the circuit needs. Replacement resistors should be of the same type and must be at least as accurate as those originally contained in the circuit, to maintain the high common-mode rejection ratio. The size, location and lead length are often critical because of frequency considerations.

Composition Resistors:

Metal-Film Resistors:

Ceramic Capacitors:

① ② and ③ —1st, 2nd and 3rd significant figures;
 ④ —multiplier; ⑤ —tolerance;
 ⑥ —temperature coefficient.

Color	Significant Figures	Multiplier		Tolerance	
		Resistors	Capacitors	Resistors	Capacitors
Silver	---	10 ⁻²	---	±10%	---
Gold	---	10 ⁻¹	---	±5%	---
Black	0	1	1	---	±20% or 2 pF*
Brown	1	10	10	±1%	±1% or 0.1 pF*
Red	2	10 ²	10 ²	±2%	±2%
Orange	3	10 ³	10 ³	±3%	±3%
Yellow	4	10 ⁴	10 ⁴	±4%	+100% -0%
Green	5	10 ⁵	10 ⁵	±0.5%	±5% or 0.5 pF*
Blue	6	10 ⁶	10 ⁶	---	---
Violet	7	---	---	---	---
Gray	8	---	10 ⁻²	---	+80% -20% or 0.25 pF*
White	9	---	10 ⁻¹	---	±10% or 1 pF*
(none)	---	---	---	±20%	±10% or 1 pF*

*For capacitance of 10 pF or less.

NOTE: ⑦ and/or ⑧ color code for capacitors depends upon manufacturer and capacitor type. May not be present in some cases.

Fig. 4-5. Color code for resistors and ceramic capacitors.

Composition, wire-wound and metal film resistors are used in this unit. The stable metal film resistors may be identified by their light blue or gray body color. If a metal film resistor has a value indicated by three significant figures and a multiplier, it will be color coded according to the EIA standard resistor color code. If it has a value of more than three significant figures and a multiplier, the value will be printed on the body of the resistor. For example, a 333 k Ω resistor will be color coded but a 333.5 k Ω resistor will have its value printed on the resistor body. The color code sequence is shown in Fig. 4-5.

Capacitors

A leaky or shorted capacitor can be detected by checking resistance with an ohmmeter using the highest scale that does not exceed the voltage rating of the capacitor. The resistance reading should be high after the initial charge of the capacitor. An open capacitor can be detected with a capacitance meter or by checking whether the capacitor passes AC signals.

The capacitance values of common disc capacitors and small electrolytics are marked in microfarads on the side of the component body. The white ceramic capacitors used in the Type 7A22 are color coded in picofarads using a modified EIA code (see Fig. 4-5).

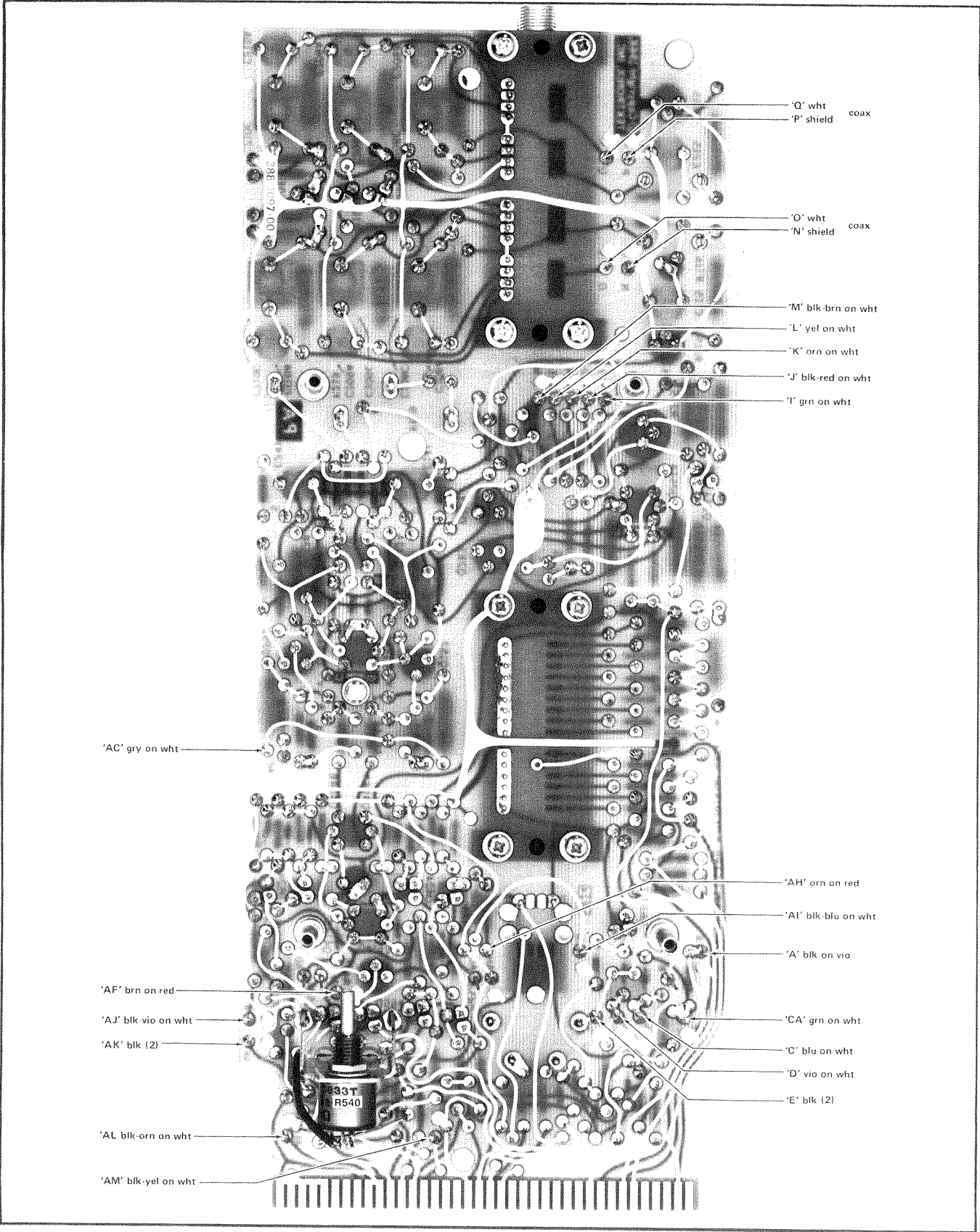


Fig. 4-7. Main circuit board (wire color code).

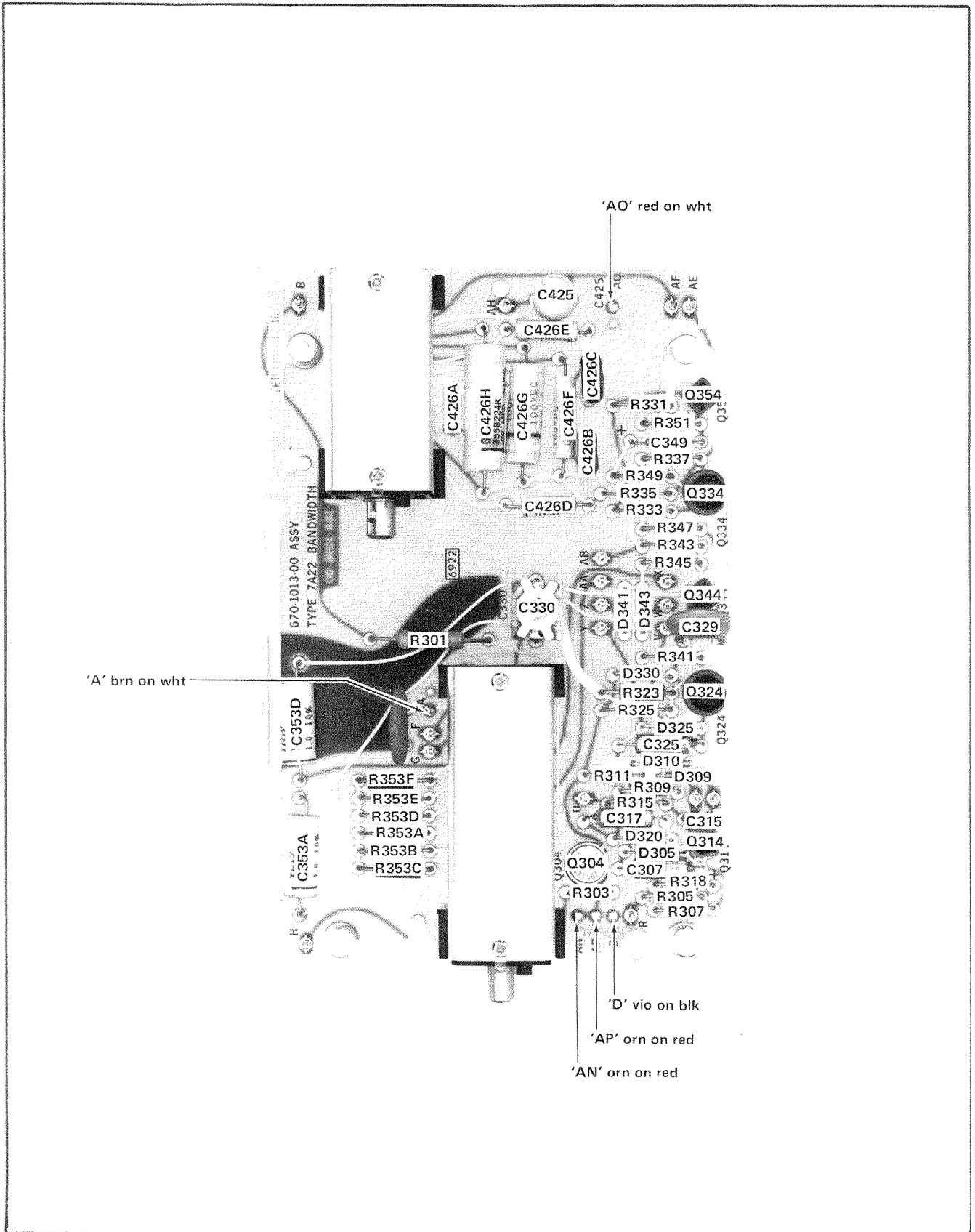


Fig. 4-8. Bandwidth circuit board (components and wire color code).

SECTION 5

PERFORMANCE CHECK / CALIBRATION PROCEDURE

Change information, if any, affecting this section will be found at the rear of the manual.

Introduction

Complete information for performing a Performance Check or Calibration of the Type 7A22 is contained in this section of the manual. The Equipment Required list is needed both for a Performance Check and for calibrating the Type 7A22. All waveform photographs, equipment setup pictures, and control settings apply whether the instrument is being calibrated or checked for performance.

To conduct a Performance Check complete all parts of each step in the following procedure, except the part subtitled ADJUST. To check the performance of the Type 7A22, it is not necessary to remove the oscilloscope side panel or make any internal adjustments. Adjustments located on the front panel of the Type 7A22 can be performed when checking the performance of the instrument. If the instrument does not meet the performance requirements given in this procedure, the complete procedure including adjustments should be done. All performance requirements given in this section correspond to the Specifications given in Section 1. For convenience in calibrating the Type 7A22, steps containing internal adjustments are marked with the symbol **Ⓐ**.

Calibration of the Type 7A22 requires completion of all parts of each step in the following procedure. Completion of every step in this procedure returns the Type 7A22 to its original performance standards. To assure accurate measurements and correct operation, the calibration of the Type 7A22 should be checked after each 1000 hours of operation; or every six months if used infrequently. Before performing a complete calibration, thoroughly clean and inspect this instrument as outlined in the Maintenance section.

TEST EQUIPMENT REQUIRED

General

The following test equipment and accessories (or their equivalent) are required for a complete performance check or calibration of the Type 7A22. Specifications given are the minimum necessary for accurate performance of this instrument. All test equipment is assumed to be correctly calibrated and operating within the given specifications. If equipment is substituted, it must meet or exceed the specifications of the recommended equipment.

For the quickest and most accurate calibration or performance check, special Tektronix calibration fixtures are used where necessary. These special calibration fixtures are

available through your local Tektronix Field Office or representative.

1. 7000 Series Oscilloscope, referred to as "oscilloscope" in this procedure. For this procedure a Type 7504 with a 7B50 Time Base is used.

2. Constant Amplitude Sine-Wave Generator. Output frequency range of 10 Hz through 1.0 MHz; output amplitude range from 2 V to 20 V peak to peak. General Radio Type 1310A is recommended.

3. Standard Amplitude Calibrator. Amplitude accuracy, within 0.25%; signal amplitude 0.5 mV to 100 V; output signal 1 kHz square wave and DC. Tektronix Calibration Fixture 067-0502-01 recommended.

4. Reed Pulse Generator. Tektronix Calibration Fixture 067-0608-00 is recommended.

5. Coaxial Cable. Impedance, 50 ohms; length, 42 inches; connectors, BNC. Two required. Tektronix Part Number 012-0057-00.

6. Dual Input BNC connector. Provides matched signal paths to both Type 7A22 input connectors. Tektronix Part Number 067-0525-00.

7. Variable Attenuator. A variable attenuator which has the end terminals of a 100 ohm potentiometer connected from input to ground and the potentiometer divider arm connected to the attenuator output. Tektronix Calibration Fixture 067-0511-00 is recommended.

8. 1000:1 Divider. Tektronix Part Number 067-0529-00 is recommended.

9. Input RC Normalizer. RC time constant, 1 megohm \times 47 pF; connectors, BNC. Tektronix Part Number 067-0541-00 is recommended.

10. Attenuator. Impedance, 50 Ω ; Ratio, 10:1; connectors, BNC. Two each, Tektronix Part Number 011-0059-00.

11. Plug-in extender. Tektronix Calibration Fixture 067-0589-00.

12. Termination. Impedance, 50 Ω . Tektronix Part Number 011-0049-00.

13. BNC T Connector. Tektronix Part Number 103-0030-00.

14. Adapter, GR to BNC Female. Tektronix Part Number 017-0063-00.

15. Adapter, GR to BNC Male. Tektronix Part Number 017-0064-00.

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16. Banana plug-jack to banana plug-jack patch cord. Tektronix Part Number 012-0031-00 (red) or 012-0034-00 (black). Three needed.

SHORT-FORM PERFORMANCE CHECK/CALIBRATION PROCEDURE

This short-form procedure is provided to aid in checking the performance or calibration of the Type 7A22. It may be used as a guide by the experienced operator or calibrator, or it may be reproduced and used as a permanent record of calibration. Since the step numbers and titles used here correspond to those used in the complete procedure, this procedure also serves as an index to locate a step in the complete procedure. Performance requirements listed here correspond to those given in Section 1.

Type 7A22, Serial No. _____

Calibration Date _____

Calibrated By _____

- 1. Check or Adjust AC Atten Bal (R505) Page 5-4
Trace within 1.5 Div of graticule center as VOLTS/DIV switch is changed from 10 mV to 20 μ V.
- 2. Check or Adjust Variable Bal (R425) Page 5-4
Maximum trace shift ± 0.2 Div as the VARIABLE (VOLTS/DIV) is rotated stop-to-stop.
- 3. Check or Adjust Coarse DC Bal (R275) Page 5-4
Trace within 0.1 Div of graticule center as LF —3 dB POINT selector is changed from 10 kHz to DC.
- 4. Check or Adjust + INPUT Zero (R115) Page 5-5
Maximum trace shift ± 2 Div as the + INPUT AC-GND-DC switch is switched from GND to AC.
- 5. Check or Adjust — INPUT Zero (R215) Page 5-5
Maximum trace shift ± 2 Div as the — INPUT AC-GND-DC switch is switched from GND to AC.
- 6. Check or Adjust GAIN (R540) Page 5-5
Correct vertical deflection in the 1 mV position of the VOLTS/DIV switch (front panel adjustment).
- 7. Check VARIABLE (VOLTS/DIV) control Page 5-5
Ratio
Display amplitude decreases by a ratio of 2.5:1 when the VARIABLE control (in the out position) is rotated fully counterclockwise.
- 8. Check VOLTS/DIV Gain Switching Page 5-5
Correct vertical deflection from 10 μ V through 10 mV.
- 9. Check Isolation Between + and — INPUTS Page 5-7
Trace deflection of 0.5 Div or less.
- 10. Check Total DC OFFSET Range Page 5-7
Check for minimum offset range of + and —1 volt.
- 11. Check or Adjust C241 - Cross Neutralization Page 5-7
With signal connected to + INPUT, switch the — INPUT AC-GND-DC from GND to DC; aberration should not exceed $\pm 1\%$.
- 12. Check or Adjust C141 - Cross Neutralization Page 5-8
With signal connected to — INPUT, switch the + INPUT AC-GND-DC from GND to DC; aberration should not exceed $\pm 1\%$.
- 13. Check or Adjust C115 - $\times 1$ + INPUT Attenuator Page 5-8
Time Constant
Optimum square wave response.
- 14. Check or Adjust C215 - $\times 1$ — INPUT Attenuator Page 5-8
Time Constant
Optimum square wave response.
- 15. Check Input Attenuator Accuracy Page 5-9
Correct vertical deflection from 10 mV through 10 V.
- 16. Check or Adjust Attenuator Differential Balance (R108E, R109E, R110E) Page 5-9
Optimum differential balance.
- 17. Check or Adjust + Input Attenuator Compensation Page 5-10
(C108C, C109C, C110C, C108A, C109A, C110A)
Optimum square wave response.
- 18. Check or Adjust — Input Attenuator Series Compensation to match + Input (C208C, C209C, C210C) Best common-mode signal rejection (minimum spike amplitude) Page 5-10
- 19. Check or Adjust — Input Attenuator Shunt Compensation (C208A, C209A, C210A) Page 5-11
Optimum flat bottom display.
- 20. Check or Adjust HF —3 dB POINT (C425) Page 5-12
HIGH FREQ —3 dB POINT bandwidth limit.
- 21. Check LF —3 dB POINT Page 5-12
LOW FREQ —3 dB POINT bandwidth limit.
- 22. Check or Adjust CMRR (C330) Page 5-13
CMRR must be equal to or better than the specified requirements at the verification points shown in Section 1, Fig. 1-2 of the manual.
- 23. Check OVERDRIVE Indicator Page 5-13
Indicator turns on at approximately 1 volt.
- 24. Check Differential Signal Range Range 5-14
No change in sine-wave amplitude when + or — 1 volt DC is applied.
- 25. Check Overall Noise Level Tangentially Page 5-15
Less than 16 μ V of displayed noise, measured tangentially.
- 26. Check Overdrive Recovery Page 5-15
10 μ s or less to recover to within 0.5% of zero level.

PERFORMANCE CHECK/CALIBRATION PROCEDURE

General

The following procedure is arranged in a sequence which allows the Type 7A22 to be calibrated with the least interaction of adjustments and reconnection of equipment. The steps in which adjustments are made are identified by the symbol following the title. Instrument performance is checked in the "CHECK" part of the step before an adjustment is made. The "ADJUST" part of the step identifies the point at which the actual adjustment is made. Steps listed in the "INTERACTION" part of the step may be affected by the adjustment just performed. This is particularly helpful when only a partial calibration procedure is performed.

NOTE

To prevent recalibration of other parts of the instrument when performing a partial calibration, readjust only if the tolerances given in the "CHECK" part of the step are not met. However, when performing a complete calibration, best overall performance is obtained if each adjustment is made to the exact setting even if the "CHECK" is within the allowable tolerance.

In the following procedure, a test-equipment setup picture is shown for each major group of checks and adjustments. Each step continues from the equipment setup preceding the desired portion. External controls or adjustments of the Type 7A22 referred to in this procedure are capitalized (e.g., POSITION). Internal adjustment names are initial capitalized only (e.g., Variable Bal).

All waveforms shown in this procedure are actual waveform photographs taken with a Tektronix Oscilloscope Camera System. The following procedure uses the equipment listed under Test Equipment Required. If equipment is substituted, control settings or test equipment setup may need to be altered to meet the requirements of the equipment used. Detailed operating instructions for the test equipment are not given in this procedure. If an doubt as to the correct operation of any of the test equipment, refer to the instruction manual for that unit.

NOTE

It is assumed that performance is checked within a temperature range of 0°C to +50°C and calibration +25°C, ±5°C; the tolerances given in this procedure are for this temperature range. However, if the procedure is performed at some other temperature, check the applicable tolerances for that temperature range.

Preliminary Procedure

1. If the Type 7A22 is to be calibrated, insert the plug-in extender into the oscilloscope and plug the Type 7A22 into the plug-in extender. If this procedure is a Performance Check only, insert the Type 7A22 into the oscilloscope plug-in compartment.

2. Connect the oscilloscope power cord to the design center operating voltage for which the oscilloscope is wired.

3. Turn on the oscilloscope POWER switch. Allow at least 20 minutes warmup for checking the instrument to the given accuracy.

4. Preset the Type 7A22 front panel controls as follows:

VOLTS/DIV	10 mV
VARIABLE	CAL IN
POSITION	Midrange
HIGH FREQ —3 dB POINT	100 Hz
LOW FREQ —3 dB POINT	10 kHz
AC-GND-DC (+INPUT)	GND
AC-GND-DC (—INPUT)	GND
STEP ATTEN DC BAL	Midrange
DC OFFSET COARSE	5 turns from either extreme
DC OFFSET FINE	Midrange

5. Preset the Time Base front panel controls to these settings:

Time/Div	.5 ms
Variable	In (Calibrated)
Triggering	Auto, AC, Int

6. Set the oscilloscope Focus and Intensity for best viewing.

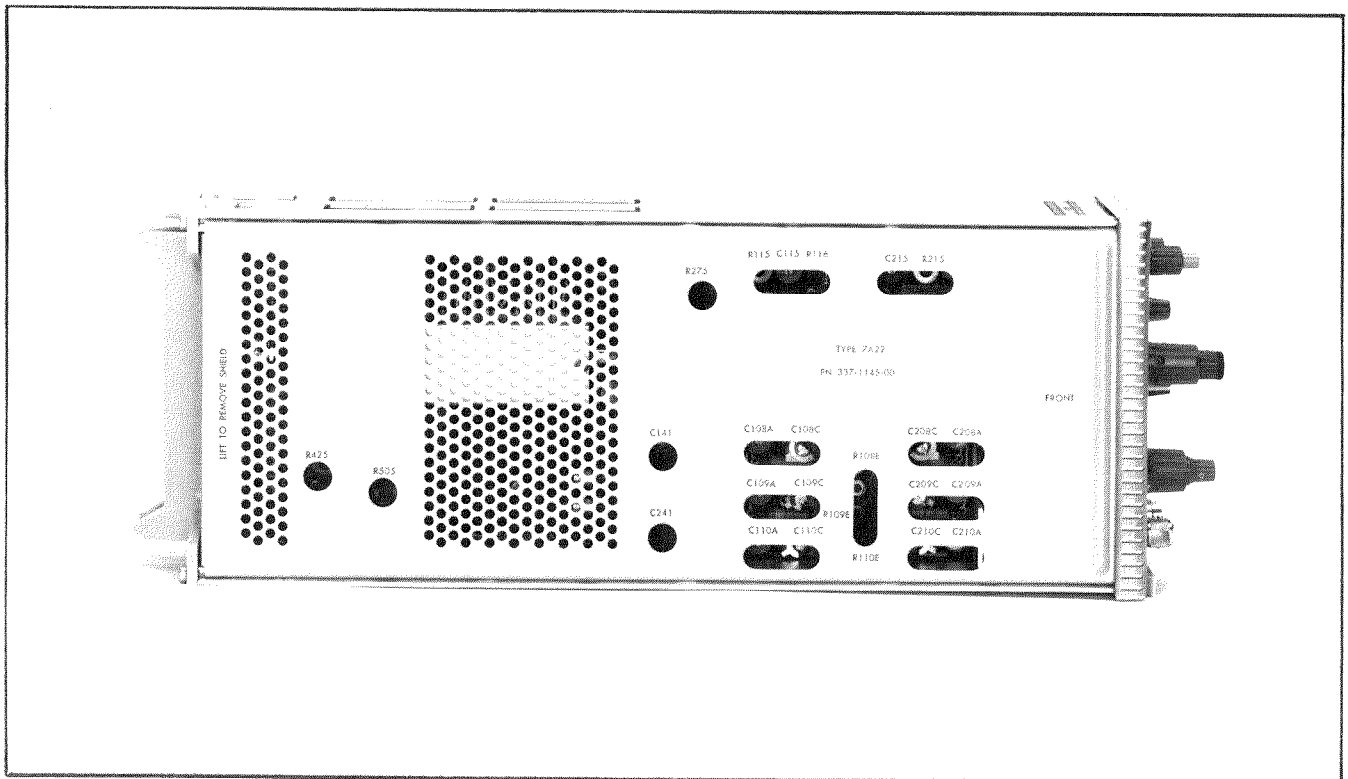


Fig. 5-1. Left side of the 7A22 showing adjustment locations.

NOTE

Calibration of the Type 7A22 must be performed with the side covers in place. All internal adjustments can be made using the access holes provided in the side covers.

1. Check or Adjust AC Step Atten Bal ①

- a. Center the trace on the CRT with the POSITION control.
- b. Rotate the VOLTS/DIV switch from 10 mV to the 20 μ V position.
- c. CHECK—The trace should remain within 1.5 div of graticule center.
- d. ADJUST—AC Step Atten Bal control, R505, (see Fig. 5-1) to position the trace within 1.5 div of graticule center.

2. Check or Adjust Variable Bal ①

- a. Set the Type 7A22 VOLTS/DIV switch to 10 mV.
- b. Position the trace to graticule center with the POSITION control.
- c. Rotate the VARIABLE VOLTS/DIV (in the out position) from stop to stop.
- d. CHECK—For maximum trace shift not to exceed ± 0.2 div while rotating the VARIABLE control throughout its range.

- e. ADJUST—Variable Bal control, R425, (see Fig. 5-1) for no trace shift while rotating the VARIABLE control.

3. Check or Adjust Coarse DC Bal ①

- a. Set the VARIABLE VOLTS/DIV to the CAL IN position.
- b. Position the LOW FREQ -3 dB POINT switch to the DC position.
- c. CHECK—The trace should be within 0.1 div of graticule center.
- d. ADJUST—Coarse DC Bal, R275, (see Fig. 5-1) to position the trace to graticule center.
- e. Set the VOLTS/DIV switch to 50 μ V.
- f. CHECK—The trace should be on screen.
- g. ADJUST—If the trace is not on screen, start at the position of the VOLTS/DIV switch where an on screen display is obtained and readjust the Coarse DC Bal, working down to the 50 μ V position so the end result is an on-screen trace at 50 μ V.

Type 7A22 Controls

VOLTS/DIV	10 μ V
VARIABLE	CAL IN
POSITION	Midrange
HIGH FREQ -3 dB POINT	100 Hz

LOW FREQ —3 dB POINT	DC
AC-GND-DC (+INPUT)	GND
AC-GND-DC (—INPUT)	GND
STEP ATTEN DC BAL	Adjust for proper DC balance (see Operating Section).

Time Base Controls

Time/Div	.5 ms
Variable (Time/Div)	Cal
Triggering	Auto, AC, Int

4. Check or Adjust + Gate Current Zero ❶

- a. Connect a 50 Ω termination to the +INPUT connector.
- b. Using the POSITION control, position the trace to graticule center.
- c. Set the +INPUT AC-GND-DC switch to AC.
- d. CHECK—For maximum trace shift within ± 2 div.
- e. ADJUST—The +Gate Current Zero control, R115, (see Fig. 5-1), to position the trace to graticule center.
- f. CHECK—(only if adjustment has been made). Switch the +INPUT AC-GND-DC switch to GND and back to AC. There should be no movement of the trace.
- g. Set the +INPUT AC-GND-DC switch to GND.

5. Check or Adjust — Gate Current Zero ❶

- a. Remove the 50 Ω termination from the +INPUT and connect it to the —INPUT connector.
- b. Position the trace to graticule center with the POSITION control.
- c. Set the —INPUT AC-GND-DC switch to AC.
- d. CHECK—For maximum trace shift within ± 2 div.
- e. ADJUST—The —Gate Current Zero control, R215, (see Fig. 5-1), to return the trace to graticule center.
- f. CHECK—(only if adjustment has been made). Return the —INPUT AC-GND-DC switch to GND and back to AC. There should be no trace shift.
- g. Disconnect the 50 Ω termination.
- h. Set the —INPUT AC-GND-DC switch to GND.

Type 7A22 controls

VOLTS/DIV	1 mV
VARIABLE	CAL IN
POSITION	Midrange
HIGH FREQ —3 dB POINT	1 MHz
LOW FREQ —3 dB POINT	DC

AC-GND-DC (+INPUT)	DC
AC-GND-DC (—INPUT)	GND
STEP ATTEN DC BAL	Adjust for proper DC balance.

6. Check or Adjust GAIN ❶

- a. Test equipment setup is shown in Fig. 5-2.
- b. Connect a 5 mV peak-to-peak square wave signal from a standard amplitude calibrator through a 1000:1 divider and a coaxial cable to the +INPUT connector.
- c. Set the 1000:1 divider to ×1.
- d. Align the display with the graticule lines using the POSITION control.
- e. CHECK—The display for a vertical amplitude of exactly 5 div.
- f. ADJUST—The GAIN control (front panel adjustment R540) for exactly 5 div of display amplitude.

7. Check VARIABLE Control Ratio

- a. With the VARIABLE in the OUT position, rotate the control fully counterclockwise.
- b. CHECK—The display amplitude should be 2 div or less to meet the 2.5:1 ratio requirement.

8. Check VOLTS/DIV Gain Switching

- a. Set the HIGH FREQ —3 dB POINT switch to 3 kHz.
- b. Set the LOW FREQ —3 dB POINT switch to 1 Hz.
- c. Set the standard amplitude calibrator output to 10 mV.
- d. Using the VARIABLE VOLTS/DIV control, adjust the display amplitude to exactly 5 div.
- e. Set the Time Base Triggering Source to Line.
- f. Position the Time/Div switch to .1 μs
- g. Switch the 1000:1 divider to ×1000.
- h. CHECK—The vertical deflection factor from 10 μV through 50 μV. Table 5-1 is provided as a guide.

TABLE 5-1

VOLTS/DIV switch position	Standard Amplitude Calibrator Output Amplitude	Divisions of Deflection	Accuracy
10 μV	.1 V	5	± 2%
20 μV	.2 V	5	± 2%
50 μV	.5 V	5	± 2%

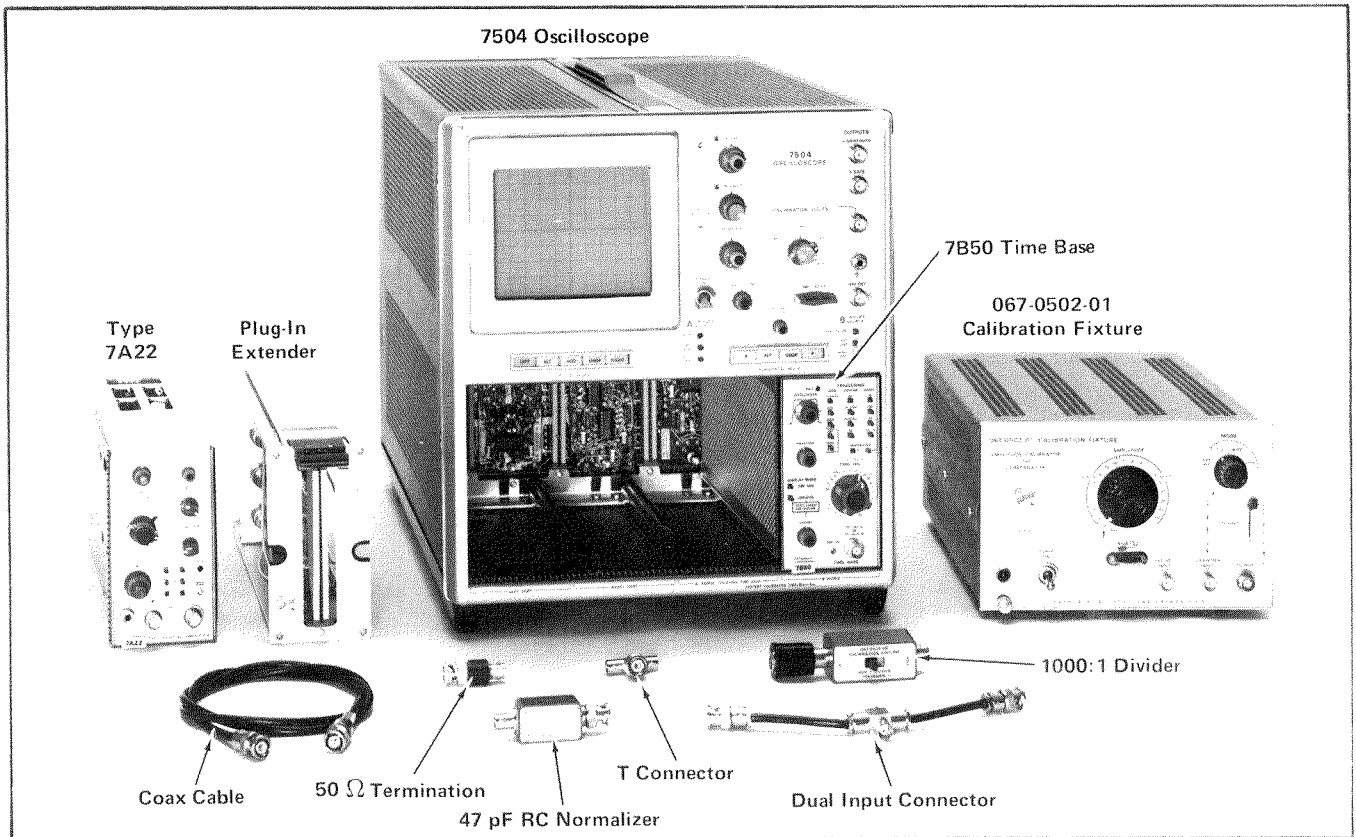


Fig. 5-2. Equipment required for steps 6 through 19.

- i. Return the VARIABLE to the CAL IN position.
- j. Switch the 1000:1 divider to the $\times 1$ position.
- k. CHECK—The vertical deflection factor from .1 mV through 10 mV using Table 5-2 as a guide.

TABLE 5-2

VOLTS/DIV switch position	Standard Amplitude Calibrator Output Amplitude	Divisions of Deflection	Accuracy
.1 mV	.5 mV	5	$\pm 2\%$
.2 mV	1 mV	5	$\pm 2\%$
.5 mV	2 mV	4	$\pm 2\%$
1 mV	5 mV	5	$\pm 2\%$
2 mV	10 mV	5	$\pm 2\%$
5 mV	20 mV	4	$\pm 2\%$
10 mV	50 mV	5	$\pm 2\%$

1. Remove the 1000:1 divider from the Standard Amplitude Calibrator.

Type 7A22 Controls

VOLTS/DIV	1 mV
VARIABLE	CAL IN
POSITION	Midrange
HIGH FREQ —3 dB POINT	1 MHz
LOW FREQ —3 dB POINT	DC OFFSET
AC-GND-DC (+INPUT)	DC
AC-GND-DC (—INPUT)	GND
STEP ATTEN DC BAL	Adjusted for DC balance

Time Base Controls

Time/Div	1 ms
Variable	Calibrated
Slope	+
Coupling	AC
Source	Int
Mode	Auto

9. Check Isolation Between + and -Inputs

- a. Set the Standard Amplitude Calibrator to .1 V square wave.
- b. Adjust the DC OFFSET COARSE and FINE controls to position the top of the square wave to the graticule center line.
- c. Switch the -AC-GND-DC switch to DC.
- d. CHECK—Trace deflection must be 0.5 division or less.
- e. Repeat the above procedure for the -INPUT, checking for trace deflection when switching the +AC-GND-DC switch to DC.

10. Check Total DC OFFSET Range

- a. Set the Type 7A22 controls as follows:

VOLTS/DIV	10 mV
AC-GND-DC (+INPUT)	DC
AC-GND-DC (-INPUT)	GND
- b. Set the Standard Amplitude Calibrator to 1 V and Mode selector to +DC.
- c. Connect a coaxial cable from the Standard Amplitude Calibrator output to the Type 7A22 +INPUT connector.
- d. Turn the COARSE and FINE OFFSET controls fully counterclockwise.
- e. CHECK—The trace can be returned to graticule center. (1 V minimum DC offset).
- i. Set the +INPUT AC-GND-DC switch to GND.
- j. Rotate the FINE and COARSE OFFSET controls clockwise to return the trace to graticule center (approximately 5 turns of the COARSE OFFSET control).

NOTE

The $\times 10$, $\times 100$, and $\times 1000$ input attenuators will be checked in step 15. Consequently, the remainder of the DC OFFSET ranges stated in Section 1 will be verified by that step.

Type 7A22 Controls

VOLTS/DIV	10 mV
VARIABLE	CAL IN
POSITION	Midrange
HIGH FREQ -3 dB POINT	1 MHz
LOW FREQ -3 dB POINT	DC
AC-GND-DC (+INPUT)	DC
AC-GND-DC (-INPUT)	GND
STEP ATTEN DC BAL	Adjusted for proper balance

Time Base Controls

Time/Div	.5 ms
Variable	In
Triggering	Norm, +Slope, AC, Int

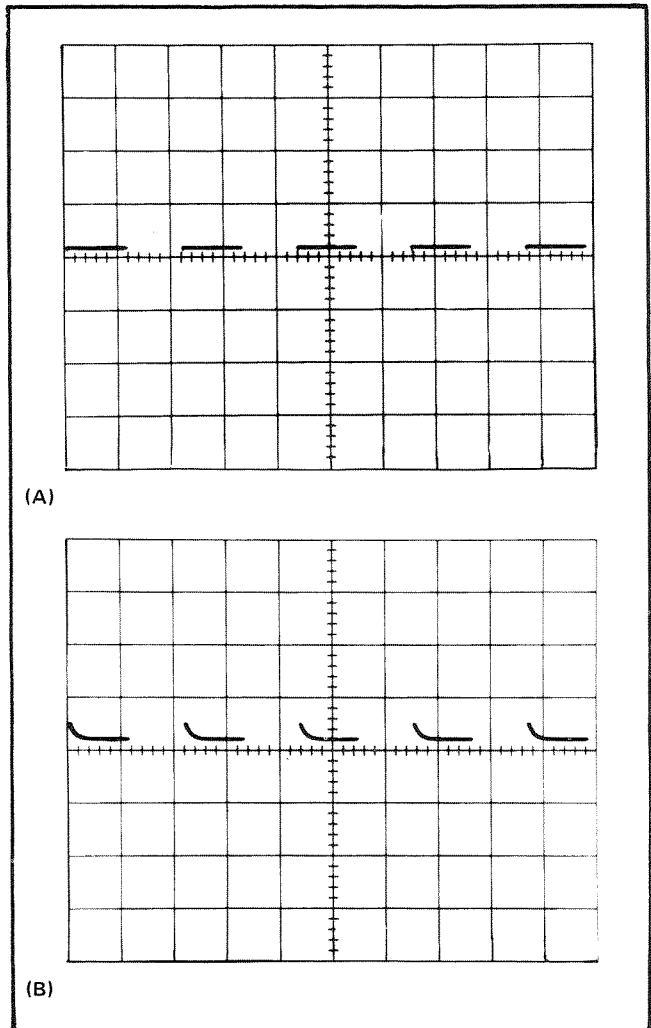


Fig. 5-3. Typical waveform showing (A) Cross neutralization properly adjusted, (B) incorrectly adjusted.

11. Check or Adjust C241 + Cross Neutralization

- a. Set the Standard Amplitude Calibrator for a 50 mV square-wave output.

NOTE

The Type 7A22 POSITION control and the Time Base horizontal positioning control may not always be mentioned. Use these controls as necessary to position the display for easy viewing.

- b. CHECK—The upper leading corner of the waveform, and note any aberration that occurs while switching the -INPUT AC-GND-DC switch from GND to DC. The waveform should appear similar to the one in Fig. 5-3. The aberration should not exceed $\pm 1\%$.

- c. Set the -INPUT AC-GND-DC switch to DC.

- d. ADJUST—C241 (see Fig. 5-1) for best square upper leading corner.

Performance Check/Calibration—Type 7A22

e. INTERACTION—C241 affects the $\times 1$ input capacitance and all other input attenuator adjustments. If C241 is adjusted out of sequence, steps 13, 14 and 16 through 18 must also be performed.

12. Check or Adjust C141 + Cross Neutralization ①

a. Disconnect the signal from the +INPUT connector and connect it to the -INPUT connector.

b. Set the +INPUT AC-GND-DC switch to GND.

c. CHECK—The lower leading corner of the second cycle of the display and note any aberration that occurs while switching the +INPUT AC-GND-DC switch from GND to DC. The bottom leading corner of the waveform should appear similar to the one shown in Fig. 5-3. The aberration should not exceed $\pm 1\%$.

d. Set the +INPUT AC-GND-DC switch to DC.

e. ADJUST—C141 (see Fig. 5-1) for best square corner.

f. INTERACTION—C141 affects the $\times 1$ input capacitance and all other input attenuator adjustments. If C141 is adjusted out of sequence, steps 13, 14 and 16 through 18 must be performed.

g. Disconnect the coaxial cable from the -INPUT.

13. Check or Adjust C115— $\times 1$ +INPUT Attenuator Time Constant ①

NOTE

It is important that C141 and C241 be properly adjusted before performing this adjustment. If you have not performed steps 11 and 12, do so at this point.

a. Connect a 47 pF input RC Normalizer to the +INPUT.

b. Connect a coaxial cable from the Standard Amplitude Calibrator to the RC Normalizer.

c. Set the -INPUT AC-GND-DC switch to GND.

d. Set the +INPUT AC-GND-DC switch to DC.

e. Set the Standard Amplitude Calibrator output to 0.1 V square wave.

f. CHECK—The square wave display for flat tops (see Fig. 5-4).

g. ADJUST—C115 (see Fig. 5-1) to obtain best square-wave response.

h. INTERACTION—If C115 is adjusted out of sequence, steps 14 and 16 through 19 must also be performed.

14. Check or Adjust C215— $\times 1$ -INPUT Attenuator Time Constant ①

a. Disconnect the RC Normalizer from the +INPUT and connect it to the -INPUT.

b. Set the +INPUT AC-GND-DC switch to GND.

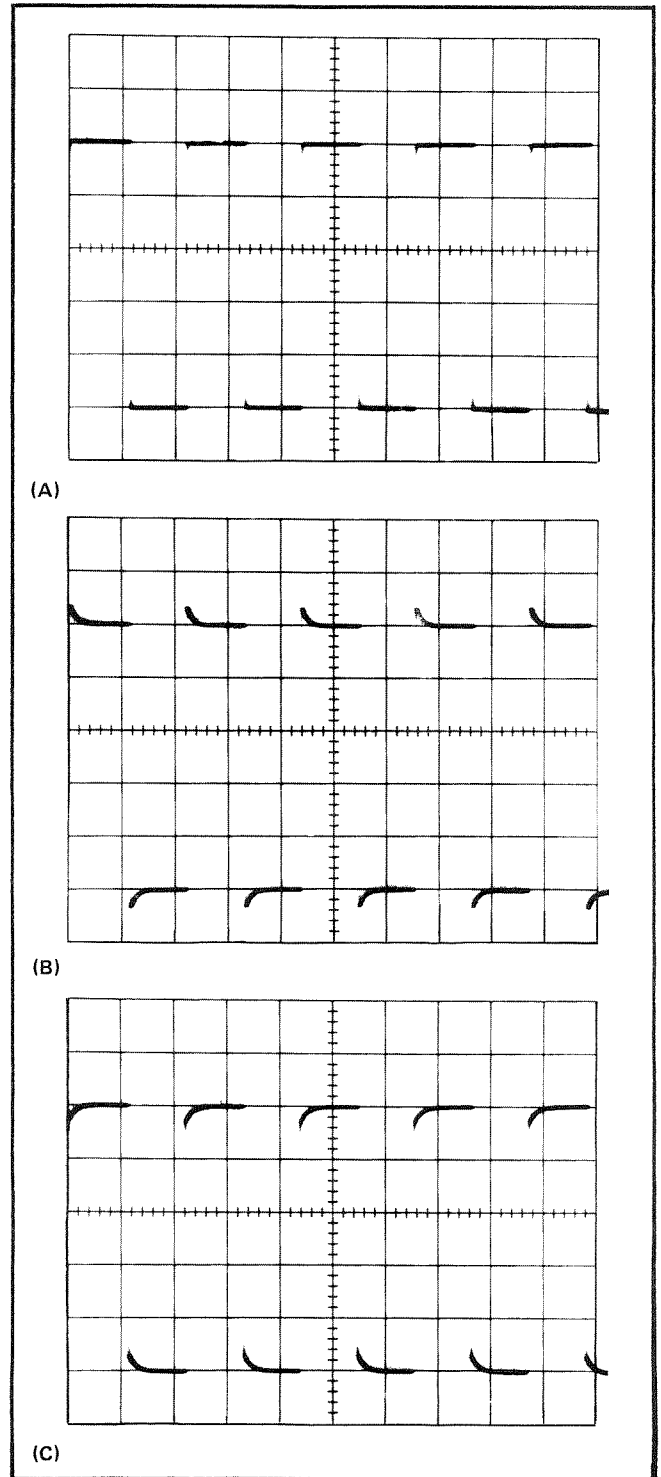


Fig. 5-4. Typical waveform showing (A) Correct adjustment of Input Attenuator Time Constant. (B) and (C) incorrect adjustment.

c. Set the -INPUT AC-GND-DC switch to DC.

d. CHECK—Each square wave for a flat bottom, using Fig. 5-4 as a guide.

e. ADJUST—C215 (see Fig. 5-1) for best flat bottom square-wave display, as in Fig. 5-4A.

- f. INTERACTION—If C215 is adjusted out of sequence, steps 13 and 16 through 19 must also be performed.
- g. Disconnect the signal and the RC Normalizer.

15. Check Input Attenuator Accuracy

- a. Connect a 50 mV peak-to-peak square wave signal from the Standard Amplitude Calibrator through a coaxial cable to the +INPUT.
- b. Set the +INPUT AC-GND-DC switch to DC.
- c. CHECK—The input attenuators using Table 5-3 as a guide.

TABLE 5-3

VOLTS/DIV Switch Position	CALIBRATOR Output Peak to Peak	VERTICAL DEFLECTION (Accuracy $\pm 2\%$)
10 mV	50 mV	5 div $\pm .1$ div
20 mV	.1 V	5 div $\pm .1$ div
50 mV	.2 V	4 div $\pm .08$ div
.1 V	.5 V	5 div $\pm .1$ div
.2 V	1 V	5 div $\pm .1$ div
.5 V	2 V	4 div $\pm .08$ div
1 V	5 V	5 div $\pm .1$ div
2 V	10 V	5 div $\pm .1$ div
5 V	20 V	4 div $\pm .08$ div
10 V	50 V	5 div $\pm .1$ div

- d. Disconnect the signal from the Type 7A22.

NOTE

(Applies to calibration only)

If there is a spike or fast rolloff of the leading corner of the square wave when checking from 20 mV to 10 V, ignore these, as they will be corrected in step 17.

16. Check or Adjust Input Attenuator Differential Balance

- a. Connect a T connector to the Output connector of the Standard Amplitude Calibrator and a dual connector to the +INPUT and -INPUT connectors of the Type 7A22.
- b. Connect a coaxial cable from the T connector to the dual input connector. Connect a coaxial cable from the T connector to the Ext In connector on the oscilloscope.
- c. Set the Type 7A22 controls as follows:

VOLTS/DIV	50 mV
AC-GND-DC (+INPUT)	DC
AC-GND-DC (-INPUT)	DC
- d. Set the oscilloscope Triggering Source switch to Ext.
- e. Set the Standard Amplitude Calibrator output to 50 V.
- f. CHECK—For optimum differential balance according to the information given in Table 5-4. When properly ad-

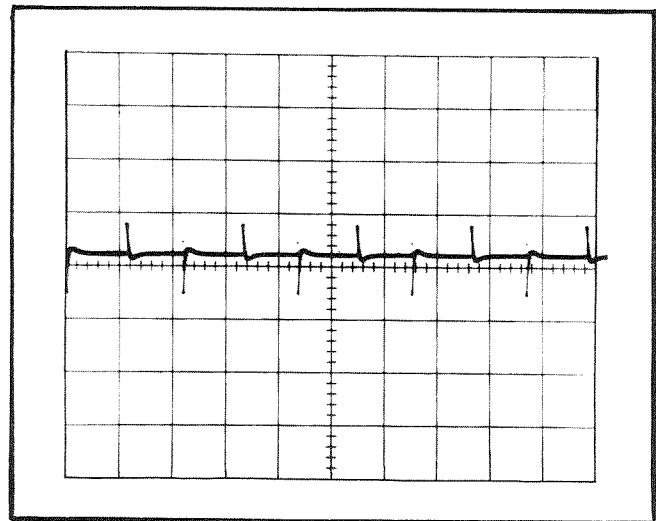


Fig. 5-5. Typical display obtained when Input Attenuators are adjusted for optimum differential balance.

justed, the waveform should appear as shown in Fig. 5-5. Disregard any spikes on the waveform.

- g. ADJUST—R108E, R109E and R110E (see Fig. 5-1) for minimum amplitude as shown in Fig. 5-5, using Table 5-4 as a guide.

TABLE 5-4

VOLTS/DIV Switch Position	Calibrator Output (peak to peak)	Check and Adjust for Null	Input Attenuator
50 mV	50 V	Check/Adjust	R108E
20 mV	50 V	Check	$\times 10$
.1 V	50 V	Check	
.5 V	100 V	Check/Adjust	R109E
.2 V	100 V	Check	$\times 100$
1 V	100 V	Check	
5 V	100 V	Check/Adjust	R110E
2 V	100 V	Check	$\times 1000$
10 V	100 V	Check	

- h. Set the Standard Amplitude Calibrator for 0.2 V peak-to-peak output.

- i. Disconnect the dual input connector from the Type 7A22.

Set the Type 7A22 Controls:

VOLTS/DIV	50 mV
VARIABLE	CAL IN
HIGH FREQ -3 dB POINT	1 MHz
LOW FREQ -3 dB POINT	DC
AC-GND-DC (+INPUT)	DC
AC-GND-DC (-INPUT)	GND
STEP ATTEN DC BAL	Adjusted for proper DC Balance

Performance Check/Calibration—Type 7A22

Set the Time Base Controls:

Time/Div	.5 ms
Variable	In (Cal)
Triggering	Auto, Ac, Int

17. Check or Adjust + Input Attenuator Compensation

a. Connect a 0.2 V peak-to-peak signal from the Standard Amplitude Calibrator through a coaxial cable to the +INPUT of the Type 7A22.

b. CHECK—The +INPUT Attenuator compensation for good square wave response.

c. ADJUST—The +INPUT Attenuator compensation for best square wave response using Table 5-5 as a guide. Fig. 5-1 shows the locations of the adjustments.

TABLE 5-5

Calibrator Output P-P	VOLTS/DIV Switch Position	Check/Adjust for Optimum		+ Input Attenuator
		Upper Leading Corner	Flat Top	
.2 V	50 mV	C108C		× 10
.1 V	20 mV	Check		
.5 V	.1 V	Check		
2 V	.5 V	C109C		× 100
1 V	.2 V	Check		
5 V	1 V	Check		
20 V	5 V	C110C		× 1000
10 V	2 V	Check		
50 V	10 V	Check		

Connect the 47 pF input RC normalizer between the +INPUT and the coaxial cable.

50 V	5 V		C110A	× 1000
20 V	2 V		Check	
100 V	10 V		Check	
5 V	.5 V		C109A	× 100
2 V	.2 V		Check	
10 V	1 V		Check	
.5 V	50 mV		C108A	× 10
.2 V	20 mV		Check	
1 V	.1 V		Check	

c. INTERACTION—If this step is performed out of sequence, steps 18 and 19 must be performed.

d. Disconnect the RC normalizer and coaxial cable from the Type 7A22 and the Standard Amplitude Calibrator.

18. Check or Adjust — Input Attenuator Series Compensation

a. Connect a T connector to the Standard Amplitude Calibrator output connector and a dual input connector to the

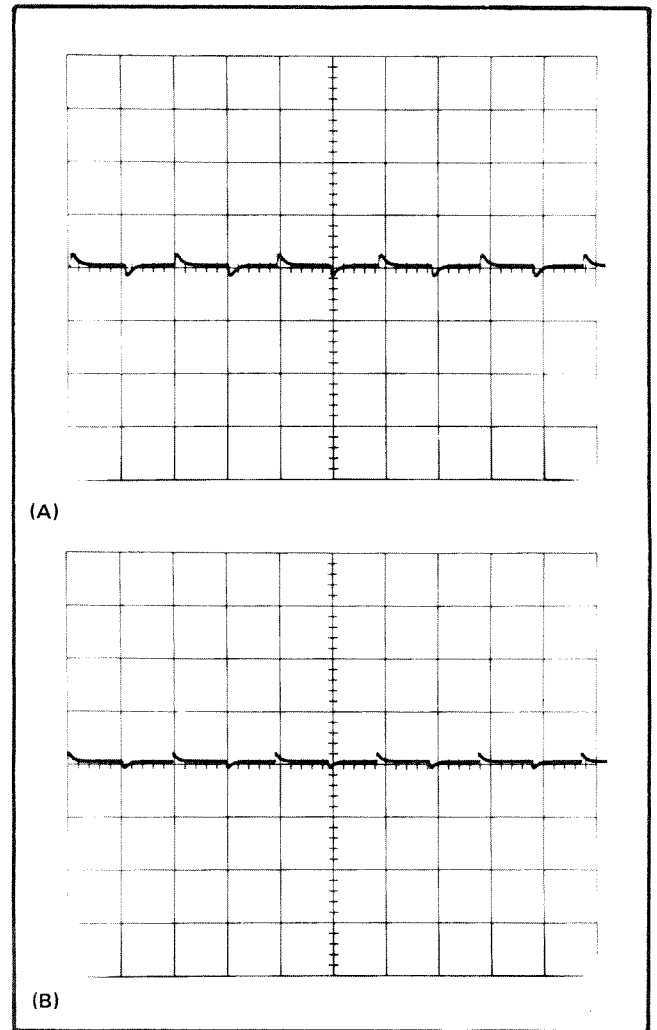


Fig. 5-6. Typical display obtained with — Input attenuator adjusted for optimum Common-Mode signal rejection. (A) C208C adjusted properly (B) C209C adjusted properly.

Type 7A22 +INPUT and —INPUT connectors. Connect a coaxial cable from the T connector to the dual input connector. Connect a coaxial cable from the T connector to the oscilloscope Ext In connector.

b. Set the oscilloscope Triggering Source switch to Ext.

c. Set the Standard Amplitude Calibrator output to 50 V square wave.

d. Set the Type 7A22 controls as follows:

VOLTS/DIV	50 mV
AC-GND-DC (—INPUT)	DC

e. CHECK—The displayed waveform for good common-mode signal rejection (minimum spike amplitude).

f. ADJUST—The — Input attenuator series compensation for best common-mode signal rejection (minimum spike amplitude) using the information given in Table 5-6. Fig. 5-6 illustrates the typical displays obtained and Fig. 5-1 shows the adjustment locations.

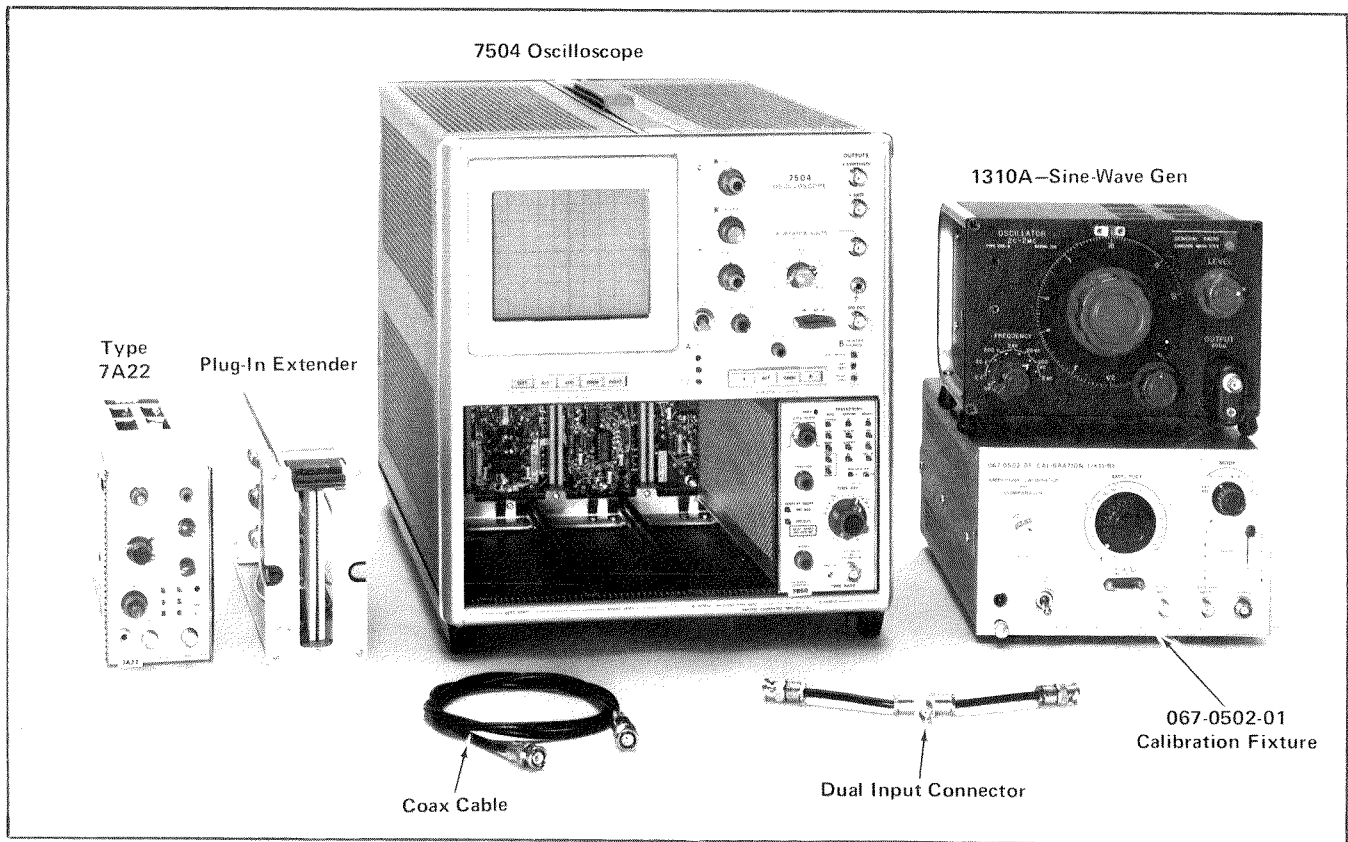


Fig. 5-7. Equipment required for steps 20 through 24.

TABLE 5-6

Calibrator Output P-P	VOLTS/DIV Switch Position	Check or Adjust for Min. Spike Amplitude	—Input Attenuator
50 Volts	50 mV	C208C	× 10
	20 mV	Check	
	.1 V	Check	
100 Volts	.5 V	C209C	× 100
	.2 V	Check	
	1 V	Check	
	5 V	C210C	× 1000
	2 V	Check	
10 V	Check		

g. Disconnect all signal leads.

h. Return the oscilloscope Triggering Source switch to Int.

19. Check or Adjust —Input Attenuator Shunt Compensation

a. Connect a 47 pF RC Normalizer to the —INPUT of the Type 7A22. Connect a coaxial cable from the Standard Amplitude Calibrator output connector to the RC Normalizer.

b. Set the Standard Amplitude Calibrator output to .5 V square wave.

c. Set the Type 7A22 controls as follows:

VOLTS/DIV 50 mV
AC-GND-DC (+INPUT) GND

d. CHECK—Using Table 5-7 as a guide, check the display for a square wave response similar to the display illustrated in Fig. 5-4.

e. ADJUST—C208A, C209A, and C210A (see Fig. 5-1) for best flat bottom on the display, according to the information given in Table 5-7.

TABLE 5-7

Standard Amplitude Calibrator Output P-P	VOLTS/DIV Switch Position	Check or Adjust for Optimum Flat Bottom	—Input Attenuator
.5 Volt	50 mV	C208A	× 10
.2 Volt	20 mV	Check	
1 Volt	.1 V	Check	
5 Volts	.5 V	C209A	× 100
2 Volts	.2 V	Check	
10 Volts	1 V	Check	
50 Volts	5 V	C210A	× 1000
20 Volts	2 V	Check	
100 Volts	10 V	Check	

Performance Check/Calibration—Type 7A22

f. Disconnect the RC normalizer and all coaxial cables.

Type 7A22 controls:

POSITION	Midrange
VOLTS/DIV	1 V
VARIABLE	CAL IN
AC-GND-DC (+INPUT)	GND
AC-GND-DC (-INPUT)	GND
LOW FREQ -3 dB POINT	DC
HIGH FREQ -3 dB POINT	1 MHz
STEP ATTEN DC BAL	Adjusted for DC balance

20. Check or Adjust HIGH FREQ -3 dB POINT

- a. Test equipment setup is shown in Fig. 5-7.
- b. Set the Type 7A22 +INPUT AC-GND-DC switch to DC and use the oscilloscope controls as necessary for easy viewing of display.
- c. Connect a coaxial cable from the constant Amplitude Sine-Wave Generator to the Type 7A22 +INPUT.
- d. Set the output frequency of the Sine Wave Generator to 1 kHz and adjust for a 6 div display on the oscilloscope.
- e. Set the Sine Wave Generator output frequency to 1 MHz.
- f. CHECK—The amplitude of the oscilloscope display should be 4.2 div (this is the -3 dB point at 1 MHz). The 4.2 div display requirement must be met at 1 MHz + or -10% (.9 MHz to 1.1 MHz).
- g. ADJUST—With the frequency set to 1 MHz, adjust C425 for 4.2 div of display amplitude. See Fig. 5-8 for location of adjustment.
- h. CHECK—The remaining positions of the HIGH FREQ -3 dB POINT selector in the same manner as in step h using Table 5-9 as a guide.

TABLE 5-9

Sine Wave Generator Output Frequency	HIGH FREQ -3 dB POINT Selector Position	Oscilloscope Display Amplitude	Bandwidth Tolerance ± 12% of Input Freq
300 kHz	300 kHz	4.2 div	±30 kHz
100 kHz	100 kHz	4.2 div	±10 kHz
30 kHz	30 kHz	4.2 div	±3 kHz
10 kHz	10 kHz	4.2 div	±1 kHz
3 kHz	3 kHz	4.2 div	±.3 kHz
1 kHz	1 kHz	4.2 div	±.1 kHz
300 Hz	300 Hz	4.2 div	±30 Hz
100 Hz	100 Hz	4.2 div	±10 Hz

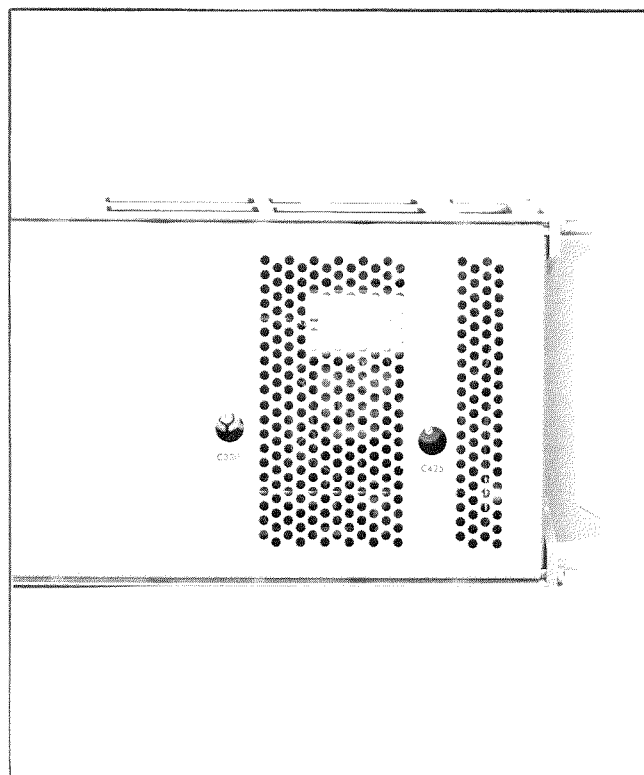


Fig. 5-8. Right side of Type 7A22 showing location of adjustments.

- i. Set the HIGH FREQ -3 dB POINT selector to 1 MHz.

21. Check LOW FREQ -3 dB POINT

- a. CHECK—Using Table 5-10 as a guide, check the LOW FREQ -3 dB POINT in the same manner that was used to check the HIGH FREQ -3 dB POINT.

TABLE 5-10

Sine Wave Generator Output Freq	LOW FREQ -3 dB POINT Selector Position	Oscilloscope Display Amplitude	Bandwidth Tolerance ± 12% of Input Freq
10 Hz	10 Hz	4.2 div	±1.2 Hz
100 Hz	100 Hz	4.2 div	±12 Hz
1 kHz	1 kHz	4.2 div	±120 Hz
10 kHz	10 kHz	4.2 div	±1.2 kHz

NOTE

The components that are used in the 0.1 Hz and 1 Hz positions of the LOW FREQ -3 dB POINT selector are also used in the other positions of the selector; therefore, the tolerance of the 0.1 Hz and 1 Hz positions are checked.

b. Set the LOW FREQ –3 dB POINT selector to DC and disconnect all signal connections to the Type 7A22, Sine Wave Generator, and Oscilloscope.

Type 7A22 controls:

VOLTS/DIV	5 V
VARIABLE	CAL IN
HIGH FREQ –3 dB POINT	1 MHz
LOW FREQ –3 dB POINT	DC
AC-GND-DC (+INPUT)	DC
AC-GND-DC (–INPUT)	GND
STEP ATTEN DC BAL	Adjusted for proper DC balance

Time-Base controls:

Time/Div	As necessary for easy viewing of display
Variable	In (Cal)
Triggering	Auto, AC, Ext

22. Check or Adjust Attenuator Common Mode Rejection

a. Attach a BNC T connector to the 7B50 EXT in connector. Connect a coaxial cable from the Constant Amplitude Sine-Wave Generator output to the BNC T connector. Connect another coaxial cable from the BNC T connector to the dual input connector.

b. Connect the Dual-Input connector to the Type 7A22 + and – INPUTS.

c. Set the 7B50 SOURCE to EXT X10, and DISPLAY MODE to AMPLIFIER. Adjust the Constant Amplitude Sine-Wave Generator Level control for 20 V P-P at 100 kHz. Use the VARIABLE on the 7B50 to adjust for a suitable display of 4 to 6 horizontal divisions.

NOTE

The Sine-Wave Generator output must be maintained at a constant 20 V p-p for all of the CMRR checks.

d. Switch the Type 7A22 AC-GND-DC (+INPUT) to GND.

e. Switch the Type 7A22 VOLTS/DIV to .1 mV.

f. Simultaneously switch the Type 7A22 + and – AC-GND-DC switches to DC.

g. CHECK—The vertical deflection should not exceed 2 div. The 2 div requirement at 0.1 mV/DIV is equivalent to a CMRR of 100,000:1 (2 div at .1 mV/DIV = .2 mV; $20\text{ V} \div .2\text{ mV} = 100,000$).

h. Adjust C330 (and C144 SN B080000 and up) for minimum vertical deflection. See Fig. 5-8 for C330 location. C144 is under the plastic cover near Q153 on the left side of the 7A22 (see Fig. 4-6, page 4-10).

NOTE

These adjustments interact, and a slight readjustment of C330 will be necessary after the cover is replaced over C144.

1. Set the Sine Wave Generator frequency to 100 kHz.
 2. Set the Type 7A22 VOLTS/DIV to 20 mV.
 3. Adjust—C108C for minimum vertical deflection.
 4. ADJUST—R116 for minimum vertical deflection.
- i. CHECK—Using Table 5-11 as a guide, check the CMRR at the remaining attenuator positions.
- j. Disconnect all test leads and connectors.

Type 7A22 Controls:

VOLTS/DIV	10 mV
VARIABLE	CAL IN
POSITION	Midrange
HIGH FREQ –3 dB POINT	1 MHz
LOW FREQ –3 dB POINT	DC
AC-GND-DC (+INPUT)	GND
AC-GND-DC (–INPUT)	GND
STEP ATTEN DC BAL	Adjusted for DC balance

Time Base Controls:

Time/Div	1 ms
Triggering	+, Auto, AC, Int

23. Check Input OVERDRIVE Indicator

a. Connect a coaxial cable between the Sine-Wave Generator output and the Type 7A22 +INPUT.

b. Set the Sine-Wave Generator amplitude to minimum and frequency to 1 kHz.

c. Increase the Sine-Wave Generator amplitude until the Type 7A22 Input OVERDRIVE indicator lights.

d. CHECK—The sine-wave amplitude, peak to peak, and divide by 2 to find the + or – driving signal amplitude. The overdrive signal should be approximately 1 volt.

e. Disconnect coaxial cable from +Input.

Type 7A22 Controls:

VOLTS/DIV	1 mV
VARIABLE	CAL IN
POSITION	Midrange
HF –3 dB POINT	1 MHz
LF –3 dB POINT	10 Hz
AC-GND-DC (+INPUT)	GND

TABLE 5-11

Sine-Wave Gen.		Type 7A22			
Amplitude	Frequency	VOLTS/DIV switch	+ and - INPUT switches	Vertical Deflection (max)	CMRR (min)
20 V p-p	100 kHz	1 mV	AC	1 div	20,000:1
	100 kHz	20 mV	AC	2.2 div	450:1
	100 kHz	20 mV	DC	2 div	500:1
	1 kHz	20 mV	DC	1 div	1,000:1
	1 kHz	20 mV	AC	1.1 div	900:1
	60 Hz	2 mV	AC	5 div	2,000:1

AC-GND-DC (-INPUT) GND
 STEP ATTEN DC BAL Adjusted for DC balance

Time Base Controls:

Time/Div 1 ms
 Variable Calibrated
 Slope +
 Coupling AC
 Source Int
 Mode Auto

b. Set the Standard Amplitude Calibrator amplitude to 1 V, +DC.

c. Connect a coaxial cable between the Sine-Wave Generator output and the Type 7A22 -INPUT.

d. Switch the - AC-GND-DC switch to DC.

e. Adjust the Sine-Wave Generator amplitude control to give 5 div of 1 kHz display on the CRT graticule (if the Sine-Wave Generator minimum amplitude is too large, insert an attenuator between the coaxial cable and the -INPUT).

f. Switch the + AC-GND-DC switch to DC.

g. CHECK—Sine wave amplitude should not change when DC level from Standard Amplitude Calibrator is applied.

h. Switch the + AC-GND-DC switch to GND.

24. Check Differential Signal Range

a. Connect a coaxial cable between the Standard Amplitude Calibrator output connector and the Type 7A22 +INPUT.

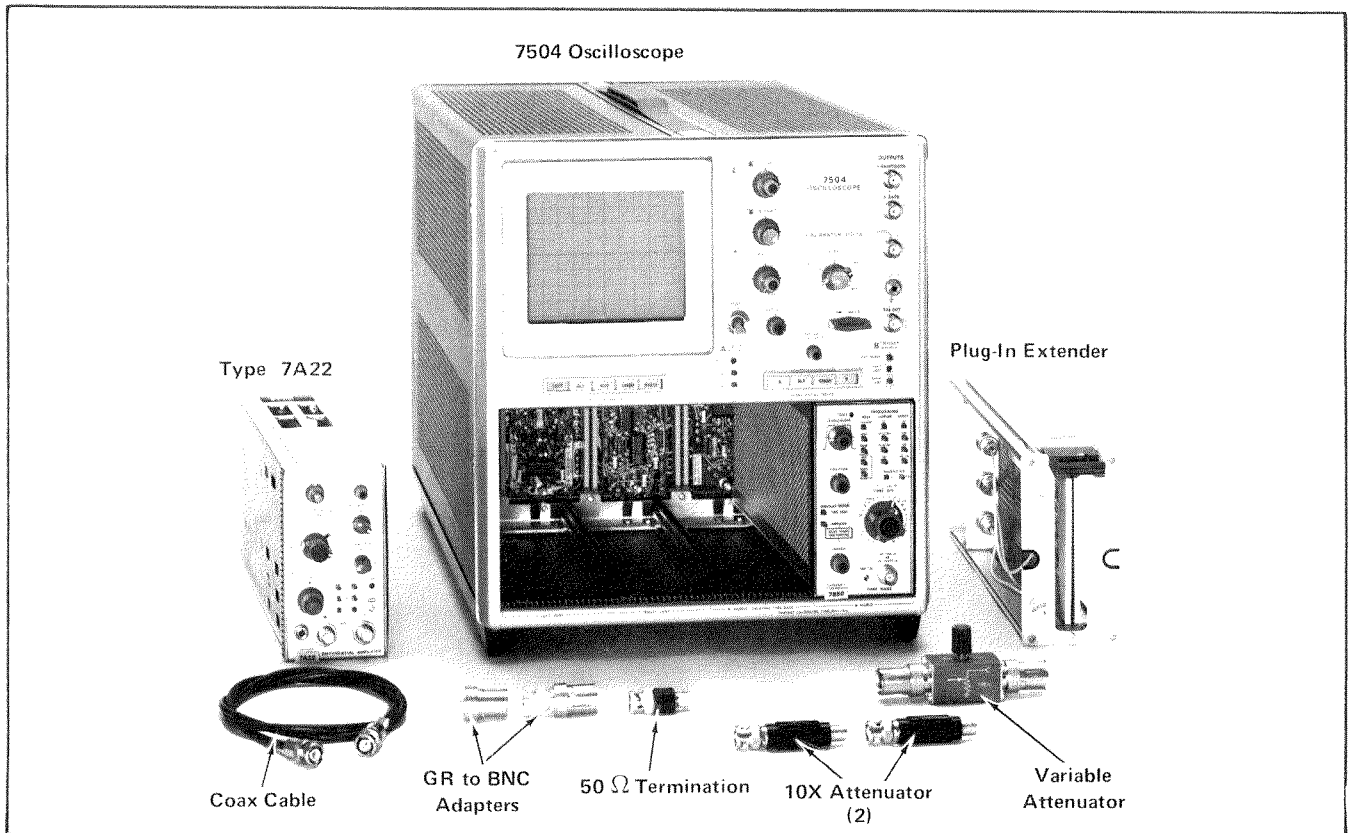


Fig. 5-9. Equipment required for step 25.

- i. Switch the Standard Amplitude Calibrator mode to -DC.
- j. Switch the + AC-GND-DC switch to DC.
- k. CHECK—Sine wave amplitude should not change when DC level from Standard Amplitude Calibrator is applied.
- l. Disconnect all test equipment and test leads.

25. Check Overall Noise Level Tangentially

- a. Equipment setup is shown in Fig. 5-9.
- b. Connect a 50 Ω termination to the +INPUT connector of the Type 7A22. Connect two 10× attenuators to the 50 Ω termination.
- c. Connect a GR to BNC adapter to the Oscilloscope Calibrator +Volts connector and connect the Variable Attenuator to the GR connector. Connect a GR to BNC adapter to the Variable Attenuator. Connect a coaxial cable from the 10× attenuators to the Variable Attenuator.
- d. Set the Oscilloscope Calibrator to 4 mV and 1 kHz.
- e. Set the 7A22 VOLTS/DIV switch to 10 μV.
- f. Turn the Variable Attenuator fully clockwise.
- g. Set the Time Base Triggering Mode to Auto and Time/Div to 10 μs.
- h. Turn the Variable Attenuator counterclockwise until the darker band between the two noise bands just disappears (see Fig. 5-10).
- i. Set the Type 7A22 VOLTS/DIV switch to 1 mV and the Time Base Time/Div switch to 1 ms.
- j. Remove the two 10× attenuators and connect the coaxial cable to the 50 Ω termination.
- k. Measure the square wave amplitude. Calculate the tangentially measured display noise as follows:

$$\frac{\text{square wave amplitude}}{100}$$

The tangentially measured noise should not exceed 16 μV.

Type 7A22 controls:

VOLTS/DIV	.2 V
VARIABLE (VOLTS/DIV)	CAL IN
POSITION	1 division below graticule center
HF -3 dB POINT	1 MHz
LF -3 dB POINT	DC
AC-GND-DC +INPUT	DC
AC-GND-DC -INPUT	GND
STEP ATTEN DC BAL	Adjusted for Dc Balance

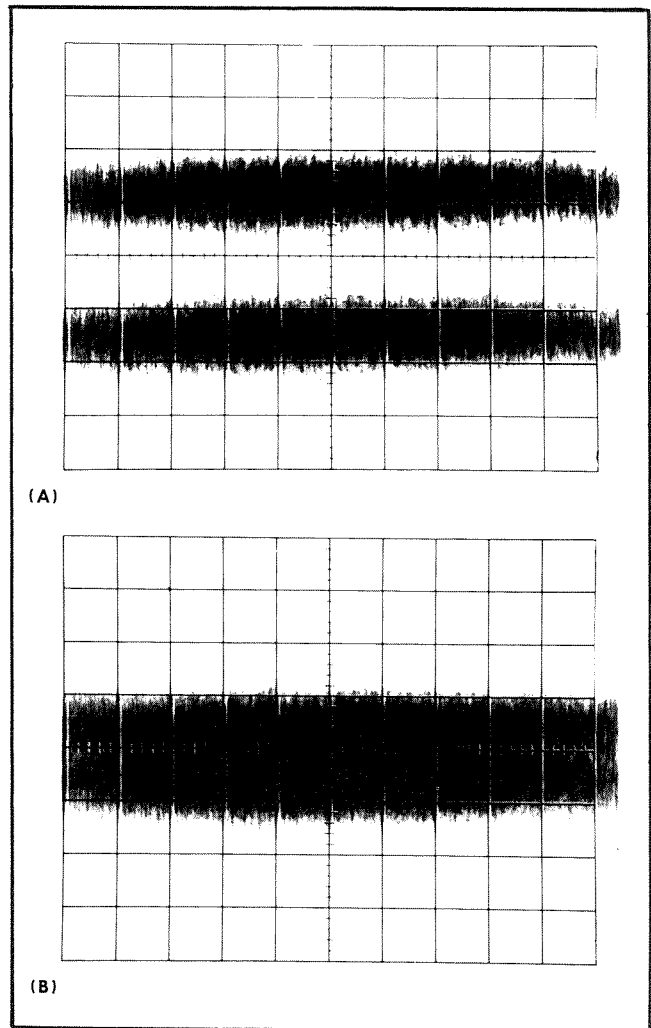


Fig. 5-10. Typical display showing (A) two noise bands and (B) merging noise bands.

Time Base controls:

Time/Div	2 μs
Variable	Cal In
Level/Slope	—
Coupling	DC
Source	Int
Mode	Auto

26. Check Overdrive Recovery Time

- a. Test equipment setup is shown in Fig. 5-11.
- b. Connect the Reed Pulse Generator Output to the Type 7A22 +INPUT.
- c. Connect the Reed Pulse Generator Power Cable to the Oscilloscope Probe Power connector (rear panel).
- d. Switch the Reed Pulse Generator Polarity switch to +.

Performance Check/Calibration—Type 7A22

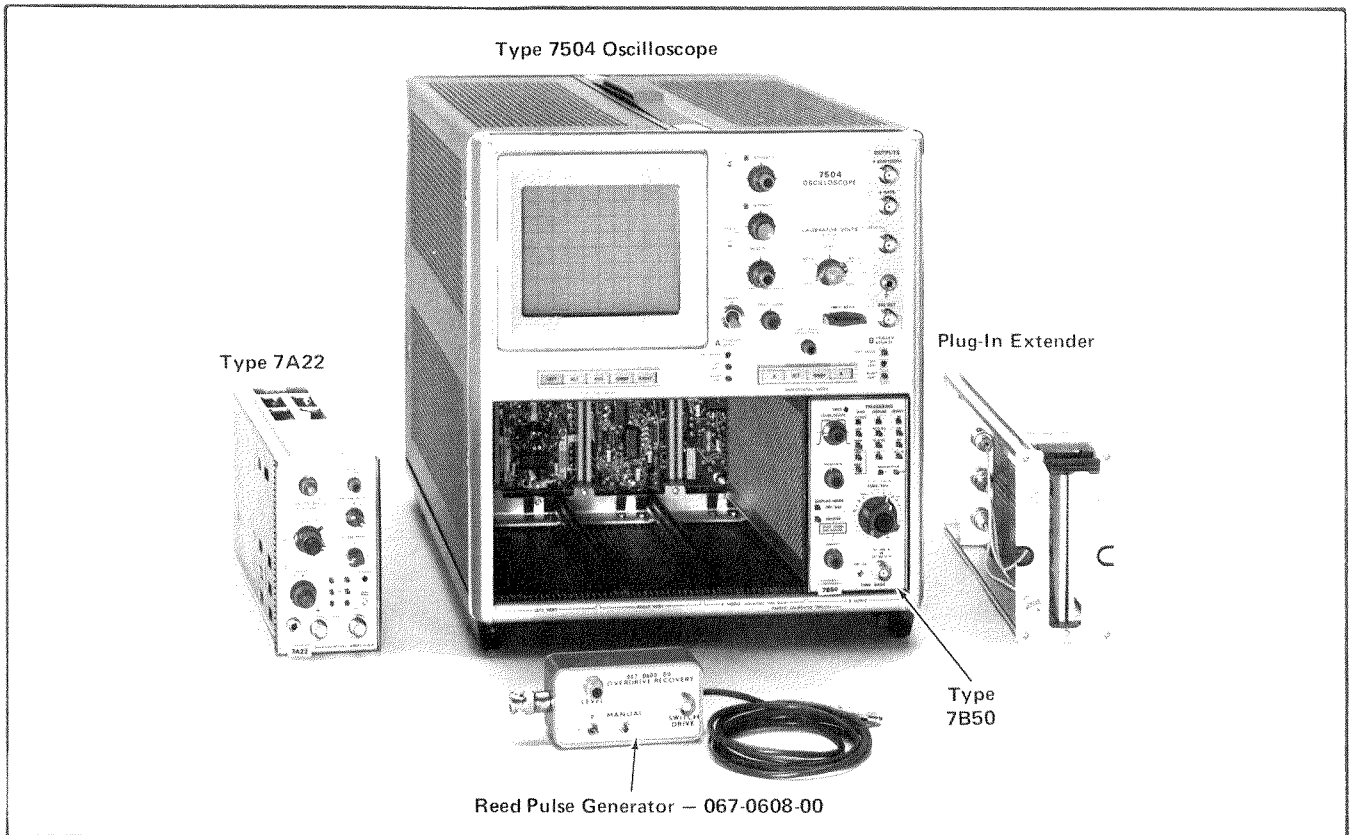


Fig. 5-11. Equipment required for step 26.

e. Hold down the Reed Pulse Generator Man switch and adjust the Level control to position the trace to the top graticule line (1 volt).

f. Release the Man switch and switch the Time Base Mode to Norm.

g. Set the Type 7A22 VOLTS/DIV switch to 1 mV.

h. Increase sweep Intensity to maximum (clockwise).

i. Depress the Reed Pulse Generator Man switch for 1 second.

j. CHECK—The waveform as the Reed Pulse Generator Man switch is released. Readjust the Time Base Level-Slope as necessary for proper triggering. The trace should return to within 5 mV of the reference (1 division below the graticule center) within 10 μ s (5 divisions). See Fig. 5-12 for photograph of typical recovery waveform.

k. Switch the Reed Pulse Generator Polarity switch to —.

l. Set the Time Base Level/Slope to +.

m. Decrease sweep Intensity for normal viewing.

n. Reset the Time Base Mode to Auto.

o. Position the trace (7A22 POSITION control) to 1 division above graticule center.

p. Reset the Time Base Mode to Norm.

q. Repeat parts h through j (waveform will be inverted from that in step j).

r. Connect Reed Pulse Generator Output to the 7A22 —INPUT and repeat above procedure.

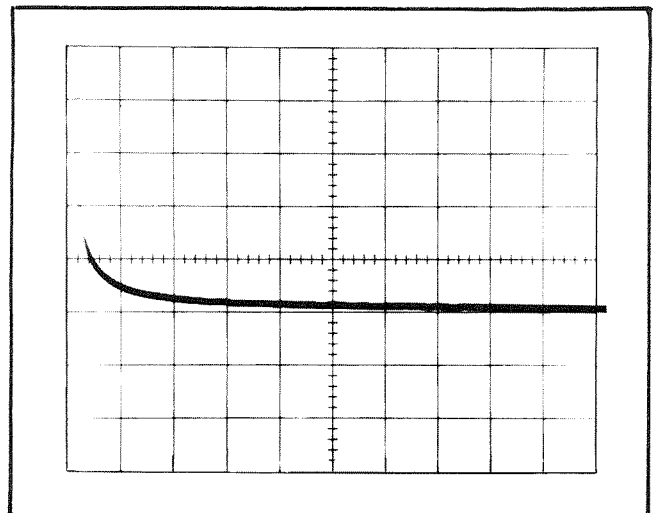


Fig. 5-12. Typical waveform showing overdrive recovery time.

PARTS LIST ABBREVIATIONS

BHB	binding head brass	int	internal
BHS	binding head steel	lg	length or long
cap.	capacitor	met.	metal
cer	ceramic	mtg hdw	mounting hardware
comp	composition	OD	outside diameter
conn	connector	OHB	oval head brass
CRT	cathode-ray tube	OHS	oval head steel
csk	countersunk	P/O	part of
DE	double end	PHB	pan head brass
dia	diameter	PHS	pan head steel
div	division	plstc	plastic
elect.	electrolytic	PMC	paper, metal cased
EMC	electrolytic, metal cased	poly	polystyrene
EMT	electrolytic, metal tubular	prec	precision
ext	external	PT	paper, tubular
F & I	focus and intensity	PTM	paper or plastic, tubular, molded
FHB	flat head brass	RHB	round head brass
FHS	flat head steel	RHS	round head steel
Fil HB	fillister head brass	SE	single end
Fil HS	fillister head steel	SN or S/N	serial number
h	height or high	S or SW	switch
hex.	hexagonal	TC	temperature compensated
HHB	hex head brass	THB	truss head brass
HHS	hex head steel	thk	thick
HSB	hex socket brass	THS	truss head steel
HSS	hex socket steel	tub.	tubular
ID	inside diameter	var	variable
inc	incandescent	w	wide or width
		WW	wire-wound

PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix, Inc. Field Office or representative.

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number, instrument type or number, serial or model number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number.

SPECIAL NOTES AND SYMBOLS

- | | |
|-----------------|---|
| ×000 | Part first added at this serial number |
| 00× | Part removed after this serial number |
| *000-0000-00 | Asterisk preceding Tektronix Part Number indicates manufactured by or for Tektronix, Inc., or reworked or checked components. |
| Use 000-0000-00 | Part number indicated is direct replacement. |

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SECTION 6

ELECTRICAL PARTS LIST

Values are fixed unless marked Variable.

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description
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CHASSIS

Bulb

DS301	150-0046-00			Incandescent #21070
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Capacitors

Tolerance $\pm 20\%$ unless otherwise indicated.

C101 ¹ }			0.1 μ F	MT	600 V	+5%—15%
C201 }	*295-0117-00		0.1 μ F	MT	600 V	+5%—15%

Connectors

J101	131-0679-00			BNC, receptacle electrical
J201	131-0579-00			BNC, receptacle electrical

Resistors

Resistors are fixed, composition, $\pm 10\%$ unless otherwise indicated.

R258	311-0467-00			100 k Ω , Var		
R265	311-0887-00			50 k Ω , Var		
R270	311-0889-00			5 k Ω , Var		
R430 ²	311-0888-00	B010100	B069999	2 x 10 k Ω , Var		
R430 ²	311-0888-01	B070000		2 x 10 k Ω , Var		
R535 ³	311-0949-00			2 k Ω , Var		
R540	311-0702-00			250 Ω , Var		
R622	315-0222-00			2.2 k Ω	1/4 W	5%

Switches

Wired or Unwired

S535 ⁴	311-0949-00					VARIABLE
S622 ⁵	311-0888-00	B010100	B069999			IDENTIFY
S622 ⁵	311-0888-01	B070000				IDENTIFY

BANDWIDTH Circuit Board Assembly

*670-1013-00

Complete Board

Capacitors

Tolerance $\pm 20\%$ unless otherwise indicated.

C307	290-0297-00			39 μ F	Elect.	10 V	10%
C315	281-0613-00			10 pF	Cer	200 V	10%
C317	290-0297-00			39 μ F	Elect.	10 V	10%
C325	290-0297-00			39 μ F	Elect.	10 V	10%
C329	283-0002-00			0.01 μ F	Cer	50 V	10%

¹Matched pair.

²Furnished as a unit with S622.

³Furnished as a unit with S535.

⁴Furnished as a unit with R535.

⁵Furnished as a unit with R430.

BANDWIDTH Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description		
Capacitors (cont)						
C330	281-0114-00		1.3-5.4 pF, Var	Air		
C349	290-0177-00		1 μ F	EMT	50 V	
C353 A	285-0809-00		1 μ F	PTM	50 V	10%
C353 D	285-0809-00		1 μ F	PTM	50 V	10%
C353 G	283-0058-00		0.027 μ F	Cer	100 V	10%
C425	281-0093-00		5.5-18 pF, Var	Cer		
C426 A	281-0528-00		82 pF	Cer	500 V	10%
C426 B	283-0604-00		304 pF	Mica	300 V	2%
C426 C	283-0594-00		0.001 μ F	Mica	100 V	1%
C426 D	285-0627-00		0.0033 μ F	PTM	100 V	5%
C426 E	285-0598-00		0.01 μ F	PTM	100 V	5%
C426 F	285-0702-00		0.033 μ F	PTM	100 V	5%
C426 G	285-0703-00		0.1 μ F	PTM	100 V	5%
C426 H	285-0633-00		0.22 μ F	PTM	100 V	10%

Semiconductor Device, Diodes

VR305	152-0306-00	B010100	B029999	Zener	1N960B	400 mW, 9.1 V, 5%
VR305	152-0168-00	B030000		Zener	1N963A	400 mW, 12 V, 5%
CR309	*152-0185-00			Silicon	Replaceable by 1N4152	
CR310	*152-0405-00			Zener	Tek Spec 1 W, 15 V, 5%	
VR320	152-0280-00			Zener	1N753A	400 mW, 6.2 V, 5%
VR325	152-0212-00			Zener	1N936	500 mW, 9 V, 5%, TC
VR330	152-0280-00			Zener	1N753A	400 mW, 6.2 V, 5%
CR341	*152-0185-00			Silicon	Replaceable by 1N4152	
CR343	*152-0185-00			Silicon	Replaceable by 1N4152	

Transistors

Q304	151-0260-00			Silicon	2N5189	
Q314	151-0220-00			Silicon	2N4122	
Q324	*151-0228-00			Silicon	Tek Spec	
Q334	*151-0228-00			Silicon	Tek Spec	
Q344	*151-0195-00			Silicon	Replaceable by MPS 6515	
Q354	*151-0195-00			Silicon	Replaceable by MPS 6515	

Resistors

Resistors are fixed, composition, $\pm 10\%$ unless otherwise indicated.

R301	323-0256-00		4.53 k Ω	1/2 W	Prec	1%
R303	317-0511-00		510 Ω	1/8 W		5%
R305	315-0103-00		10 k Ω	1/4 W		5%
R307	315-0103-00		10 k Ω	1/4 W		5%
R309	315-0512-00		5.1 k Ω	1/4 W		5%
R311	315-0513-00		51 k Ω	1/4 W		5%
R313	315-0101-00		100 Ω	1/4 W		5%
R315	315-0222-00		2.2 k Ω	1/4 W		5%
R323	301-0562-00		5.6 k Ω	1/2 W		5%
R325	321-0160-00		453 Ω	1/8 W	Prec	1%

BANDWIDTH Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description		
Resistors (cont)						
R331	315-0473-00		47 k Ω	1/4 W		5%
R333	315-0433-00		43 k Ω	1/4 W		5%
R335	321-0184-00		806 Ω	1/8 W	Prec	1%
R337	315-0104-00		100 k Ω	1/4 W		5%
R341	315-0364-00		360 k Ω	1/4 W		5%
R343	315-0363-00		36 k Ω	1/4 W		5%
R345	315-0105-00		1 M Ω	1/4 W		5%
R347	315-0304-00		300 k Ω	1/4 W		5%
R349	315-0204-00		200 k Ω	1/4 W		5%
R351	315-0151-00		150 Ω	1/4 W		5%
R353 A	321-0303-00		14 k Ω	1/8 W	Prec	1%
R353 B	321-0408-00		174 k Ω	1/8 W	Prec	1%
R353 C	301-0165-00		1.6 M Ω	1/2 W		5%
R353 D	321-0303-00		14 k Ω	1/8 W	Prec	1%
R353 E	321-0408-00		174 k Ω	1/8 W	Prec	1%
R353 F	301-0165-00		1.6 M Ω	1/2 W		5%
Switches						
	Wired or Unwired					
S353 ⁶	*670-1013-00		Cam		LF 3 dB POINT	
S426 ⁶	*670-1013-00		Cam		LF 3 dB POINT	

MAIN Circuit Board Assembly

*670-1014-00	B010100	B029999	Complete Board
*670-1014-01	B030000	B059999	Complete Board
*670-1014-03	B060000	B069999	Complete Board
*670-1014-04	B070000	B079999	Complete Board
*670-1014-05	B080000		Complete Board

Capacitors

Tolerance $\pm 20\%$ unless otherwise indicated.

C108 A	281-0092-00	B010100	B059999	9-35 pF, Var	Cer		
C108 A	281-0131-00	B060000		2.4-24.5 pF, Var	Air	250 V	
C108 C	281-0131-00			2.4-24.5 pF, Var	Air	250 V	
C108 D	283-0638-00			130 pF	Mica	100 V	
C109 A	281-0092-00	B010100	B059999	9-35 pF, Var	Cer		1%
C109 A	281-0131-00	B060000		2.4-24.5 pF, Var	Air	250 V	
C109 C	281-0081-00			1.8-13 pF, Var	Air		
C109 D	283-0594-00			0.001 μ F	Mica	100 V	
C110 A	281-0092-00	B010100	B059999	9-35 pF, Var	Cer		1%
C110 A	281-0131-00	B060000		2.4-24.5 pF, Var	Air	250 V	
C110 C	281-0079-00			1.5-9.1 pF, Var	Air		
C110 D	283-0617-00			4700 pF	Mica	300 V	
C115	281-0092-00	B010100	B059999	9-35 pF, Var	Cer		10%
C115	281-0131-00	B060000		2.4-24.5 pF, Var	Air	250 V	
C141	281-0093-00			5.5-18 pF, Var	Cer		
C144	281-0544-00	B010100	B079999	5.6 pF	Cer	500 V	10%
C144	281-0122-00	B080000		2.5-9 pF, Var	Cer	100 V	
C156	283-0594-00			0.001 μ F	Mica	100 V	1%
C208 A	281-0092-00	B010100	B059999	9-35 pF, Var	Cer		
C208 A	281-0131-00	B060000		2.4-24.5 pF, Var	Air	250 V	
C208 C	281-0131-00			2.4-24.5 pF, Var	Air	250 V	

⁶See Mechanical Parts List for replacement parts.

MAIN Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description	
Capacitors (cont)					
C208 D	283-0638-00			130 pF	Mica 100 V 1%
C209 A	281-0092-00	B010100	B059999	9-35 pF, Var	Cer
C209 A	281-0131-00	B060000		2.4-24.5 pF, Var	Air 250 V
C209 C	281-0081-00			1.8-13 pF, Var	Air
C209 D	283-0594-00			0.001 μ F	Mica 100 V 1%
C210 A	281-0092-00	B010100	B059999	9-35 pF, Var	Cer
C210 A	281-0131-00	B060000		2.4-24.5 pF, Var	Air 250 V
C210 C	281-0079-00			1.5-9.1 pF, Var	Air
C210 D	283-0617-00			4700 pF	Mica 300 V 10%
C215	281-0092-00	B010100	B059999	9-35 pF, Var	Cer
C215	281-0131-00	B060000		2.4-24.5 pF, Var	Air 250 V
C241	281-0093-00			5.5-18 pF, Var	Cer
C244	281-0544-00			5.6 pF	Cer 500 V 10%
C256	283-0594-00			0.001 μ F	Mica 100 V 1%
C264	283-0059-00			1 μ F	Cer 25 V +80% -20%
C283	290-0284-00			4.7 μ F	Elect. 35 V 10%
C413	281-0534-00			3.3 pF	Cer 500 V \pm 0.25 pF
C443	283-0000-00			0.001 μ F	Cer 500 V
C445	283-0092-00			0.03 μ F	Cer 200 V +80% -20%
C447	283-0111-00			0.1 μ F	Cer 50 V
C513	281-0534-00			3.3 pF	Cer 500 V \pm 0.25 pF
C543	283-0000-00			0.001 μ F	Cer 500 V
C545	283-0059-00			1 μ F	Cer 25 V +80% -20%
C547	283-0134-00			0.47 μ F	Cer 50 V +80% -20%
C610	283-0080-00			0.022 μ F	Cer 25 V +80% -20%
C620	283-0080-00			0.022 μ F	Cer 25 V +80% -20%
C630	283-0080-00			0.022 μ F	Cer 25 V +80% -20%

Semiconductor Device, Diodes

CR119	*152-0323-00			Silicon	Tek Spec
CR120	*152-0323-00			Silicon	Tek Spec
CR131	*152-0185-00			Silicon	Replaceable by 1N4152
CR144	*152-0185-00			Silicon	Replaceable by 1N4152
CR219	*152-0323-00			Silicon	Tek Spec
CR220	*152-0323-00			Silicon	Tek Spec
CR244	*152-0185-00			Silicon	Replaceable by 1N4152
CR260	*152-0185-00			Silicon	Replaceable by 1N4152
VR270	152-0212-00			Zener	1N936 500 mW, 9 V, 5%, TC
VR271	*152-0405-00			Zener	Tek Spec 1 W, 15 V, 5%
CR413	*152-0185-00			Silicon	Replaceable by 1N4152
CR415	*152-0185-00			Silicon	Replaceable by 1N4152
CR417	*152-0185-00			Silicon	Replaceable by 1N4152
CR419	*152-0185-00			Silicon	Replaceable by 1N4152
CR513	*152-0185-00			Silicon	Replaceable by 1N4152
CR515	*152-0185-00			Silicon	Replaceable by 1N4152
CR517	*152-0185-00			Silicon	Replaceable by 1N4152
CR519	*152-0185-00			Silicon	Replaceable by 1N4152
CR610	*152-0185-00			Silicon	Replaceable by 1N4152
CR615	*152-0185-00			Silicon	Replaceable by 1N4152
CR618	*152-0185-00	XB070000		Silicon	Replaceable by 1N4152
CR620	*152-0185-00			Silicon	Replaceable by 1N4152
CR630	*152-0185-00			Silicon	Replaceable by 1N4152

MAIN Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description
Fuses				
F119	159-0024-00			1/16 A 3AG Fast-Blo
F219	159-0024-00			1/16 A 3AG Fast-Blo
Transistors				
Q133	*151-1027-00			FET Dual, Tek Spec
Q144	*151-0261-00			Silicon Dual, Tek Spec
Q153	*151-1028-00			FET Tek Spec
Q253	*151-1028-00			FET Tek Spec
Q264	*151-0261-00			Silicon Dual, Tek Spec
Q273	*151-1028-00			FET Tek Spec
Q283	*151-1028-00			FET Tek Spec
Q284	*151-0195-00			Silicon Replaceable by MPS 6515
Q404	151-1019-00	B010100	B069999	FET Dual
Q404	151-1050-00	B070000		FET Dual
Q414	*151-0261-00			Silicon Dual, Tek Spec
Q424	*151-0219-00			Silicon Replaceable by 2N4250
Q434	*151-0219-00			Silicon Replaceable by 2N4250
Q444	*151-0219-00			Silicon Replaceable by 2N4250
Q454	*151-0219-00			Silicon Replaceable by 2N4250
Q524	*151-0219-00			Silicon Replaceable by 2N4250
Q534	*151-0219-00			Silicon Replaceable by 2N4250
Q544	*151-0219-00			Silicon Replaceable by 2N4250
Q554	*151-0219-00			Silicon Replaceable by 2N4250
Q614	151-0254-00			Silicon 2N5308
Resistors				
Resistors are fixed, composition, $\pm 10\%$ unless otherwise indicated.				
R108 C	323-0611-07			900 k Ω 1/2 W Prec 1/10%
R108 D	321-0389-01			110 k Ω 1/8 W Prec 1/2%
R108 E	311-0609-00			2 k Ω , Var 1/2 W Prec 1/10%
R109 C	323-0614-07			900 k Ω 1/2 W Prec 1/10%
R109 D	321-0289-00			10 k Ω 1/8 W Prec 1%
R109 E	311-0605-00			200 Ω , Var 1/2 W Prec 1/10%
R110 C	323-0623-07			900 k Ω 1/2 W Prec 1/10%
R110 D	321-0197-00			11 k Ω 1/8 W Prec 1%
R110 E	311-0609-00			2 k Ω , Var 1/2 W Prec 1/10%
R110 F	321-0289-01			10 k Ω 1/8 W Prec 1/2%
R111	323-0481-07			1 M Ω 1/2 W Prec 1/10%
R115	311-0827-00	B010100	B069999	250 Ω , Var 1/2 W Prec 1/10%
R115	311-1260-00	B070000		250 Ω , Var 1/2 W Prec 1/10%
R116	311-0635-00			1 k Ω , Var 1/2 W Prec 1/10%
R117	321-0210-00			1.5 k Ω 1/8 W Prec 1%
R119	315-0510-00			51 Ω 1/4 W Prec 5%

MAIN Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description		
Resistors (cont)						
R121	321-0344-00			37.4 kΩ	1/8 W	Prec 1%
R123	315-0151-00			150 Ω	1/4 W	5%
R133	308-0495-00			4.5 kΩ	25 W	WW 1/10%
R141	315-0512-00			5.1 kΩ	1/4 W	5%
R145	321-0289-00			10 kΩ	1/8 W	Prec 1%
R151	308-0546-00			125 Ω	3 W	WW 1/10%
R153	321-0114-00			150 Ω	1/8 W	Prec 1%
R155	315-0101-00			100 Ω	1/4 W	5%
R157	321-0030-00			20 Ω	1/8 W	Prec 1%
R159	308-0436-00			2 kΩ	3 W	WW 1/10%
R208 C	323-0611-07			900 kΩ	1/2 W	Prec 1/10%
R208 D	321-0389-01			110 kΩ	1/8 W	Prec 1/2%
R209 C	323-0614-07			990 kΩ	1/2 W	Prec 1/10%
R209 D	321-0289-00			10 kΩ	1/8 W	Prec 1%
R210 C	323-0623-07			999 kΩ	1/2 W	Prec 1/10%
R210 D	321-0197-00			1.1 kΩ	1/8 W	Prec 1%
R210 F	321-0289-01			10 kΩ	1/8 W	Prec 1/2%
R211	323-0481-07			1 MΩ	1/2 W	Prec 1/10%
R215	311-0827-00	B010100	B069999	250 Ω, Var		
R215	311-1260-00	B070000		250 Ω, Var		
R217	321-0222-00			2 kΩ	1/8 W	Prec 1%
R219	315-0510-00			51 Ω	1/4 W	5%
RT221	307-0181-00			100 kΩ	Thermal	
RT223	307-0181-00			100 kΩ	Thermal	
R227	315-0562-00			5.6 kΩ	1/4 W	5%
R233	308-0495-00			4.5 kΩ	25 W	WW 1/10%
R241	315-0512-00			5.1 kΩ	1/4 W	5%
R245	321-0289-00			10 kΩ	1/8 W	Prec 1%
R251	308-0546-00			125 Ω	3 W	WW 1/10%
R253	321-0114-00			150 Ω	1/8 W	Prec 1%
R255	315-0101-00			100 Ω	1/4 W	5%
R256	315-0185-00			1.8 MΩ	1/4 W	5%
R257	321-0030-00			20 Ω	1/8 W	Prec 1%
R259	308-0436-00			2 kΩ	3 W	WW 1/10%
R261	321-0126-00			200 Ω	1/8 W	Prec 1%
R263	321-0385-00			100 kΩ	1/8 W	Prec 1%
R264	321-0414-00			200 kΩ	1/8 W	Prec 1%
R267	321-0385-00			100 kΩ	1/8 W	Prec 1%
R269	321-0126-00			200 Ω	1/8 W	Prec 1%
R271	308-0436-00			2 kΩ	3 W	WW 1/10%
R273	321-0114-00			150 Ω	1/8 W	Prec 1%
R275	311-0532-00			1.5 kΩ, Var		
R277	321-0114-00			150 Ω	1/8 W	Prec 1%
R279	308-0436-00			2 kΩ	3 W	WW 1/10%
R281	321-0126-00			200 Ω	1/8 W	Prec 1%
R283	323-0220-00			1.91 kΩ	1/2 W	Prec 1%

MAIN Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description		
Resistors (cont)						
R285	321-0423-00			249 k Ω	1/8 W	Prec 1%
R287	308-0633-00			475 Ω	3 W	WW 1/4%
R289	308-0633-00			475 Ω	3 W	WW 1/4%
R291	321-0423-00			249 k Ω	1/8 W	Prec 1%
R293	323-0214-00			1.65 k Ω	1/2 W	Prec 1%
R401	315-0202-00			2 k Ω	1/4 W	
R403	321-0272-00			6.65 k Ω	1/8 W	Prec 1%
R405	321-0039-00			24.9 Ω	1/8 W	Prec 1%
R407 A	321-0001-01			10 Ω	1/8 W	Prec 1/2%
R407 B	321-0762-01			20.1 Ω	1/8 W	Prec 1/2%
R407 C	321-1058-01			50.5 Ω	1/8 W	Prec 1/2%
R407 D	321-0078-01			102 Ω	1/8 W	Prec 1/2%
R407 E	321-0127-01			205 Ω	1/8 W	Prec 1/2%
R407 F	321-1166-01			530 Ω	1/8 W	Prec 1/2%
R407 G	321-0763-07			1.12 k Ω	1/8 W	Prec 1/10%
R407 H	321-1231-01			2.52 k Ω	1/8 W	Prec 1/2%
R407 J	321-1289-01			10.1 k Ω	1/8 W	Prec 1/2%
R407 K	321-0332-00			28 k Ω	1/8 W	Prec 1%
R409	321-0343-00			36.5 k Ω	1/8 W	Prec 1%
R413	321-0361-00			56.2 k Ω	1/8 W	Prec 1%
R421	321-0614-00			10.1 k Ω	1/8 W	Prec 1%
R423	315-0101-00			100 Ω	1/4 W	
R425	311-0704-00	B010100	B069999	500 Ω , Var		5%
R425	311-1261-00	B070000		500 Ω , Var		
R427	321-0227-00			2.26 k Ω	1/8 W	Prec 1%
R431	321-0334-00			29.4 k Ω	1/8 W	Prec 1%
R432	321-0164-00			499 Ω	1/8 W	Prec 1%
R433	321-0335-00			30.1 k Ω	1/8 W	Prec 1%
R434	321-0211-00			1.54 k Ω	1/8 W	Prec 1%
R436	321-0197-00			1.1 k Ω	1/8 W	Prec 1%
R437	321-0260-00			4.99 k Ω	1/8 W	Prec 1%
R440	321-0143-00	B010100	B029999	301 Ω	1/8 W	Prec 1%
R440	321-0149-00	B030000		348 Ω	1/8 W	Prec 1%
R441	321-0222-00	B010100	B029999	2 k Ω	1/8 W	Prec 1%
R441	321-0202-00	B030000		1.24 k Ω	1/8 W	Prec 1%
R443	317-0561-00	B010100	B029999	550 Ω	1/8 W	5%
R443	317-0391-00	B010100	B089999	390 Ω	1/3 W	5%
R443	315-0391-00	B090000		390 Ω	1/4 W	5%
R445	321-0251-00	B010100	B029999	4.02 k Ω	1/8 W	Prec 1%
R445	321-0231-00	B030000		2.49 k Ω	1/8 W	Prec 1%
R450	321-0147-00	B010100	B029999	332 Ω	1/8 W	Prec 1%
R450	321-0149-00	B030000		348 Ω	1/8 W	Prec 1%
R451	321-0222-00	B010100	B029999	2 k Ω	1/8 W	Prec 1%
R451	321-0202-00	B030000		1.24 k Ω	1/8 W	Prec 1%
R453	321-0251-00	B010100	B029999	4.02 k Ω	1/8 W	Prec 1%
R453	321-0231-00	B030000		2.49 k Ω	1/8 W	Prec 1%
R457	317-0241-00	B010100	B089999	240 Ω	1/8 W	
R457	315-0241-00	B090000		240 Ω	1/4 W	5%
R458	301-0470-00			47 Ω	1/2 W	5%
R459	315-0510-00			51 Ω	1/4 W	5%
R501	315-0202-00			2 k Ω	1/4 W	5%
R503	321-0272-00			6.65 k Ω	1/8 W	Prec 5%
R505	311-0839-00			50 Ω , Var		1%

MAIN Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description
Resistors (cont)				
R509	321-0343-00			36.5 k Ω 1/8 W Prec 1%
R513	321-0361-00			56.2 k Ω 1/8 W Prec 1%
R521	321-0614-00			10.1 k Ω 1/8 W Prec 1%
R523	315-0101-00			100 Ω 1/4 W 5%
R527	321-0227-00			2.26 k Ω 1/8 W Prec 1%
R531	321-0334-00			29.4 k Ω 1/8 W Prec 1%
R532	321-0164-00			499 Ω 1/8 W Prec 1%
R533	321-0335-00			30.1 k Ω 1/8 W Prec 1%
R534	321-0211-00			1.54 k Ω 1/8 W Prec 1%
R541	321-0222-00	B010100	B029999	2 k Ω 1/8 W Prec 1%
R541	321-0202-00	B030000		1.24 k Ω 1/8 W Prec 1%
R543	317-0561-00	B010100	B029999	560 Ω 1/8 W 5%
R543	317-0391-00	B030000	B089999	390 Ω 1/8 W 5%
R543	315-0391-00	B090000		390 Ω 1/4 W 5%
R545	321-0251-00	B010100	B029999	4.02 k Ω 1/8 W Prec 1%
R545	321-0231-00	B030000		2.49 k Ω 1/8 W Prec 1%
R551	321-0222-00	B010100	B029999	2 k Ω 1/8 W Prec 1%
R551	321-0202-00	B030000		1.24 k Ω 1/8 W Prec 1%
R553	321-0251-00	B010100	B029999	4.02 k Ω 1/8 W Prec 1%
R553	321-0231-00	B030000		2.49 k Ω 1/8 W Prec 1%
R557	317-0241-00	B010100	B089999	240 Ω 1/8 W 5%
R557	315-0241-00	B090000		240 Ω 1/4 W 5%
R559	315-0510-00			51 Ω 1/4 W 5%
R610	321-0223-00			2.05 k Ω 1/8 W Prec 1%
R614	321-0299-00			12.7 k Ω 1/8 W Prec 1%
R618 A	315-0154-00			150 k Ω 1/4 W 5%
R618 B	321-0344-00			37.4 k Ω 1/8 W Prec 1%
R618 C	315-0154-00			150 k Ω 1/4 W 5%
R618 D	315-0753-00			75 k Ω 1/4 W 5%
R618 E	315-0753-00			75 k Ω 1/4 W 5%
R618 F	315-0154-00			150 k Ω 1/4 W 5%
R618 G	315-0753-00			75 k Ω 1/4 W 5%
R618 H	315-0753-00			75 k Ω 1/4 W 5%
R618 J	315-0123-00			12 k Ω 1/4 W 5%
R618 K	321-0344-00			37.4 k Ω 1/8 W Prec 1%
R618 L	315-0753-00			75 k Ω 1/4 W 5%
R618 M	315-0154-00			150 k Ω 1/4 W 5%
R618 N	315-0513-00			51 k Ω 1/4 W 5%
R620	321-0223-00			2.05 k Ω 1/8 W Prec 1%
R630	315-0104-00			100 k Ω 1/4 W 5%
R632	317-0223-00	B030000	B089999	22 k Ω 1/8 W 5%
R632	315-0223-00	B090000		22 k Ω 1/4 W 5%
R634	317-0203-00	B010100	B089999	20 k Ω 1/8 W 5%
R634	315-0203-00	B090000		20 k Ω 1/4 W 5%
Switches				
Wired or Unwired				
S108 [†]	*670-1014-00	B010100	B029999	Cam VOLTS/DIV
S108 [†]	*670-1014-01	B030000	B059999	Cam VOLTS/DIV
S108 [†]	*670-1014-03	B060000	B069999	Cam VOLTS/DIV
S108 [†]	*670-1014-04	B070000		Cam VOLTS/DIV
S407 [†]	*670-1014-00	B010100	B029999	Cam GAIN and READOUT
S407 [†]	*670-1014-01	B030000	B059999	Cam GAIN and READOUT
S407 [†]	*670-1014-03	B060000	B069999	Cam GAIN and READOUT
S407 [†]	*670-1014-04	B070000		Cam GAIN and READOUT

[†]See Mechanical Parts List for replacement parts.

—AC-GND-DC Circuit Board Assembly

Ckt. No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Description
	*670-1050-00	B010100	B059999	Complete Board
	*670-1050-01	B060000		Complete Board

Bulb

DS101	150-0093-00	B010100	B039999	Incandescent T3/4
DS101	*150-0057-01	B040000	B059999	Incandescent 7153AS15, selected
DS101	*150-0048-01	B060000		Incandescent #683, selected

CapacitorTolerance $\pm 20\%$ unless otherwise indicated.

C103	283-0636-00		36 pF	Mica	100 V	± 0.5 pF
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ResistorResistors are fixed, composition, $\pm 10\%$ unless otherwise indicated.

R103	315-0105-00		1 M Ω	$\frac{1}{4}$ W		5%
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Switch

Wired or Unwired

S101A, B ^s	*670-1050-00	B010100	B059999	Pushbutton	—AC-GND-DC
S101A, B ^s	*670-1050-01	B060000		Pushbutton	—AC-GND-DC

+AC-GND-DC Circuit Board Assembly

	*670-1051-00	B010100	B059999	Complete Board
	*670-1051-01	B060000		Complete Board

Bulb

DS201	150-0093-00	B010100	B039999	Incandescent T3/4
DS201	*150-0057-01	B040000	B059999	Incandescent 7153AS15, selected
DS201	*150-0048-01	B060000		Incandescent #683, selected

CapacitorTolerance $\pm 20\%$ unless otherwise indicated.

C203	283-0636-00		36 pF	Mica	100 V	± 0.5 pF
------	-------------	--	-------	------	-------	--------------

ResistorResistors are fixed, composition, $\pm 10\%$ unless otherwise indicated.

R203	315-0105-00		1 M Ω	$\frac{1}{4}$ W		5%
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Switch

Wired or Unwired

S201A, B ^s	*670-1051-00	B010100	B059999	Pushbutton	+AC-GND-DC
S201A, B ^s	*670-1051-01	B060000		Pushbutton	+AC-GND-DC

^sSee Mechanical Parts List for replacement parts.

SECTION 7

DIAGRAMS

and

MECHANICAL PARTS ILLUSTRATIONS

The following special symbols are used on the diagrams:



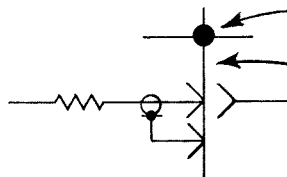
Screwdriver adjustment



Front or rear-panel control or connector.

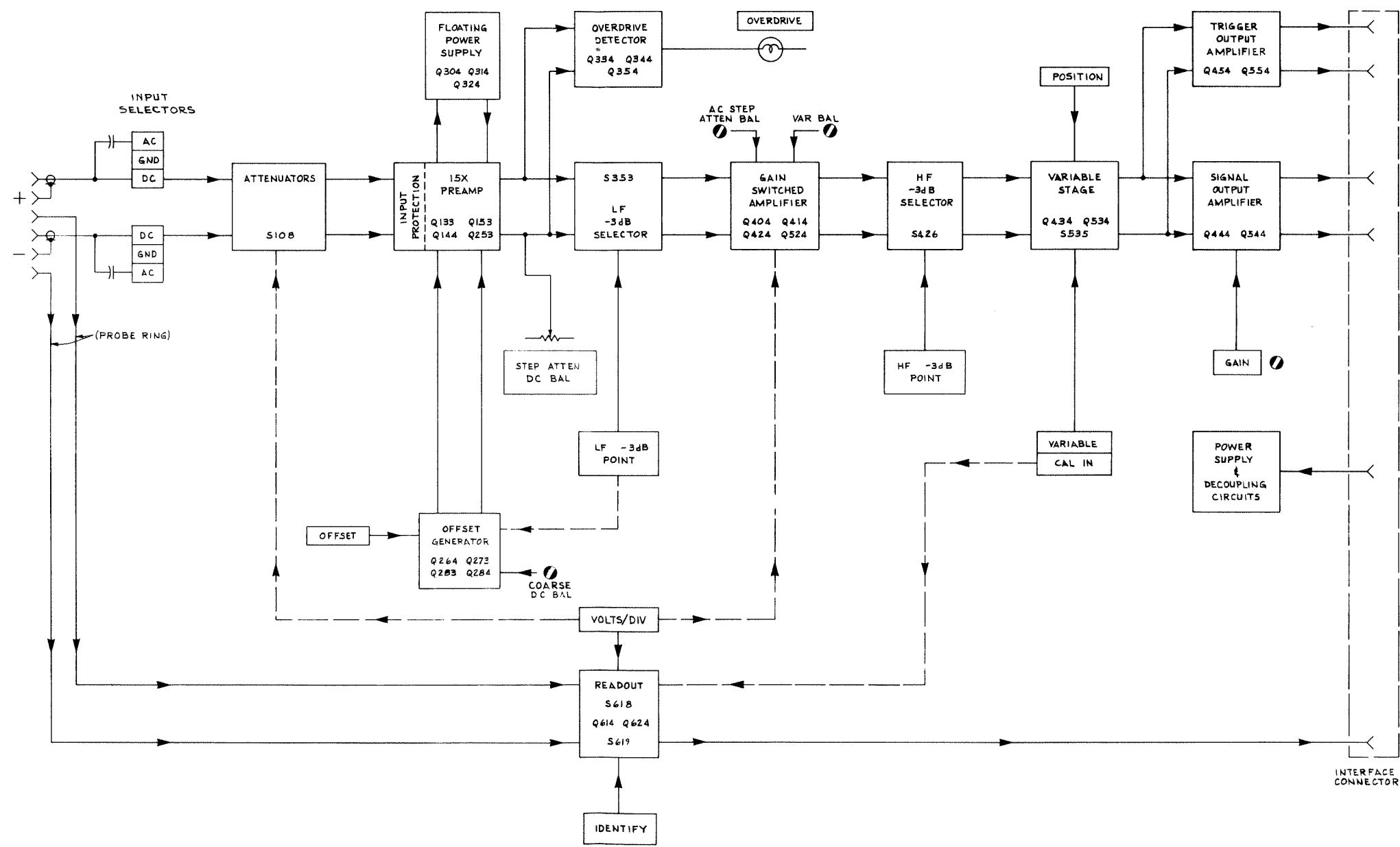


Refers to the indicated diagram.



Connection soldered to circuit board.

Blue line encloses components located on circuit board.



VOLTAGE AND WAVEFORM CONDITIONS

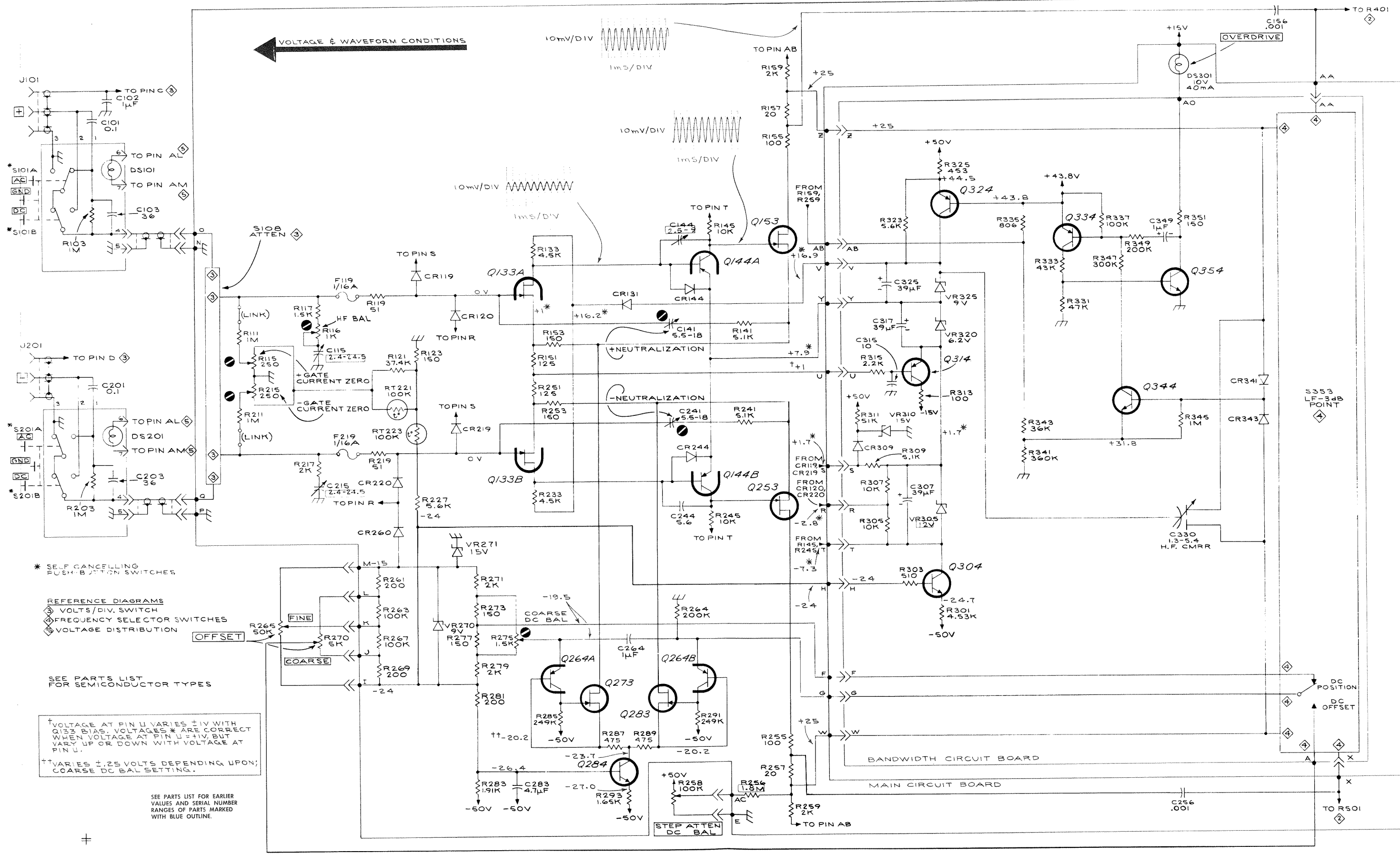
Waveforms show amplitude of signal at various points with **-INPUT** grounded and 25 mV peak to peak sine wave of approximately 1 kHz applied to **+ INPUT**. Front panel controls set as follows:

LF -3 dB Point	DC
HF -3 dB Point	1 MHz
VOLTS/DIV	As shown at waveform

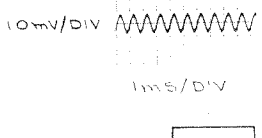
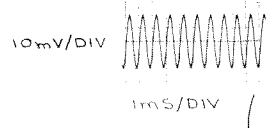
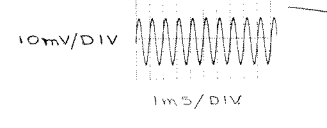
DC voltages measured with front panel controls set as follows:

LF -3 dB Point	DC
VOLTS/DIV	1 mV
POSITION	Trace centered vertically
+INPUT AC-GND-DC	GND
-INPUT AC-GND-DC	GND

X15 PREAMP 1



VOLTAGE & WAVEFORM CONDITIONS



* SELF CANCELLING PUSH-BUTTON SWITCHES

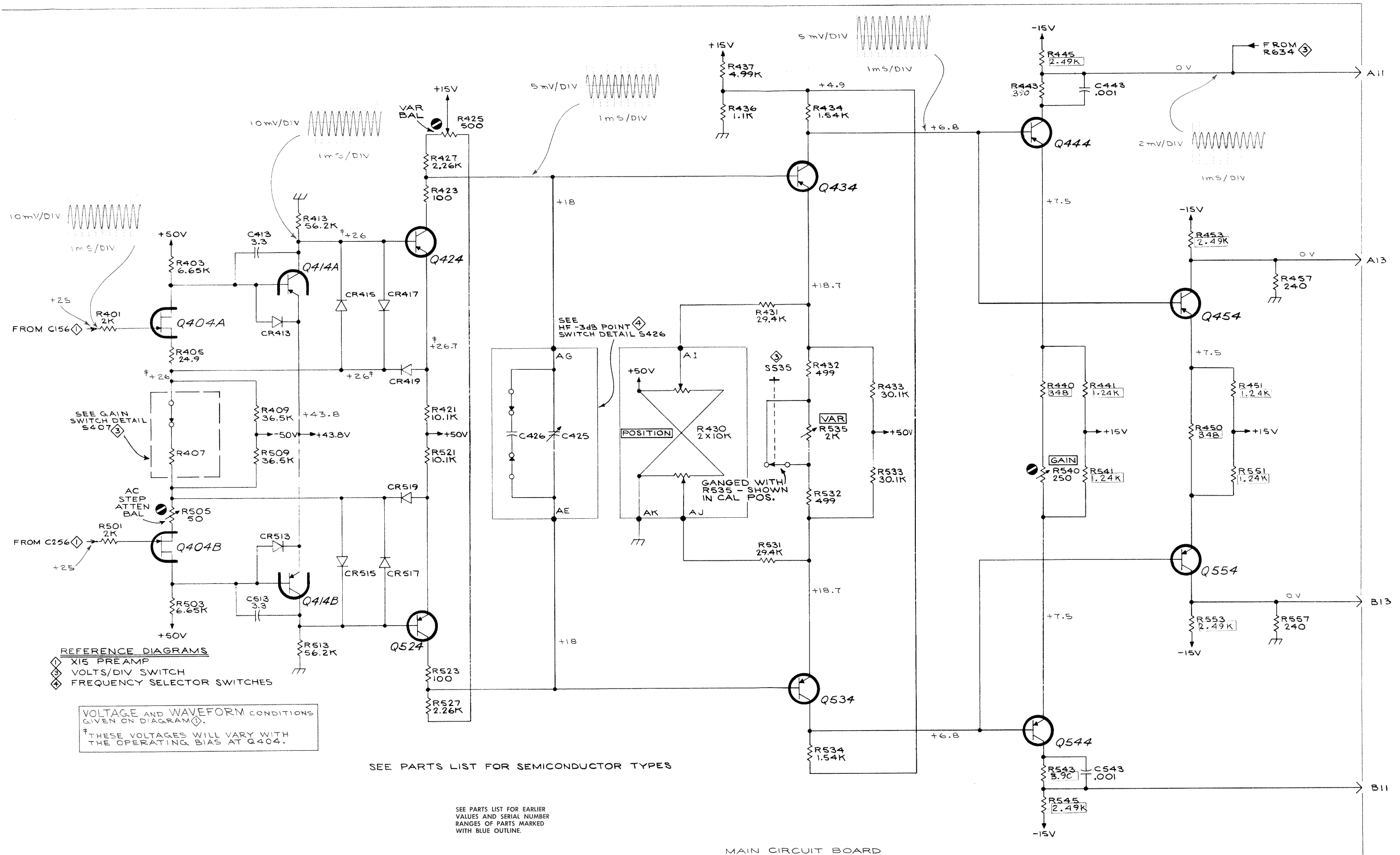
REFERENCE DIAGRAMS
 Ⓛ VOLTS/DIV. SWITCH
 Ⓧ FREQUENCY SELECTOR SWITCHES
 Ⓧ VOLTAGE DISTRIBUTION

SEE PARTS LIST FOR SEMICONDUCTOR TYPES

† VOLTAGE AT PIN U VARIES ±1V WITH Q133 BIAS. VOLTAGES * ARE CORRECT WHEN VOLTAGE AT PIN U = +1V, BUT VARY UP OR DOWN WITH VOLTAGE AT PIN U.
 †† VARIES ±.25 VOLTS DEPENDING UPON; COARSE DC BAL SETTING.

SEE PARTS LIST FOR EARLIER VALUES AND SERIAL NUMBER RANGES OF PARTS MARKED WITH BLUE OUTLINE

X15 PREAMP 1



- REFERENCE DIAGRAMS
- ① XI5 PREAMP
 - ② VOLTS/DIV SWITCH
 - ③ FREQUENCY SELECTOR SWITCHES

VOLTAGE AND WAVEFORM CONDITIONS GIVEN ON DIAGRAM.

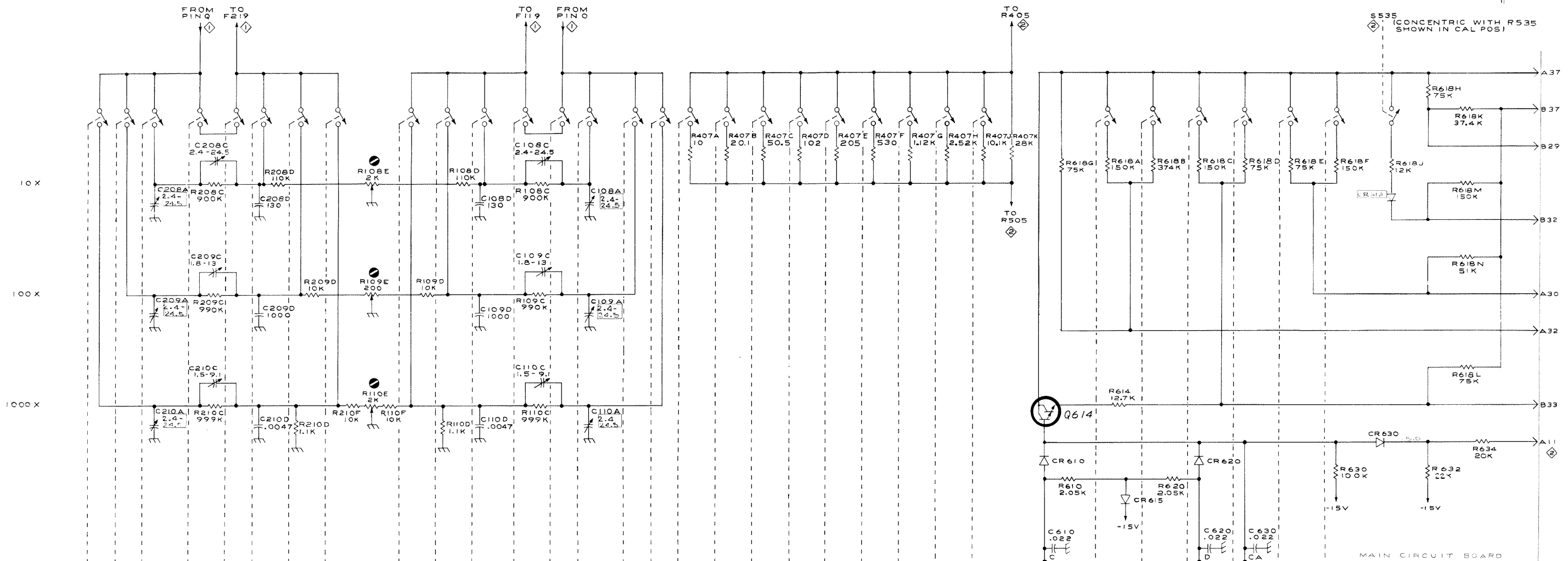
* THESE VOLTAGES WILL VARY WITH THE OPERATING BIAS AT Q404.

SEE PARTS LIST FOR SEMICONDUCTOR TYPES

SEE PARTS LIST FOR EARLIER VALUES AND SERIAL NUMBER RANGES OF PARTS MARKED WITH BLUE OUTLINE.

MAIN CIRCUIT BOARD

OUTPUT AMPLIFIER ②



S535 (CONCENTRIC WITH R535 SHOWN IN CAL POS)

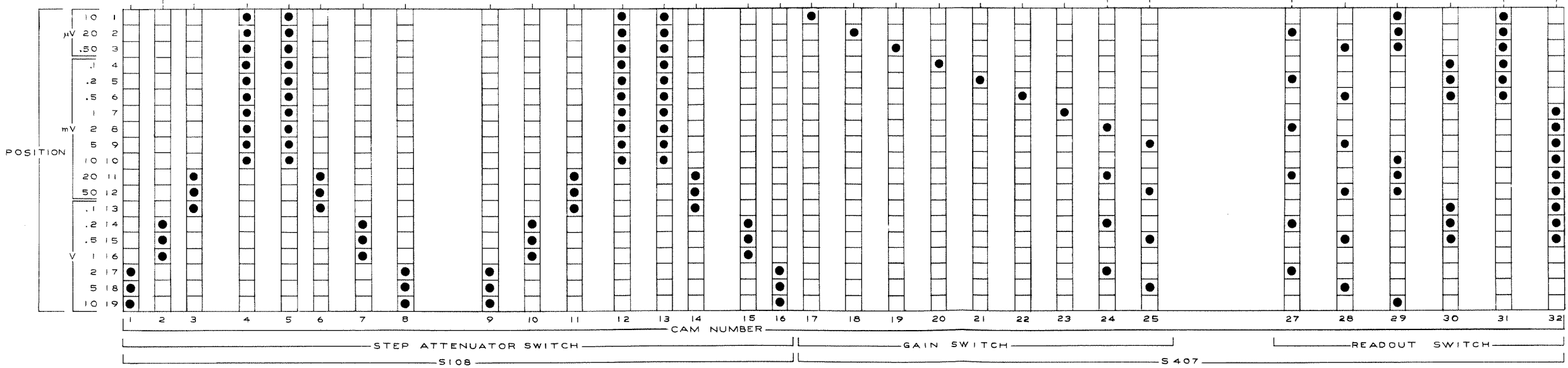
S622 IDENTIFY (CONCENTRIC TO POS)

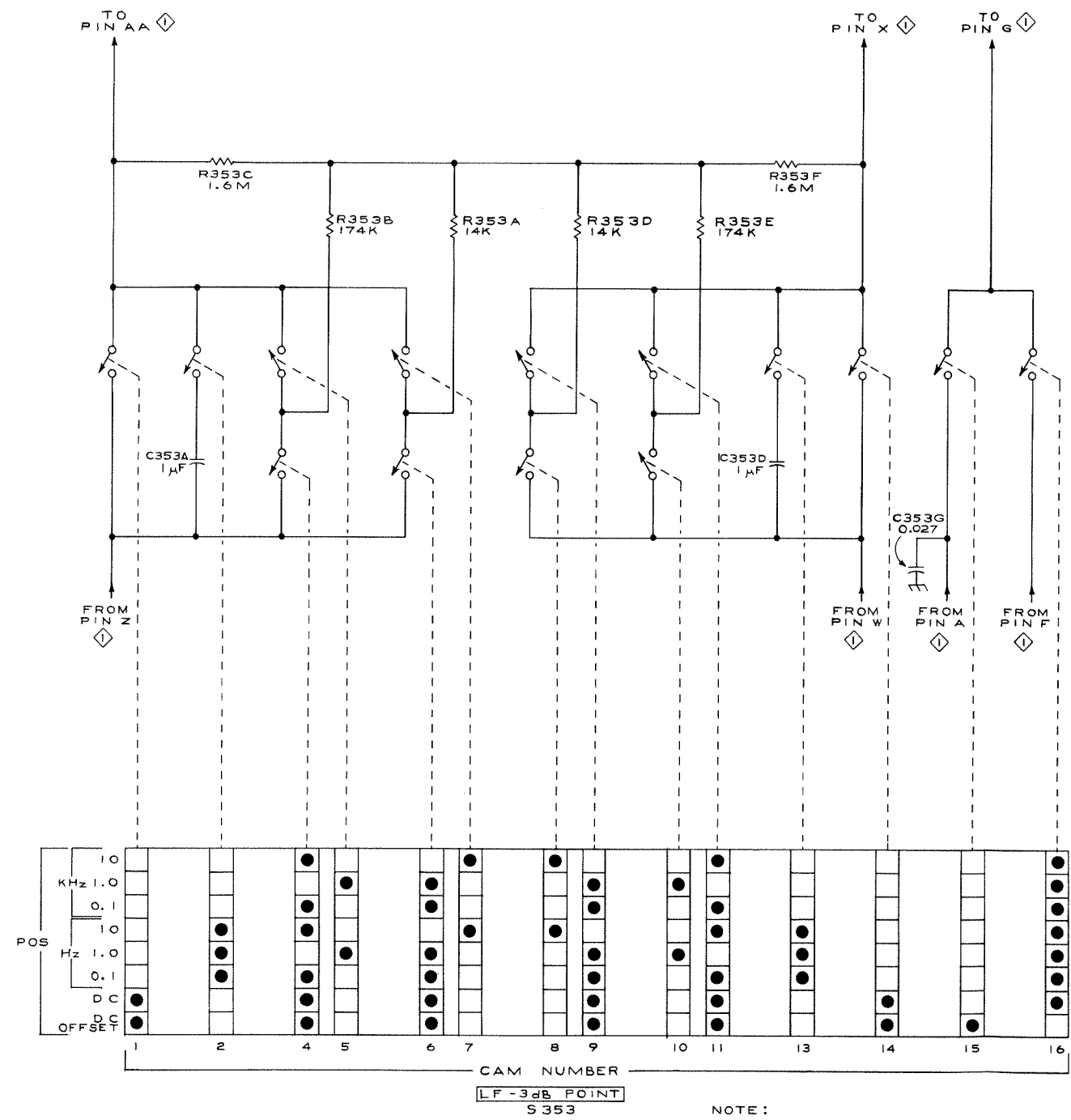
REFERENCE DIAGRAM
 ◊ X15 PREAMP
 ⊕ OUTPUT AMPLIFIER

SEE PARTS LIST FOR EARLIER VALUES AND SERIAL NUMBER RANGES OF PARTS MARKED WITH BLUE OUTLINE.

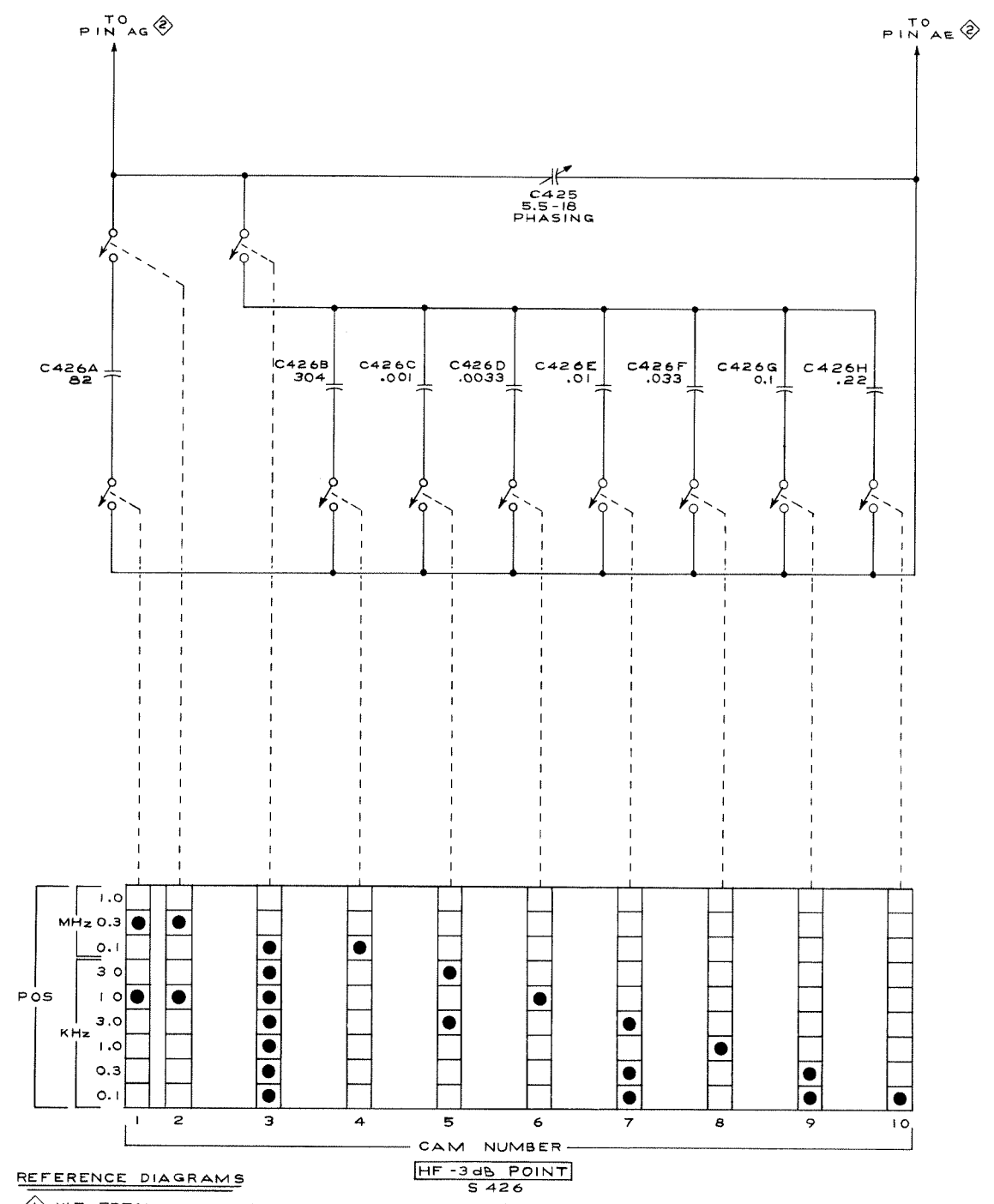
NOTE: SWITCH CLOSED WHEN DOT PRESENT IN APPLICABLE BOX OF SELECTED SWITCH POSITION

VOLTAGE CONDITIONS GIVEN ON DIAGRAM





NOTE:
 1. SWITCH CLOSED WHEN DOT PRESENT IN APPLICABLE BOX OF SELECTED SWITCH POSITION
 2. SWITCHES LOCATED ON BANDWIDTH CIRCUIT BOARD

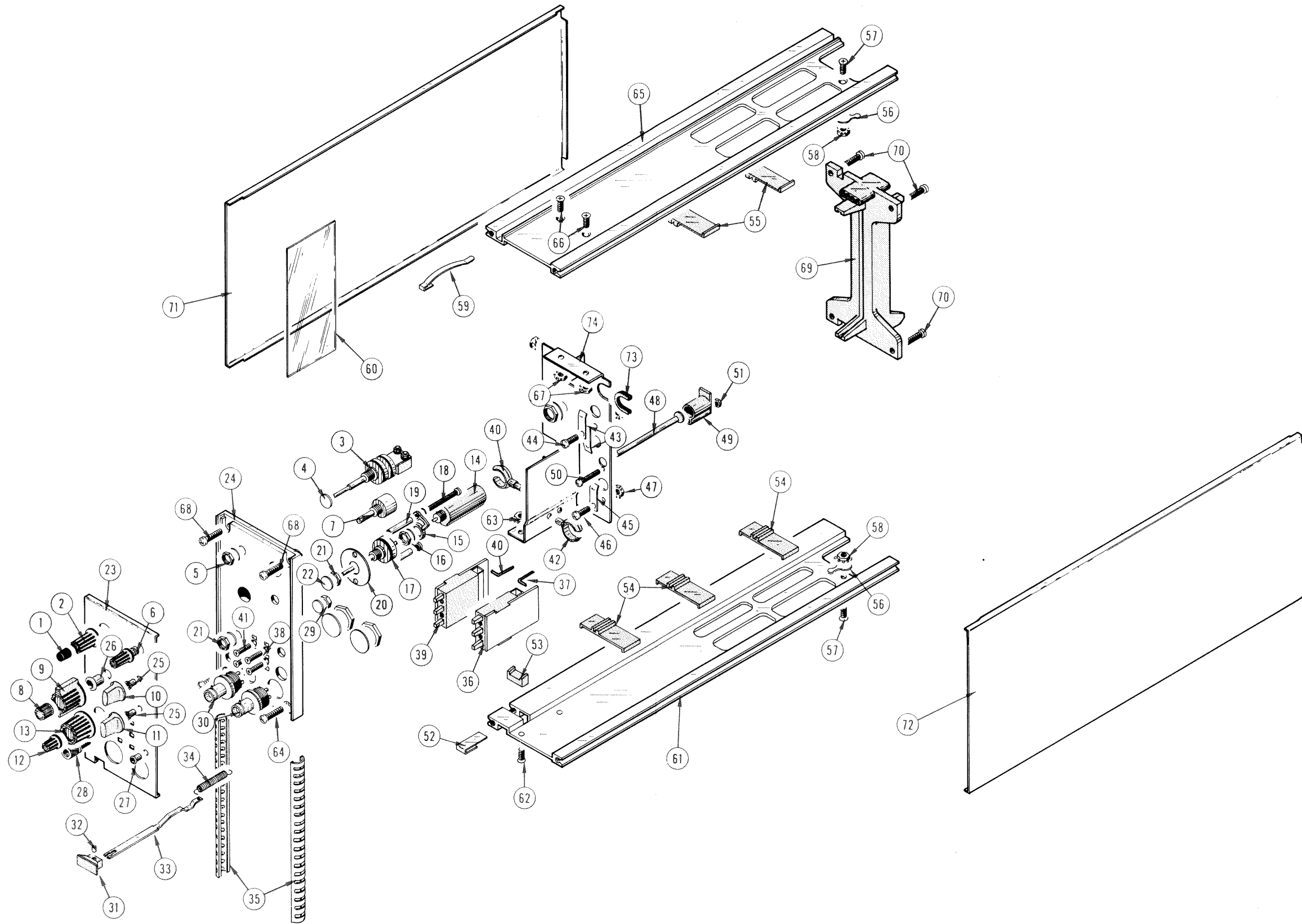


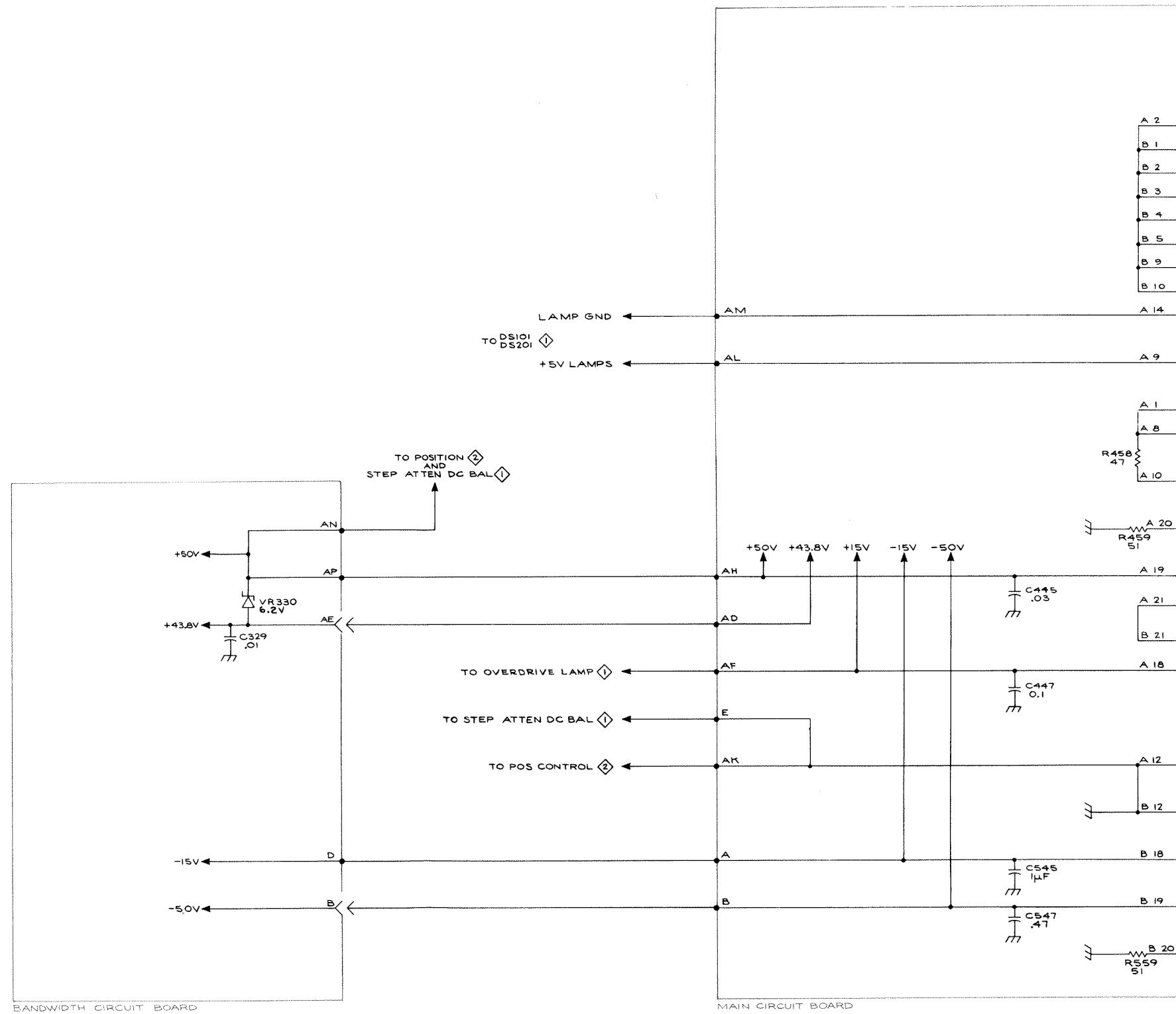
REFERENCE DIAGRAMS
 Ⓛ XIS PREAMP
 Ⓛ OUTPUT AMPLIFIER

TYPE 7A22

FREQUENCY SELECTOR SWITCHES 4

FIGURE 1 FRONT & REAR





BANDWIDTH CIRCUIT BOARD

MAIN CIRCUIT BOARD

REFERENCE DIAGRAMS

- ① X15 PREAMP
- ② OUTPUT AMPLIFIER

VOLTAGE DISTRIBUTION ⑤

+ TYPE 7A22

Ⓐ

WR11 763
VOLTAGE DISTRIBUTION ⑤

+

FIGURE 2 CIRCUIT BOARDS

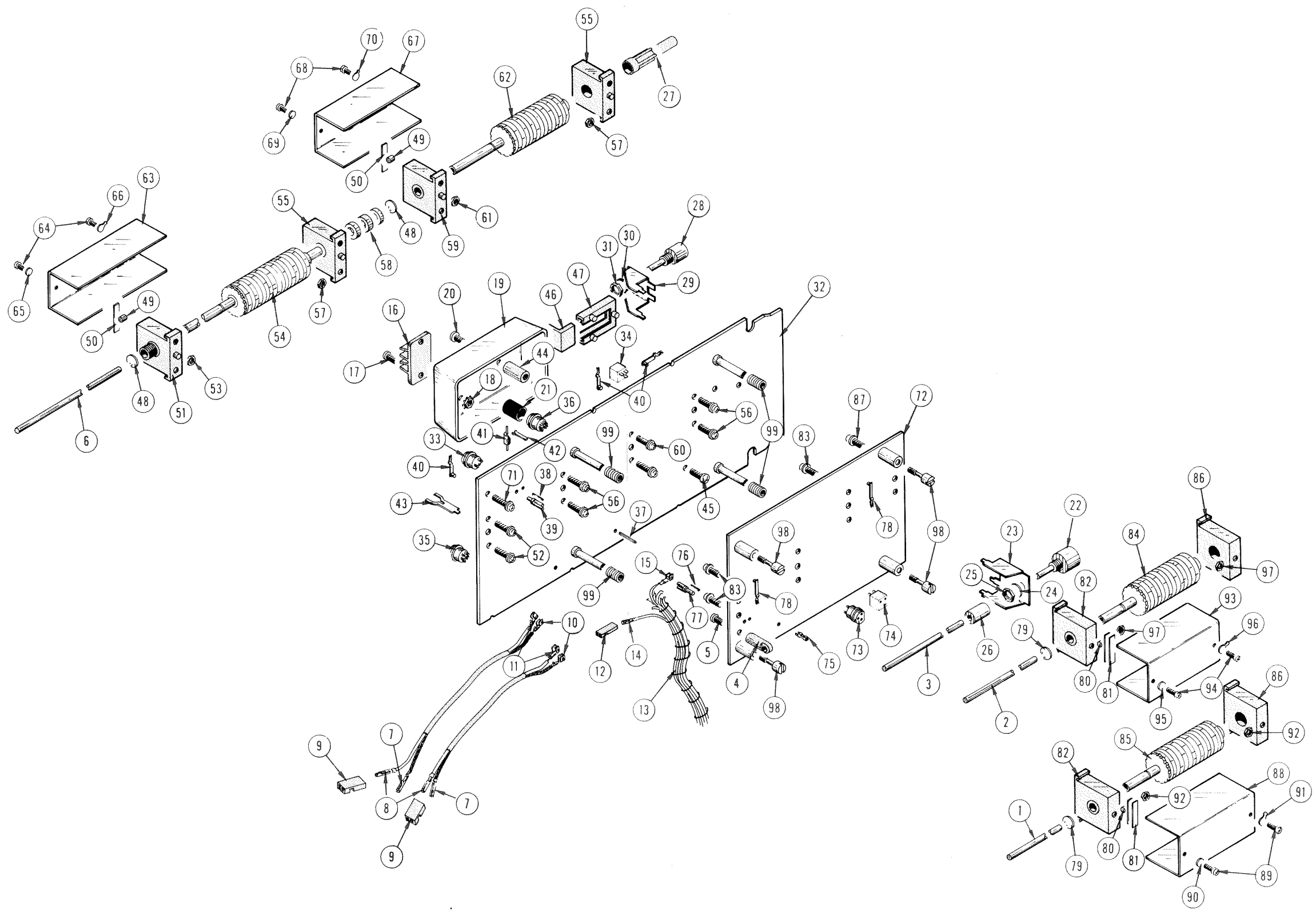


FIG. 2 CIRCUIT BOARDS

TYPE 7A22 DIFFERENTIAL AMPLIFIER

+

(A)

FIGURE AND INDEX NUMBERS

Items in this section are referenced by figure and index numbers to the illustrations which appear either on the back of the diagrams or on pullout pages immediately following the diagrams of the instruction manual.

INDENTATION SYSTEM

This mechanical parts list is indented to indicate item relationships. Following is an example of the indentation system used in the Description column.

Assembly and/or Component
Detail Part of Assembly and/or Component
mounting hardware for Detail Part
Parts of Detail Part
mounting hardware for Parts of Detail Part
mounting hardware for Assembly and/or Component

Mounting hardware always appears in the same indentation as the item it mounts, while the detail parts are indented to the right. Indented items are part of, and included with, the next higher indentation.

Mounting hardware must be purchased separately, unless otherwise specified.

PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix, Inc. Field Office or representative.

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number, instrument type or number, serial or model number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number.

Change information, if any, is located at the rear of this manual.

ABBREVIATIONS AND SYMBOLS

For an explanation of the abbreviations and symbols used in this section, please refer to the page immediately preceding the Electrical Parts List in this instruction manual.

INDEX OF MECHANICAL PARTS ILLUSTRATIONS

Title	Location (reverse side of)
Figure 1. Front and Rear	Frequency Selector Switches Diagram
Figure 2. Circuit Boards	Voltage Distribution Diagram

SECTION 8

MECHANICAL PARTS LIST

FIG. 1 FRONT & REAR

Fig. & Index No.	Tektronix Part No.	Serial/Model No. Eff	No. Disc	Q					Description	
				t	y	1	2	3		4
1-1	366-1059-00			1						PUSHBUTTON, gray—IDENTIFY
-2	366-1077-00			1						KNOB, gray—POSITION
	- - - - -			-						knob includes:
	213-0153-00			1						SETSCREW, 5-40 x 0.125 inch, HSS
-3	- - - - -			1						RESISTOR, variable
	- - - - -			-						mounting hardware: (not included w/resistor)
-4	210-0046-00			1						WASHER, lock, internal, 0.261 ID x 0.408 inch OD
	210-0940-00			1						WASHER, flat, 1/4 ID x 3/8 inch OD
-5	210-0583-00			1						NUT, hex., 1/4-32 x 5/16 inch
-6	366-0392-02			1						KNOB, gray—STEP ATTEN DC BAL
-7	- - - - -			1						RESISTOR, variable
	- - - - -			-						mounting hardware: (not included w/resistor)
	213-0020-00			1						SETSCREW, 6-32 x 1/8 inch, HSS (not shown)
-8	366-1166-00			1						KNOB, red—VARIABLE (CAL IN)
	- - - - -			-						knob includes:
	213-0153-00			1						SETSCREW, 5-40 x 0.125 inch, HSS
-9	366-1057-00			1						KNOB, gray—VOLTS/DIV
	- - - - -			-						knob includes:
	213-0153-00			1						SETSCREW, 5-40 x 0.125 inch, HSS
-10	366-0379-00			1						KNOB, gray—HF-3dB POINT
	- - - - -			-						knob includes:
	213-0153-00			1						SETSCREW, 5-40 x 0.125 inch, HSS
-11	366-0379-00			1						KNOB, gray—LF-3dB POINT
	- - - - -			-						knob includes:
	213-0153-00			1						SETSCREW, 5-40 x 0.125 inch, HSS
-12	366-1084-00			1						KNOB, gray—OFFSET (COARSE)
	- - - - -			-						knob includes:
	213-0725-00			1						SETSCREW, 3-48 x .094 inch, HSS
-13	366-1101-00			1						KNOB, gray—OFFSET (FINE)
	- - - - -			-						knob includes:
	213-0153-00			1						SETSCREW, 5-40 x 0.125 inch, HSS
-14	- - - - -			1						RESISTOR, variable
	- - - - -			-						mounting hardware: (not included w/resistor)
-15	426-0474-00			1						MOUNT, plastic
	210-0046-00			1						WASHER, lock, internal, 0.261 ID x 0.400 inch OD
-16	210-0583-00			1						NUT, hex., 1/4-32 x 5/16 inch

FIG. 1 FRONT & REAR (cont)

Fig. & Index No.	Tektronix Part No.	Serial/Model Eff	No. Disc	Q † y	Description					
					1	2	3	4	5	
1-17	- - - - -			1	RESISTOR, variable					
	- - - - -			-	mounting hardware: (not included w/resistor)					
-18	211-0111-00			2	SCREW, 2-56 x 1 inch, PHS					
	210-0001-00			2	WASHER, lock, internal, #2					
-19	361-0280-00			2	SPACER					
-20	386-1095-00			1	PLATE					
-21	210-0583-00			2	NUT, hex., 1/4-32 x 5/16 inch					
-22	210-0046-00			1	WASHER, lock, internal, 0.261 ID x 0.400 inch OD					
	210-0940-00			1	WASHER, flat, 1/4 ID x 3/8 inch OD					
-23	333-1106-00			1	PANEL, front					
-24	386-1447-07			1	SUBPANEL, front					
-25	348-0031-00			2	GROMMET, plastic, 3/32 inch diameter					
-26	358-0216-00			1	BUSHING, plastic, gray					
-27	358-0378-00			1	BUSHING, sleeve, plastic					
-28	129-0053-00			1	ASSEMBLY, binding post					
	- - - - -			-	assembly includes:					
	200-0103-00			1	CAP, binding post					
	355-0507-00			1	STEM, binding post					
	- - - - -			-	mounting hardware: (not included w/assembly)					
	210-0046-00			1	WASHER, lock, internal, 0.261 ID x 0.400 inch OD					
-29	210-0583-00			1	NUT, hex., 1/4-32 x 5/16 inch					
-30	131-0679-00			2	CONNECTOR, receptacle, BNC, female w/hardware					
-31	366-1058-07			1	KNOB, latch					
	- - - - -			-	mounting hardware: (not included w/knob)					
-32	214-1095-00			1	PIN, spring, split					
-33	105-0076-00			1	RELEASE BAR, latch					
-34	214-1280-00			1	SPRING, helical compression					
-35	348-0157-00	B010100	B010180	2	SHIELDING GASKET, electrical					
	348-0235-00	B010181		2	SHIELDING GASKET, electrical					
-36	670-1050-00	B010100	B059999	1	ASSEMBLY, circuit board— —AC GND DC					
	670-1050-01	B060000		1	ASSEMBLY, circuit board— —AC GND DC					
	- - - - -			-	assembly includes:					
-37	131-0589-00			2	TERMINAL, pin					
	380-0154-02			1	HOUSING					
	- - - - -			-	mounting hardware: (not included w/housing)					
	213-0181-00			1	SCREW, thread forming, #2 x 0.375 inch, PHS					
	210-0259-00			1	LUG, solder, #2					
	- - - - -			-	mounting hardware: (not included w/assembly)					
-38	211-0156-00			2	SCREW, 1-72 x 1/4 inch, FHS					
-39	670-1051-00	B010100	B059999	1	ASSEMBLY, circuit board— +AC GND DC					
	670-1051-01	B060000		1	ASSEMBLY, circuit board— +AC GND DC					
	- - - - -			-	assembly includes:					
-40	131-0589-00			2	TERMINAL, pin					
	380-0154-02			1	HOUSING					
	- - - - -			-	mounting hardware: (not included w/housing)					
	213-0181-00			1	SCREW, thread forming, #2 x 0.375 inch, PHS					
	210-0259-00			1	LUG, solder, #2					
	- - - - -			-	mounting hardware: (not included w/assembly)					
-41	211-0156-00			2	SCREW, 1-72 x 1/4 inch, FHS					
-42	352-0068-00			2	HOLDER, plastic					

FIG. 1 FRONT & REAR (cont)

Fig. & Index No.	Tektronix Part No.	Serial/Model Eff	No. Disc	Q † y	Description				
					1	2	3	4	5
1-43	131-0664-00			2	CONTACT, electrical				
	- - - - -			-	mounting hardware: (not included w/contact)				
-44	211-0008-00			1	SCREW, 4-40 x 1/4 inch, PHS				
	210-0586-00			1	NUT, keps, 4-40 x 1/4 inch				
-45	131-0664-00			1	CONTACT, electrical				
	- - - - -			-	mounting hardware: (not included w/contact)				
-46	211-0008-00			1	SCREW, 4-40 x 1/4 inch, PHS				
-47	210-0586-00			1	NUT, keps, 4-40 x 1/4 inch				
-48	378-0600-00			1	LENS, indicator light				
-49	352-0067-00			1	HOLDER, neon, single				
	- - - - -			-	mounting hardware: (not included w/holder)				
-50	211-0018-00			1	SCREW, 4-40 x 7/8 inch, RHS				
-51	210-0406-00			1	NUT, hex., 4-40 x 3/16 inch				
-52	214-1054-00			1	SPRING, flat, latch detent				
-53	105-0075-00			1	BOLT, latch, plastic				
-54	344-0162-00	B010100	B050549	3	CLIP, spring tension, plastic				
	344-0211-00	B050550		3	CLIP, circuit board				
-55	344-0161-00	B010100	B050549	2	CLIP, spring tension, plastic				
	344-0210-00	B050550		2	CLIP, circuit board				
-56	210-0201-00			2	LUG, solder, SE #4				
	- - - - -			-	mounting hardware for each: (not included w/lug)				
-57	211-0101-00			1	SCREW, 4-40 x 1/4 inch, 100° csk, FHS				
-58	210-0586-00			1	NUT, keps, 4-40 x 1/4 inch				
-59	214-1061-00			1	SPRING, flat, sliding ground				
-60	337-1243-00			1	SHIELD, electrical				
-61	426-0499-03			1	FRAME SECTION, bottom				
	- - - - -			-	mounting hardware: (not included w/frame section)				
-62	211-0101-00			2	SCREW, 4-40 x 1/4 inch, 100° csk, FHS				
-63	210-0586-00			2	NUT, keps, 4-40 x 1/4 inch				
-64	213-0192-00			2	SCREW, thread forming, 6-32 x 1/2 inch, Fil HS				
-65	426-0505-03			1	FRAME SECTION, top				
	- - - - -			-	mounting hardware: (not included w/frame section)				
-66	211-0101-00			2	SCREW, 4-40 x 1/4 inch, 100° csk, FHS				
-67	210-0586-00			2	NUT, keps, 4-40 x 1/4 inch				
-68	213-0192-00			2	SCREW, thread forming, 6-32 x 1/2 inch, Fil HS				
-69	386-1402-00			1	PANEL, rear				
	- - - - -			-	mounting hardware: (not included w/panel)				
-70	213-0192-00			4	SCREW, thread forming, 6-32 x 1/2 inch, Fil HS				
	361-0326-00			1	SPACER, sleeve (not shown)				
-71	337-1145-00	B010100	B069999	1	SHIELD, electrical, left				
	337-1145-01	B070000		1	SHIELD, electrical, left				
-72	337-1146-00	B010100	B069999	1	SHIELD, electrical, right				
	337-1146-01	B070000		1	SHIELD, electrical, right				
-73	348-0115-00			1	GROMMET, plastic, U shaped				
-74	337-1045-00			1	SHIELD, electrical				

FIG. 2 CIRCUIT BOARDS

Fig. & Index No.	Tektronix Part No.	Serial/Model Eff	No. Disc	Q					Description
				†	1	2	3	4	
2-1	384-0255-00			1					ROD, extension, 5 ¹⁹ / ₃₂ inches long
-2	384-0306-00			1					ROD, extension 9 ¹ / ₂ inches long
-3	384-0444-00			1					EXTENSION SHAFT, 11.062 inches long
-4	351-0159-00			1					GUIDE, shaft
	- - - - -			-					mounting hardware: (not included w/guide)
-5	213-0088-00			1					SCREW, thread forming #4 x 1/4 inch, PHS
-6	384-0448-00			1					EXTENSION SHAFT, plastic, 11.312 inches long
-7	131-0792-00			2					CONNECTOR, terminal, straight (for large wire)
-8	131-0622-00			2					CONNECTOR, terminal, straight (for small wire)
-9	352-0198-00			2					HOLDER, terminal connector
-10	131-0740-00			2					CONNECTOR, terminal (for large wire)
-11	131-0755-00			2					CONNECTOR, terminal (for small wire)
-12	352-0169-00			2					HOLDER, terminal connector
-13	179-1407-00			1					CABLE HARNESS, main
	- - - - -			-					cable harness includes:
-14	131-0707-00			4					CONNECTOR, terminal, straight
-15	131-0512-00			27					CONNECTOR, terminal
-16	352-0136-00			1					HOLDER, fuse (spare)
	- - - - -			-					mounting hardware: (not included w/holder)
-17	211-0008-00			2					SCREW, 4-40 x 3/16 inch, PHS
-18	210-0586-00			2					NUT, keps, 4-40 x 1/4 inch
-19	200-0902-00			1					COVER, circuit board
	- - - - -			-					mounting hardware: (not included w/cover)
-20	211-0008-00			1					SCREW, 4-40 x 1/4 inch, PHS
-21	200-0687-01			2					COVER, transistor, plastic
-22	- - - - -			1					RESISTOR, variable
	- - - - -			-					mounting hardware: (not included w/resistor)
-23	407-0554-00			1					BRACKET
-24	210-0046-00			1					WASHER, lock, internal, 0.261 ID x 0.400 inch OD
-25	210-0583-00			1					NUT, hex., 1/4-32 x 5/16 inch
-26	376-0029-00			1					COUPLING, shaft
	- - - - -			-					coupling includes:
	213-0075-00			2					SCREW, set, 4-40 x 3/32 inch, long
-27	214-1190-00			1					EXTENDER RETRACTOR, knob
	- - - - -			-					knob includes:
	213-0075-00			1					SETSCREW, 4-40 x 3/32 inch, HSS
-28	- - - - -			1					RESISTOR, variable
	- - - - -			-					mounting hardware: (not included w/resistor)
-29	407-0553-00			1					BRACKET
-30	210-0046-00			1					WASHER, lock, internal, 0.261 ID x 0.400 inch OD
-31	210-0583-00			1					NUT, hex., 1/4-32 x 5/16 inch

FIG. 2 CIRCUIT BOARDS (cont)

Fig. & Index No.	Tektronix Part No.	Serial/Model No. Eff	Disc	Q † y	1 2 3 4 5					Description
					1	2	3	4	5	
2-32	670-1014-00	B010100	B029999	1						ASSEMBLY, circuit board—MAIN
	670-1014-01	B030000	B059999	1						ASSEMBLY, circuit board—MAIN
	670-1014-03	B060000	B069999	1						ASSEMBLY, circuit board—MAIN
	670-1014-04	B070000	B079999	1						ASSEMBLY, circuit board—MAIN
	670-1014-05	B080000		1						ASSEMBLY, circuit board—MAIN
	- - - - -									assembly includes:
	388-1097-00			1						BOARD, circuit
	337-1266-02			1						SHIELD, electrical (not shown)
-33	136-0183-00			4						SOCKET, transistor, 3 pin
-34	136-0220-00			10						SOCKET, transistor, 3 pin
-35	136-0235-00			4						SOCKET, transistor, 6 pin
-36	136-0235-01			1						SOCKET, transistor, plastic, 6 pin
-37	131-0590-00			18						TERMINAL, pin, 0.665 in long
-38	131-0608-00			22						TERMINAL, pin, 0.365 inch long
-39	351-0155-00			22						GUIDE, terminal lead
-40	131-0604-00			33						CONTACT-POST ASSEMBLY
-41	131-0566-00			2						LINK, terminal
-42	214-0579-00			4						PIN, test point
-43	344-0154-00			4						CLIP, fuse
-44	385-0107-00			1						ROD, plastic, 1/4 x 3/4 inch long
	- - - - -			-						mounting hardware: (not included w/rod)
-45	211-0008-00			1						SCREW, 4-40 x 1/4 inch, PHS
-46	214-1136-00			1						ACTUATOR, slide, plastic
-47	351-0180-00			1						GUIDE, actuator, plastic
	105-0413-00			1						ACTUATOR ASSY, cam switch (ATTEN)
	105-0465-00			1						ACTUATOR ASSY, cam switch (GAIN-READOUT)
	- - - - -			-						each actuator assy includes:
-48	354-0219-00			1						RING, retaining
-49	214-1127-00			1						ROLLER, detent
-50	214-1139-00 ¹			-						SPRING, flat, gold
	214-1139-02 ¹			-						SPRING, flat, green
	214-1139-03 ¹			-						SPRING, flat, red
-51	401-0054-00			2						BEARING, front
	- - - - -			-						mounting hardware for each: (not included w/bearing)
-52	211-0116-00			2						SCREW, sems, 4-40 x 5/16 inch, PHB
-53	210-0406-00			2						NUT, hex, 4-40 x 3/16 inch
-54	105-0110-00			1						DRUM, cam switch (ATTENUATOR)
-55	401-0056-00			1						BEARING, rear
	- - - - -			-						mounting hardware for each: (not included w/bearing)
-56	211-0116-00			2						SCREW, sems, 4-40 x 5/16 inch, PHB
-57	210-0406-00			2						NUT, hex., 4-40 x 3/16 inch
-58	376-0091-00			1						COUPLING, shaft, flexible
	213-0020-00	XB020160		4						SETSCREW, 6-32 x 0.125 inch, HSS
-59	401-0057-00			1						BEARING, front
	- - - - -			-						mounting hardware: (not included w/bearing)
-60	211-0116-00			2						SCREW, sems, 4-40 x 5/16 inch, PHB
-61	210-0406-00			2						NUT, hex., 4-40 x 3/16 inch
-62	105-0111-00			1						DRUM, cam switch (GAIN-READOUT)
-63	200-0952-00			1						COVER, cam switch (ATTEN actr assy only)
	- - - - -			-						mounting hardware: (not included w/cover)
-64	211-0022-00			2						SCREW, 2-56 x 3/16 inch, PHS
-65	210-0001-00			1						WASHER, lock, internal, #2
-66	210-0259-00			1						LUG, solder, #2
	210-0405-00			2						NUT, hex., 2-56 x 3/16 inch

¹Replace only with part bearing the same color as the original part in your instrument.

FIG. 2 CIRCUIT BOARDS (cont)

Fig. & Index No.	Tektronix Part No.	Serial/Model No. Eff	No. Disc	Q † y	1 2 3 4 5					Description
					1	2	3	4	5	
2-67	200-0953-00			1						COVER, cam switch (GAIN READOUT actr assy only)
	- - - - -			-						mounting hardware: (not included w/cover)
-68	211-0022-00			2						SCREW, 2-56 x 3/16 inch, PHS
-69	210-0001-00			1						WASHER, lock, internal, #2
-70	210-0259-00			1						LUG, solder, #2
	210-0405-00			2						NUT, hex., 2-56 x 3/16 inch
	- - - - -			-						mounting hardware: (not included w/assembly)
-71	211-0116-00			1						SCREW, sems, 4-40 x 5/16 inch, PHB
	210-0586-00			1						NUT, keps, 4-40 x 1/4 inch
	210-0840-00			1						WASHER, flat, 0.390 ID x 9/16 inch OD
	210-0413-00			1						NUT, hex., 3/8-32 x 1/2 inch
	342-0050-00	XB050000		15						INSULATOR, terminal pin
-72	670-1013-00			1						ASSEMBLY, circuit board—BANDWIDTH
	- - - - -			-						assembly includes:
	388-1096-00			1						BOARD, circuit
	131-0840-00	XB010181		1						CONTACT, electrical grounding (not shown)
-73	136-0183-00			3						SOCKET, transistor, 3 pin
-74	136-0220-00			3						SOCKET, transistor, 3 pin
-75	136-0263-01	B010100	B010180	18						SOCKET, pin terminal
	136-0263-03	B010181		18						SOCKET, pin terminal
-76	131-0608-00			5						TERMINAL, pin, 0.365 inch long
-77	351-0155-00			5						GUIDE, terminal lead
-78	131-0604-00			24						CONTACT-POST ASSEMBLY
	105-0415-00			1						ACTUATOR ASSY, cam switch(LF)
	105-0466-00			1						ACTUATOR ASSY, cam switch (HF)
	- - - - -			-						each Actuator Assy includes:
-79	354-0219-00			1						RING retaining
-80	214-1127-00			1						ROLLER, detent
	407-0714-00	XB010181		1						BRACKET, grounding (not shown)
-81	214-1126-00 ²			-						SPRING, flat, gold
	214-1126-01 ²			-						SPRING, flat, green
	214-1126-02 ²			-						SPRING, flat, red
-82	401-0058-00			1						BEARING, front
	- - - - -			-						mounting hardware for each: not included w/bearing)
-83	211-0116-00			2						SCREW, sems, 4-40 x 5/16 inch, PHB
	210-0406-00			2						NUT, hex, 4-40 x 3/16 inch
-84	105-0109-00			1						DRUM, cam switch (HF-3 dB POINT)
	- - - - -			-						drum includes:
	213-0075-00			2						SETSCREW, 4-40 x 3/32 inch, HSS
-85	105-0112-00			1						DRUM, cam switch (LF-3 dB POINT)
	- - - - -			-						drum includes:
	213-0075-00			2						SETSCREW, 4-40 x 3/32 inch, HSS
-86	401-0061-00			1						BEARING, rear
	- - - - -			-						mounting hardware for each: (not included w/bearing)
-87	211-0116-00			2						SCREW, sems, 4-40 x 5/16 inch, PHB
	210-0406-00			2						NUT, hex., 4-40 x 3/16 inch
-88	200-0996-00			1						COVER, cam switch (LF actr assy only)
	- - - - -			-						mounting hardware (not included w/cover)
-89	211-0022-00			2						SCREW, 2-56 x 3/16 inch, PHS
-90	210-0001-00			1						WASHER, lock, internal, #2
-91	210-0259-00			1						LUG, solder #2
-92	210-0405-00			1						NUT, hex, 2-56 x 3/16 inch

²Replace only with part bearing the same color as the original part in your instrument.

FIG. 2 CIRCUIT BOARDS (cont)

Fig. & Index No.	Tektronix Part No.	Serial/Model Eff	No. Disc	Q					Description	
				y	1	2	3	4		5
2-93	200-0995-00			1						COVER, cam switch (HF actr assy only mounting hardware: (not included w/cover)
-94	211-0022-00			2						SCREW, 2-56 x 3/16 inch, PHS
-95	210-0001-00			2						WASHER, lock, internal, #2
-96	210-0259-00			1						LUG, solder, #2
-97	210-0405-00			1						NUT, hex., 2-56 x 3/16 inch mounting hardware: (not included w/assembly)
-98	211-0155-00			4						SCREW, 4-40 x 0.081 inch, PHS
-99	214-1140-00			4						SPRING, helical compression

STANDARD ACCESSORIES

070-0931-00	1	MANUAL, instruction (not shown)
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MANUAL CHANGE INFORMATION

At Tektronix, we continually strive to keep up with latest electronic developments by adding circuit and component improvements to our instruments as soon as they are developed and tested.

Sometimes, due to printing and shipping requirements, we can't get these changes immediately into printed manuals. Hence, your manual may contain new change information on following pages.

A single change may affect several sections. Since the change information sheets are carried in the manual until all changes are permanently entered, some duplication may occur. If no such change pages appear following this page, your manual is correct as printed.

SERVICE NOTE

Because of the universal parts procurement problem, some electrical parts in your instrument may be different from those described in the Replaceable Electrical Parts List. The parts used will in no way alter or compromise the performance or reliability of this instrument. They are installed when necessary to ensure prompt delivery to the customer. Order replacement parts from the Replaceable Electrical Parts List.

CALIBRATION TEST EQUIPMENT REPLACEMENT

Calibration Test Equipment Chart

This chart compares TM 500 product performance to that of older Tektronix equipment. Only those characteristics where significant specification differences occur, are listed. In some cases the new instrument may not be a total functional replacement. Additional support instrumentation may be needed or a change in calibration procedure may be necessary.

Comparison of Main Characteristics

DM 501 replaces 7D13		
PG 501 replaces 107 108 111 114 115	PG 501 - Risetime less than 3.5 nsec into 50 Ω . PG 501 - 5 V output pulse; 3.5 nsec Risetime. PG 501 - Risetime less than 3.5 nsec; 8 nsec Pretrigger pulse delay. PG 501 - ± 5 V output. PG 501 - Does not have Paired, Burst, Gated, Delayed & Undelayed pulse mode; ± 5 V dc Offset; short proof output. Has ± 5 V output.	107 - Risetime less than 3.0 nsec into 50 Ω . 108 - 10 V output pulse; 1 nsec Risetime. 111 - Risetime 0.5 nsec; 30 to 250 nsec Pretrigger Pulse delay. 114 - ± 10 V output. Short proof output. 115 - Paired, Burst, Gated, Delayed & Undelayed pulse mode; ± 10 V output. Short proof output.
PG 502 replaces 107 108 111 114 115 2101	PG 502 - 5 V output PG 502 - Risetime less than 1 nsec; 10 nsec Pretrigger pulse delay. PG 502 - ± 5 V output. PG 502 - Does not have Paired, Burst, Gated, Delayed & Undelayed pulse mode; ± 5 V output. Short proof output. PG 502 - Does not have Paired, Delayed, Undelayed and output locked mode; ± 5 V output.	108 - 10 V output. 111 - Risetime 0.5 nsec; 30 to 250 nsec Pretrigger pulse delay. 114 - ± 10 V output. Short proof output. 115 - Paired, Burst, Gated, Delayed & Undelayed pulse mode; ± 10 V output. Short proof output. 2101 - Paired, Delayed, Undelayed and output locked on mode; 10 V output.
PG 506 replaces 106 067-0502-01	PG 506 - Positive-going trigger output signal at least 1 V; High Amplitude, 60 V output. PG 506 - Does not have chopped feature.	106 - Positive and Negative-going trigger output signal, 50 nsec and 1 V; High Amplitude output, 100 V. 0502-01 - Comparator output can be alternately chopped to a reference voltage.
SG 503 replaces 190, 190A, 190B 191 067-0532-01	SG 503 - Amplitude range 5 mV to 5.5 V p-p. SG 503 - Frequency range 250 kHz to 250 MHz. SG 503 - Frequency range 250 kHz to 250 MHz.	190B - Amplitude range 40 mV to 10 V p-p. 191 - Frequency range 350 kHz to 100 MHz. 0532-01 - Frequency range 65 MHz to 500 MHz.
TG 501 replaces 180, 180A 181 2901	TG 501 - Marker outputs, 5 sec to 1 ns. Sinewave available only at 5, 2, and 1 ns. Trigger output - slaved to marker output from 5 sec through 100 ns. Only one time-mark can be generated. TG 501 - Marker outputs, 5 sec to 1 ns. Sinewave available only at 5, 2, and 1 ns. TG 501 - Marker outputs, 5 sec to 1 ns. Sinewave available only at 5, 2, and 1 ns. Trigger output - slaved to marker output from 5 sec through 100 ns. Only one time-mark can be generated.	180A - Marker outputs, 5 sec to 1 μ s. Sinewave available at 5, 10, and 50 MHz. Trigger pulses 1, 10, 100 Hz; 1, 10, and 100 kHz. Multiple time-marks can be stacked. 181 - Marker outputs, 1, 10, 100, 1000, and 10,000, plus 10 MHz sinewave. 2901 - Marker outputs, 5 sec to 0.1 μ s. Sinewave available at 5, 10, and 50 ns. Separate trigger pulses, from 5 sec to 0.1 μ s. Multiple time-marks can be stacked.



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MANUAL CHANGE INFORMATION

PRODUCT 7A22

CHANGE REFERENCE M24,160

EFF SN .100000-up

DATE 11-26-75

CHANGE:

DESCRIPTION

070-0931-00

ELECTRICAL PARTS LIST AND SCHEMATIC CHANGE

CHANGE TO: MAIN Circuit Board Assembly

Q133 151-1101-00 FET MONOLITHIC DUAL,2N5565

R505 311-1258-00 50 ohm, Var

Q133 is located on the MAIN INTERFACE circuit board 670-1014-05 and is shown on diagram 1 X15 PREAMP.

R505 is located on the MAIN INTERFACE circuit board 670-1014-05 and shown on diagram 2 OUTPUT AMPLIFIER.

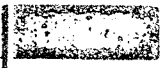


MANUAL CHANGE INFORMATION

TEKTRONIX

PRODUCT: 7A22

CHANGE REFERENCE:



HOLLAND N.V.

SFF SN : 700461

H-3286

DATE: May 14, 1974

CHANGE TO:

DESCRIPTION

C 413	281-0593-00	Capacitor ceramic 3.9 pF.
C 513	281-0593-00	Capacitor ceramic 3.9 pF.

C 413 and C 513 are located on the Main Interface circuit board.

