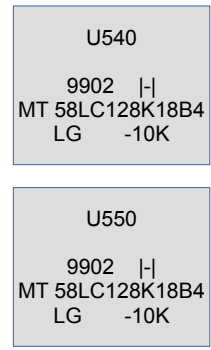
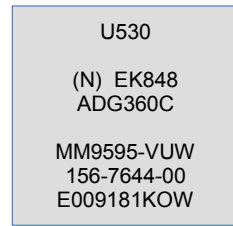


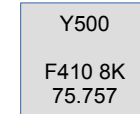
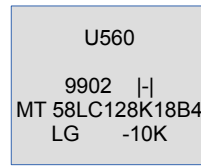
TP10



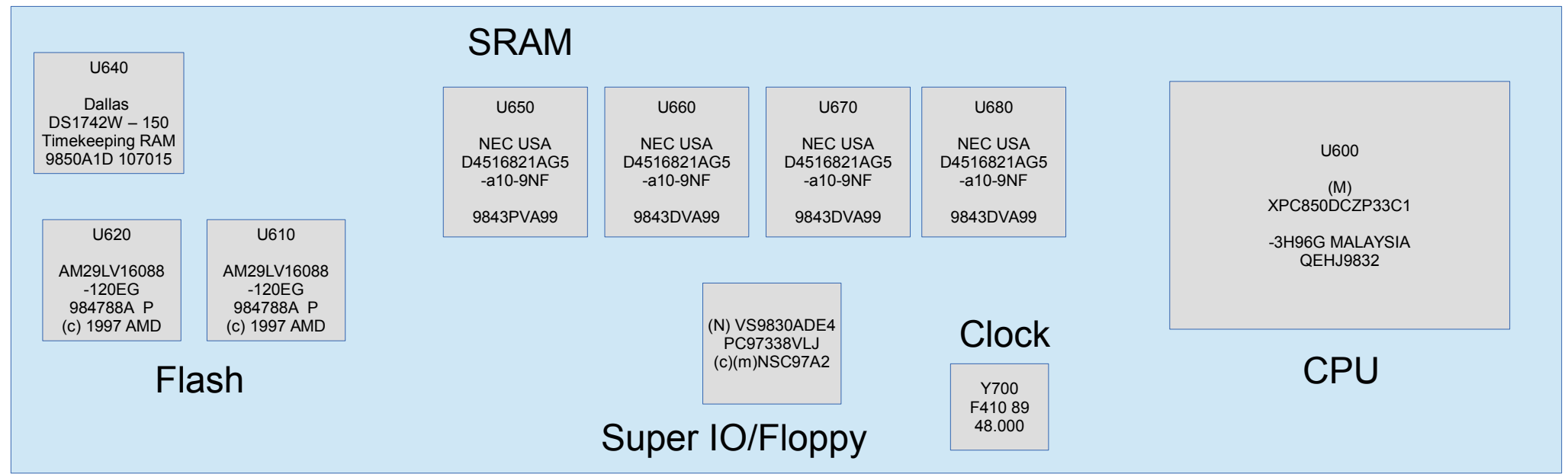
Micron
Synchronous SRAM
Flow through mode?



DSP



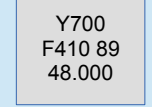
Clock



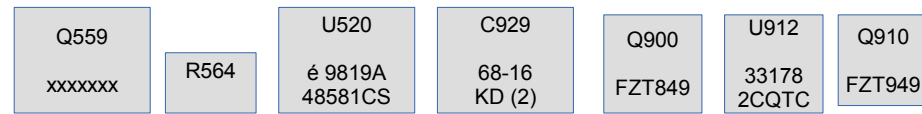
Flash

Super IO/Floppy

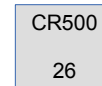
Clock



CPU



TP40

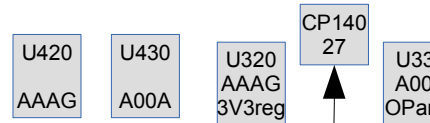
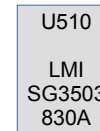
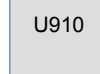
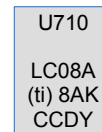
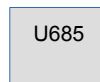
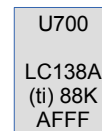
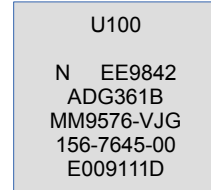
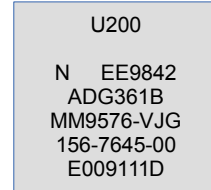
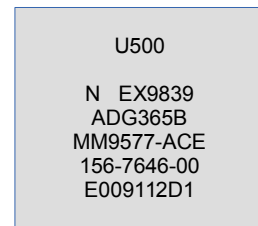
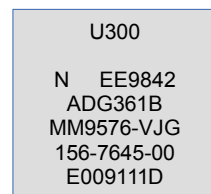
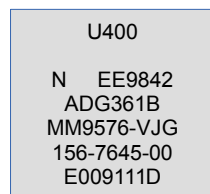


TP1

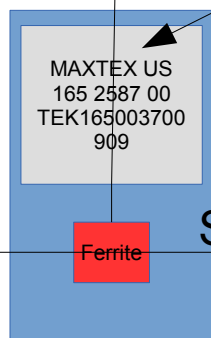
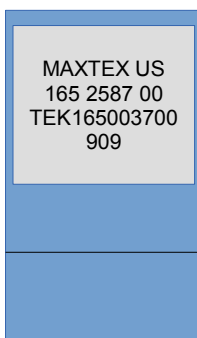


Trigger ASIC

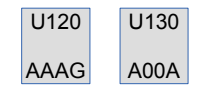
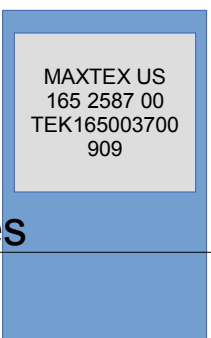
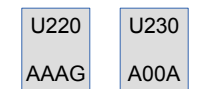
ADC



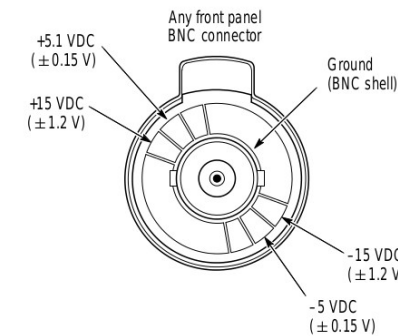
Signal ok



Split in PCB planes



Frontend amplifier



1. Noise over and under signal
2. No not entirely random
3. When signal negative, negative have more noise
4. When signal positive, positive have more noise
5. Noise not random, 20uS = 50kHz
6. Noise is max and min 50 V

CR = Diod
U = Integrated chip/semiconductor
Q = Transistor
C = Capacitor
R = Resistor
TP = Testpoint

U510 = Precision 2.5V ref.

Series 100 = channel 1
Series 200 = channel 2
Series 300 = channel 3
Series 400 = channel 4
Series 500 = Digital signal processing
Series 600 = CPU
Series 700 = ???

