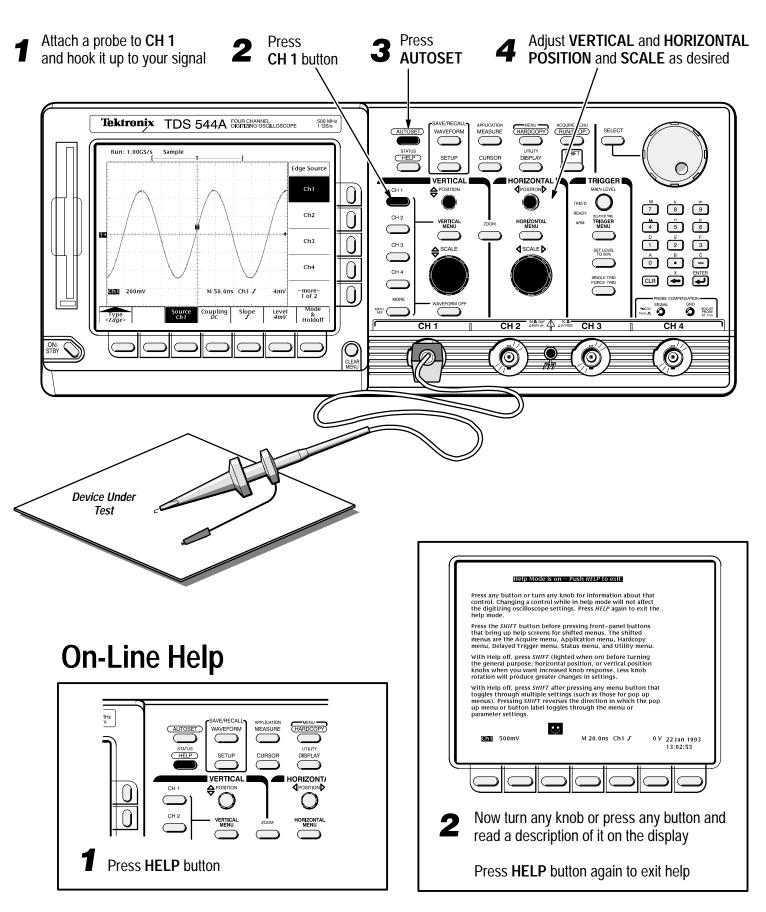
Reference

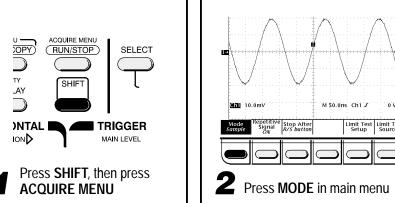
TDS Family Digitizing Oscilloscopes (TDS 520A, 524A, 540A, 544A, 620A, 640A, & 644A)

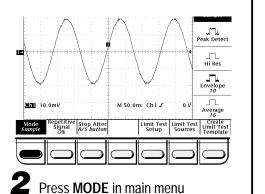
070-8711-02

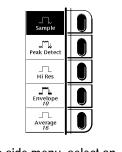
Displaying a Waveform



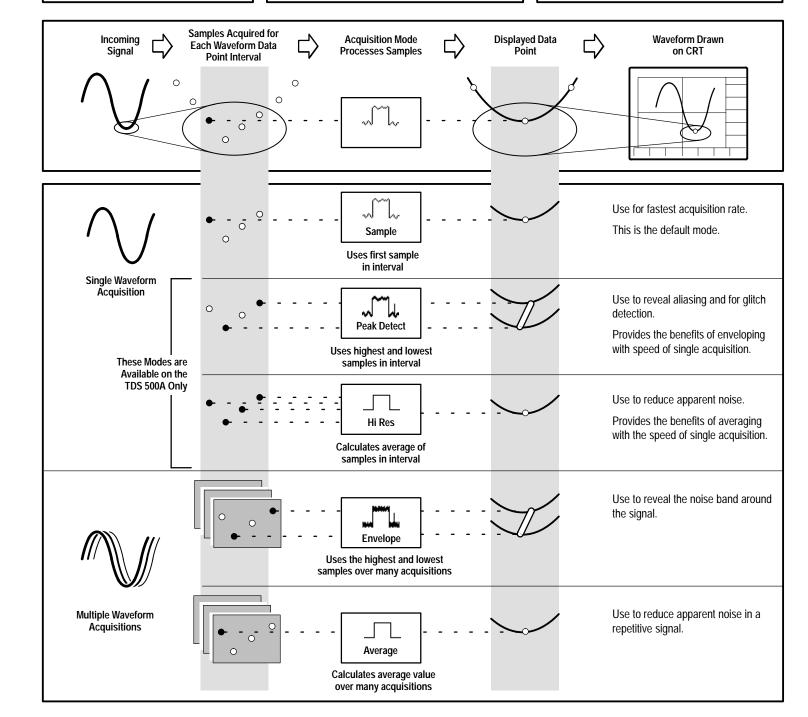
Acquisition Mode



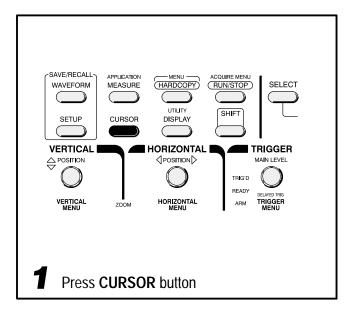


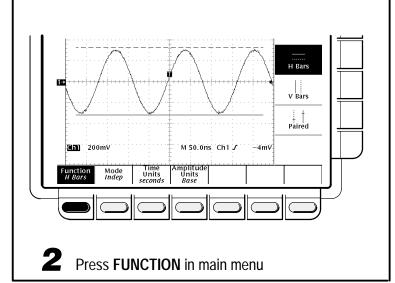


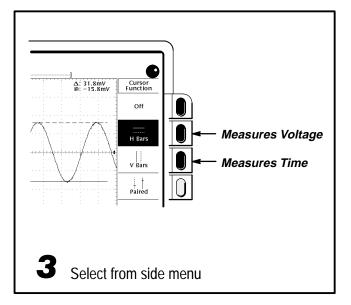
From the side menu, select an acquisition mode that will serve your application

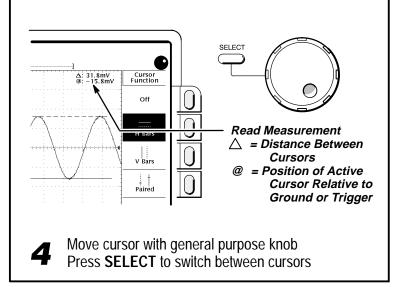


Cursor Measurements



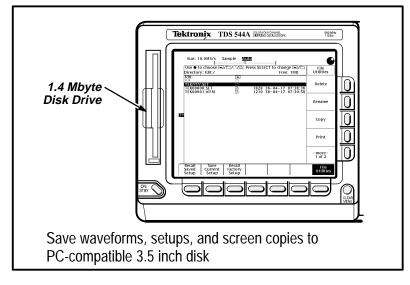




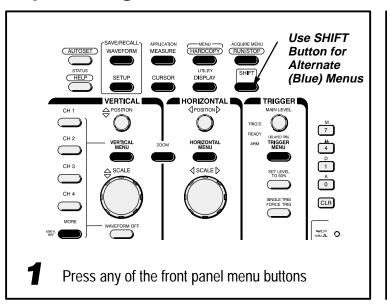


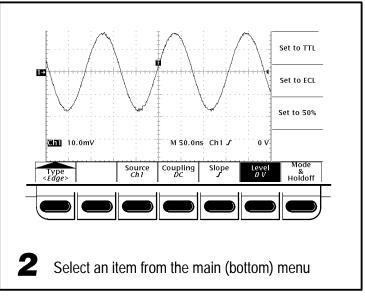
Mass Storage

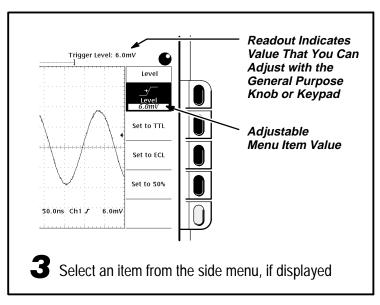
(Optional on TDS 520A, 540A, 620A, & 640A)

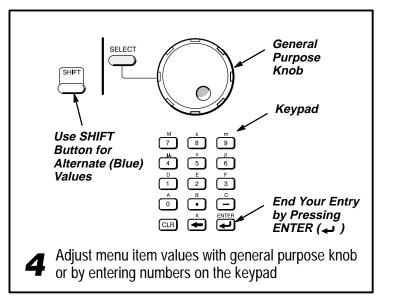


Operating a Menu

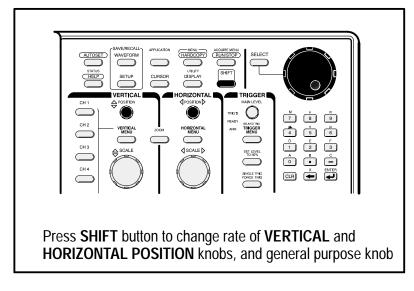




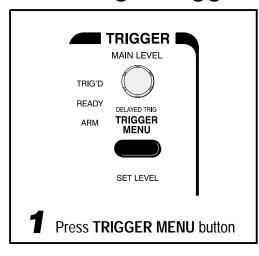


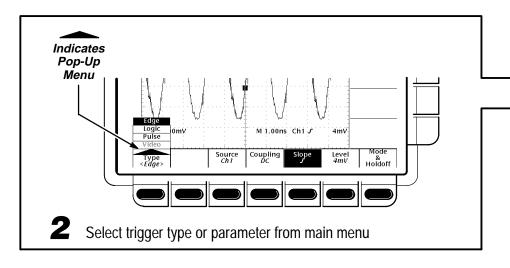


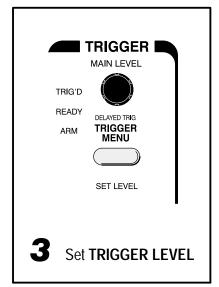
Quicker Adjustments

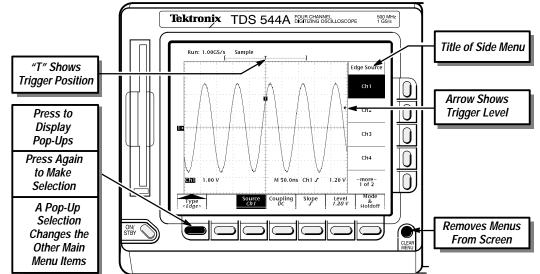


Selecting a Trigger



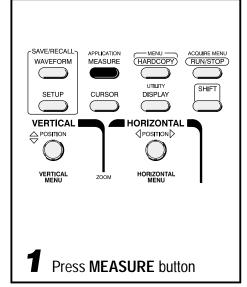


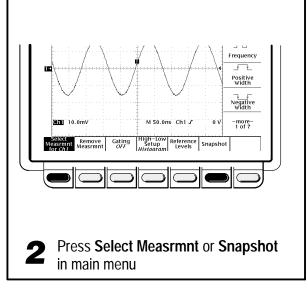


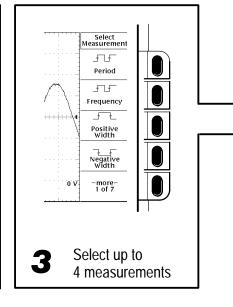


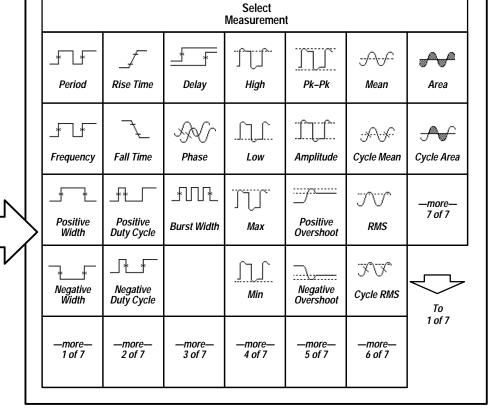
TRIGGER SELECTIONS TYPE TYPE TYPE TYPE <Logic> <Pulse> <Video> <Edqe> **CLASS** CLASS CLASS **CLASS** CLASS (Optional) <Glitch> <Pattern> <State> <Width> <Runt> AND D Positive _____ Positive -Positive | | | | AND NTSC DC DC Negative] [1] OR ∌ Negative -OR Negative _____ AC AC \sim PAL NAND Either [] HDTV NAND Reject NOR ⇒ Within LF Reject NOR ⋽ Width _□→\+ FlexFmt Limits Noise Rej (DC Out of Low Sensitivity) Limits Define High, Low, Define High, Low, Upper Positive and Don't Care and Don't Care states Upper The Limits states for channels for channels 1, 2, 1, 2, 3, and 4 Qualify condition Clock on channel 4 Reject Glitch Lower Lower Lower Negative by time Limits

Automated Measurements









AUTOMATED MEASUREMENT SELECTIONS

