

INSTRUCTION MANUAL
MODELS 1080/1081
SWEEP GENERATOR

WAVETEK[®] INDIANA, INC.

5808 CHURCHMAN, P.O. BOX 190

BEECH GROVE, INDIANA 46107

317-787-3332

(TOLL FREE OUTSIDE INDIANA)

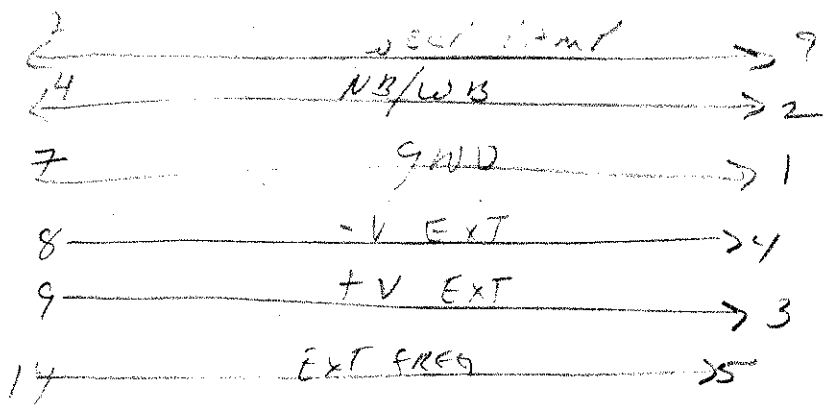
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CONTAINS MOD FOR TEST BENCH

INSTRUCTION MANUAL
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SWEEP GENERATOR

Y416-1081

J4 CONTROLS



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SECTION 1

GENERAL INFORMATION

1.1 INTRODUCTION

The Wavetek Models 1080/1081 are economical, laboratory-quality, wide-band sweep generators designed to meet the needs of engineering, production, maintenance, and educational facilities.

The major features of these instruments include the wide frequency range (1 to 1000 MHz), the ability to sweep over any portion of the frequency range, digital frequency and output level readouts, and display linearity of 1%. Operating modes include CW, Δf , and FS (full sweep). A unique "auto-zero" circuit virtually eliminates annoying frequency drift normally associated with wide-band sweep generators, and provides for superior narrow-band operation and improved frequency accuracy.

A crystal-marker circuit provides markers at 1, 10, and 100 MHz harmonic intervals (with different amplitudes for positive identification of the interval points) throughout the entire frequency band. Marker width is adjustable from the front panel for optimum wide- and narrow-band operation. An external marker input is provided on the front

panel. A calibrated variable "pinball" marker is also provided in the full sweep mode.

The rear-panel SLOPE control allows up to 1 dB/GHz (.1 dB/100 MHz) of slope (1 MHz pivot point) to be imposed on the output to compensate for frequency-dependent variations in the external circuitry.

The instrument's center frequency, sweep width, and output level can also be remotely controlled via a rear-panel connector. This connector also provides access to the blanking, 1 V/100 MHz, sweep ramp, pen lift, and ± 18 V Power Supply output signals, as well as providing a connection for an external trigger input.

These instruments feature modular PC board plug-in construction for all non-RF circuitry. The RF circuitry is contained in its own shielded assembly. This concept offers protection against obsolescence, since updated and additional features can be simply and economically added as new requirements dictate. In addition, maintenance problems can be greatly simplified by stocking a few assemblies instead of hundreds of discrete components.

1.2 SPECIFICATIONS

1.2.1 FREQUENCY

RANGE	1 to 1000 MHz
INDICATOR	3½ digit display.
ACCURACY	
CW	± 10 MHz (see NOTE)
Δf	Center frequency ± 10 MHz (see NOTE) Sweep width at 100 MHz intervals (VERNIER off) ± 2% (VERNIER is uncalibrated).
FS	Start frequency (1 MHz) ± 1 MHz Stop frequency (1000 MHz) ± 10 MHz Variable marker ± 10 MHz
	NOTE — Using FREQ OFFSET and crystal markers, accuracy can be improved to ± 1 MHz at specific frequencies.
DISPLAY LINEARITY	± 1% at maximum sweep width
DRIFT	< 200 kHz/10 minutes after 30 minute warm-up

1.2.2 RF OUTPUT

IMPEDANCE	50 ohms (Model 1080); 75 ohms (Model 1081)
LEVEL	Continuously adjustable from +13 to -70 dBm (Model 1080) or +60 to -10 dBmV (Model 1081) in seven 10 dB steps plus vernier.
INDICATOR	3 digit display.
ACCURACY	
Flatness	±0.25 dB
OUTPUT Vernier	±0.5 dB
Step Attenuator	±0.3 dB + 1% of attenuation
Maximum Combined Error	±1.75 dB
BLANKING	RF output removed during sweep retrace
SPECTRAL PURITY	
Harmonics	≤ -15 dBc for RF output frequency of from 1 to 2 MHz ≤ -25 dBc for RF output frequency of from 2 to 10 MHz ≤ -30 dBc for RF output frequency of from 10 to 1000 MHz
Non-Harmonics	≤ -50 dBc for RF output frequency of from 1 to 400 MHz ≤ -35 dBc for RF output frequency of from 400 to 1000 MHz
RESIDUAL FM	< 10 kHz peak in CW mode

1.2.3 SWEEP

OPERATING MODES	CW, Δf , and FS (full sweep)
CW	Frequency is set via the front-panel FREQUENCY control and displayed on a 3½ digit display (resolution=1 MHz).
Δf	Center frequency is set via the FREQUENCY control. Sweep width is set in calibrated 100 MHz steps plus a 100 MHz vernier.
FS (full sweep)	The entire 1 to 1000 MHz frequency range is swept. The FREQUENCY control and 3½ digit FREQUENCY display control a 1 to 1000 MHz variable ("pinball") marker.
SWEEP MODES	Recurring sweep, single (triggered) sweep
SWEEP WIDTH	200 kHz to 1000 MHz
SWEEP TIME	Fast: Adjustable from 0.01 to 1 sec (typ) Slow: Adjustable from 1 to 100 sec (typ) Line: 1/2 of AC line period
HORIZONTAL OUTPUT	0 to 10 V. In CW mode, the output voltage is proportional to the RF output frequency (1 V/100 MHz).
Accuracy	CW $\pm 1\%$ $\Delta f \pm 0.2 V$ FS $\pm 0.2 V$
Impedance	1 kohm

1.2.4 MARKERS

	Birdy bypass marker system provides markers at 1, 10, and 100 MHz harmonic intervals. Front-panel switch provides selection of 100 MHz, 100 and 10 MHz, or 100, 10, and 1 MHz harmonic markers. When multiple markers are selected, amplitude differences provide identification of markers.
ACCURACY	$\pm 0.005\%$
SIZE	Adjustable from approximately 1 mVpp to 1 Vpp
WIDTH	Adjustable from approximately 100 to 400 kHz in three steps
EXTERNAL MARKER	Front-panel connector provides a birdy marker by combining an external CW signal with a sample of the sweep oscillator signal. Input signal required is approximately 0.1 V into 50 ohms.
VARIABLE MARKER	This voltage-derived marker is produced only in the FS mode. A bright spot ("pinball") is produced on the display by momentarily delaying the forward sweep for approximately 2 msec. (Accuracy is as listed in Section 1.2.1.)

1.2.5 INTERFACE

	A rear-panel plug provides connections for remote programming and interfacing with other instruments.
TRIGGER	The sweep may be externally triggered by a high-to-low transition (TTL or contact closure).

FREQUENCY	This input allows remote control of CW frequency, Δf center frequency and sweep width, and also provides external FM capability.
Impedance	100 kohms
Tuning Sensitivity	1 V/100 MHz (0 V=1 MHz)
FM Response	DC to 8 Hz (3 dB) in CW mode.
LEVEL	This input allows remote control of the RF output level over a > 13 dB range (Model 1080) or > 10 dB range (Model 1081). Sensitivity is 1 dB/V. This input is not recommended for external AM.
PEN LIFT	Provides contact closure during slow forward sweep.
BLANKING	Approximately -15 V during forward sweep and +15 V during retrace.
SWEEP	Provides connection to the 0 to 10 V sweep ramp.
1 V/100 MHz	Provides a reference voltage output proportional to the instantaneous RF output frequency.
POWER SUPPLY	Provides connection to ± 18 V and ground. (Maximum external current draw is 10 mA for each supply.)
1.2.6 GENERAL	
OPERATING TEMPERATURE	20° to 30° C all specifications apply 0° to 50° C with slight degradation of specifications
POWER REQUIREMENTS	100 \pm 10 VAC, 115 \pm 10 VAC, 200 \pm 20 VAC, or 230 \pm 20 VAC; 50 to 400 Hz; approximately 25 W
CONNECTORS	BNC except for INTERFACE connector which is 15-pin in-line plug.
DIMENSIONS	28.6 cm wide x 13.3 cm high x 26.7 cm deep (11 1/4" x 5 1/4" x 10 1/2")
WEIGHT	5 kg (11.0 lb) net; 7 kg (15.5 lb) shipping
1.3 ACCESSORIES	
1.3.1 FURNISHED WITH INSTRUMENT	Instruction manual Mating jack for INTERFACE plug (MOL 09-50-7151 housing with 08-50-0107 contacts; W-I P/N 2112-00-0033 and 2113-07-0001). Fuse for 200/230 VAC operation; ¼ A, time delay
1.3.2 AVAILABLE AT EXTRA COST	Rack Mount Kit K019 for standard 19" instrument rack Wide Band RF Detector Model D151 (50 ohms, up to 1000 MHz) Wide Band RF Detector Model D171 (75 ohms, up to 1000 MHz)

SECTION 2

OPERATION

2.1 INTRODUCTION

This section provides electrical and mechanical (including rack mounting) instructions, functional control descriptions, operating instructions, and programming instructions for Models 1080/1081. In addition, special operating notes cover sweep rate errors, overloading, low-level measurements, and marker generation by level modulation.

2.2 MECHANICAL INSTALLATION

2.2.1 INITIAL INSPECTION

After unpacking the instrument, visually inspect external parts for damage to knobs, connectors, surface areas, etc. The shipping container and packing material should be saved in case it is necessary to reship the unit.

2.2.2 DAMAGE CLAIMS

If the instrument received has been damaged in transit, notify the carrier and either the nearest Wavetek area representative or the factory in Indiana.

Retain the shipping carton and packing material for the carrier's inspection.

The local representative or the factory will immediately arrange for either replacement or repair of your instrument without waiting for damage claim settlements.

2.2.3 RACK MOUNTING

Refer to Figure 2-1.

The K019 Rack Mount Kit enables mounting the instrument in a standard 19 inch wide rack.

CONTENTS

ITEM	DESCRIPTION	WAVETEK P/N	TO
A	Top Tray	1410-00-1130	1
B	Side	1410-60-1660	2
C	Bottom Tray	1410-00-1120	1
D	Screw, 10-32 x 1/2"	2810-18-0208	4
E	Screw, 6-32 x 3/8"	2810-17-6106	12
F	Handle	2810-07-0001	2

PROCEDURE

Attach both sides (item B) to the bottom tray (item C) using the #6 screws (item E) provided. Position the instrument on the tray so that the feet fit into the provided holes. Attach the top tray (item A) with the remaining #6 screws (item E). Attach the handles (item F) with the #10 screws (item D) provided.

2.3 ELECTRICAL INSTALLATION

The instrument can operate from any of 100, 115, 200, or 230 VAC supply mains. The rear-panel LINE VOLTAGE switches select which of these operating voltages is being used and adjust the Power Supply accordingly. The Power Supply is designed to operate over a 50 to 400 Hz AC supply frequency.

Instruments are shipped from the factory set up for 115 VAC operation unless otherwise specified.

NOTE

Before operating the instrument, check that the rear-panel FUSE is the correct value for the supply voltages (see Section 2.5).

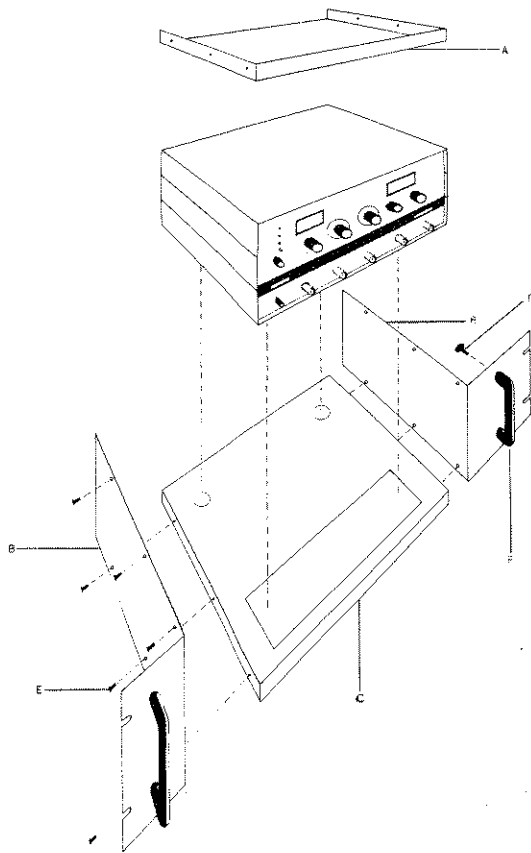


Figure 2-1. Rack Mounting

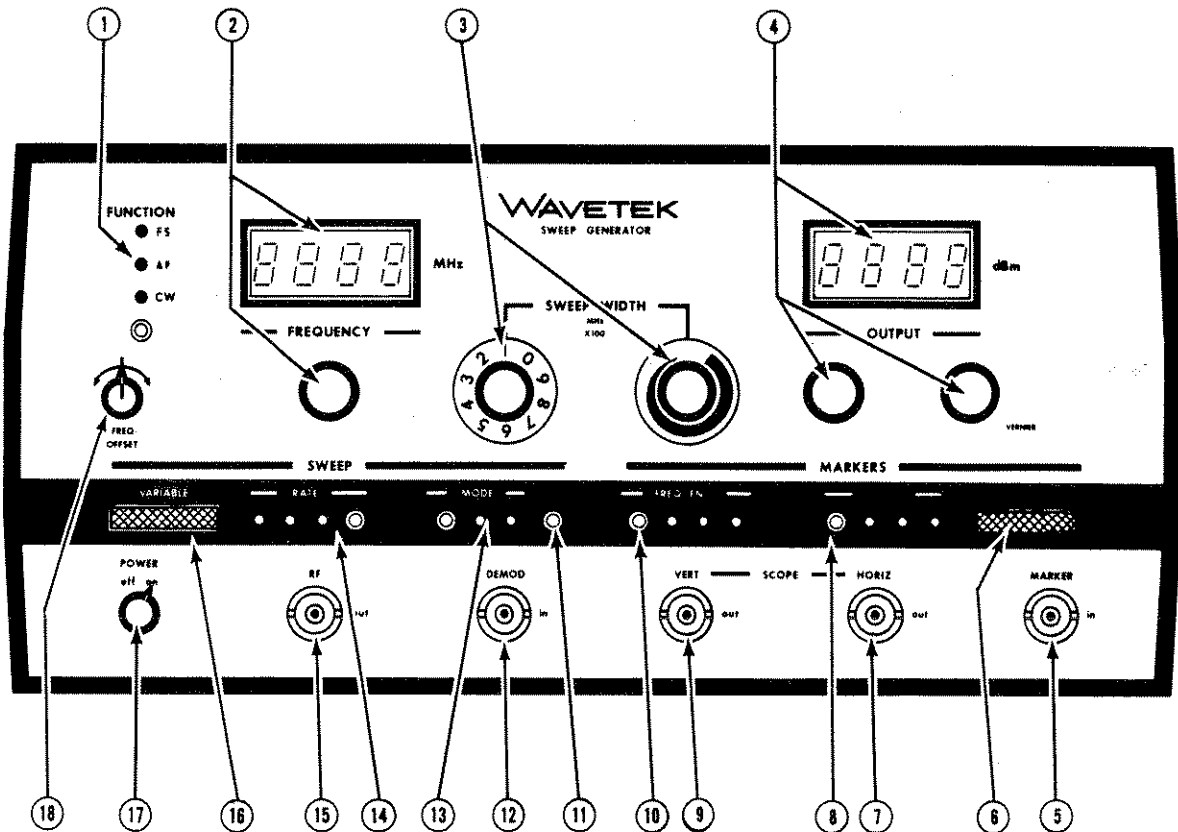


Figure 2-2. Front Panel

2.4 DESCRIPTION OF FRONT PANEL

Refer to Figure 2-2.

(1) **FUNCTION** pushbutton sequentially selects CW, Δf , or FS mode of operation. The selected mode is indicated by a lit LED.

(2) **FREQUENCY** control adjusts the frequency in CW mode; the center frequency in Δf mode; and the variable "pinball" marker frequency in FS mode. The 3½ digit display indicates the frequency set by the control.

(3) **SWEEP WIDTH** controls set the sweep width in Δf operation. The switch selects the sweep width in 100 MHz steps. The **VERNIER** provides continuous adjustment from 0 (full ccw) to 100 MHz (full cw). When the **FUNCTION** is set for CW or FS mode, the **SWEEP WIDTH** controls have no function.

(4) **OUTPUT** controls set the RF output level. The **STEP** control provides 0 to 70 dB of attenuation in 10 dB steps. The **VERNIER** provides > 13 dB of continuous attenuation when the step control is set to 0 dB (+13 to 0 dBm output, Model 1080 only), and > 10 dB of continuous attenuation at all other step control settings. The 3 digit display indicates the output level set by the controls.

(5) **MARKER IN** connector accepts an external CW signal to produce a birdy marker at the external frequency.

NOTE

The external marker will not operate properly when only the 100 MHz harmonic markers are selected. When 100/10 or 100/10/1 MHz harmonic markers or no markers are selected, the external marker will function properly.

(6) **MARKERS SIZE** thumbwheel control adjusts the birdy marker amplitude.

(7) **SCOPE HORIZ OUT** connector provides the 0 to 10 V triangle waveform to drive a display oscilloscope horizontal channel.

(8) **MARKERS WIDTH** pushbutton sequentially selects the birdy marker width. The selected width (wide \cong 400 kHz, narrow \cong 100 kHz) is indicated by a lit LED.

(9) **SCOPE VERT OUT** connector provides the vertical display signal (markers plus demodulated output if **DEMOD IN** connector is used) for a display oscilloscope.

(10) **MARKERS FREQUENCY** pushbutton sequentially selects 100 MHz harmonic, 100/10 MHz harmonic, or

100/10/1 MHz harmonic birdy markers. The active markers are indicated by lit LEDs.

(11) **SWEEP TRIG** pushbutton provides manual triggering of the sweep when the **SWEEP MODE** is set for single sweep.

(12) **DEMOD IN** connector accepts the detected swept signal from the device under test so that markers may be added. The combined signal is available at the **SCOPE VERT OUT** connector.

(13) **SWEEP MODE** pushbutton alternately selects either recurring or single sweep mode. The selected mode is indicated by a lit LED.

(14) **SWEEP RATE** pushbutton sequentially selects slow, fast, or line sweep rates (see Section 1.2.3). The selected rate is indicated by a lit LED.

(15) **RF OUT** connector provides connection for the RF output signal.

(16) **SWEEP VARIABLE** thumbwheel control provides continuously adjustable control of the sweep rate when the **SWEEP RATE** is set for either fast or slow rate. When the **SWEEP RATE** is set for line rate, the **SWEEP VARIABLE** control has no function.

(17) **POWER** switch applies AC power to the Power Supply.

(18) **FREQ OFFSET** control provides fine adjustment of the RF output frequency in CW or Δf mode independent of the **FREQUENCY** control or display.

2.5 DESCRIPTION OF REAR PANEL

Refer to Figure 2-3.

(1) **SLOPE** control adjusts the slope of the RF output up to ± 1 dB/GHz (± 0.1 dB/100 MHz) with the pivot point being 1 MHz.

(2) **INTERFACE** connector provides connections to internal power sources and outputs, and for external control of frequency, level, and triggering.

(3) **AC line cord** provides connection to AC supply mains.

(4) **LINE VOLTAGE** switches set the Power Supply to operate from the available AC supply mains.

(5) **FUSE** is time-delay; ½ amp for 100/115 VAC operation, ¼ amp for 200/230 VAC operation.

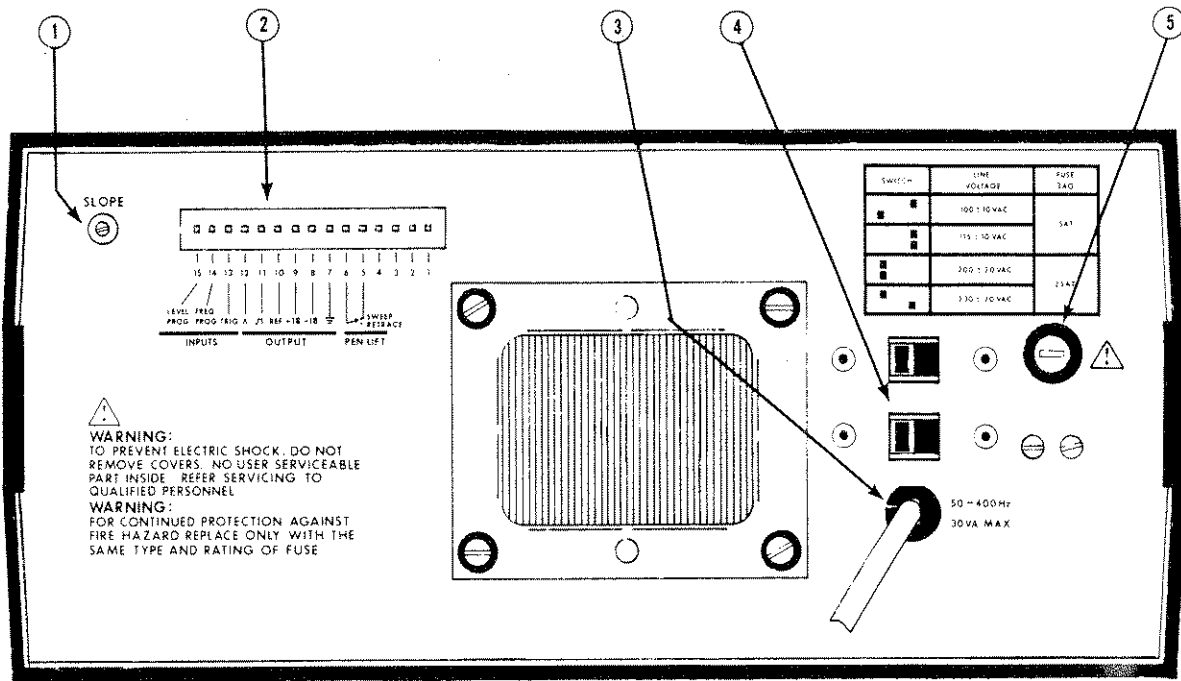


Figure 2-3. Rear Panel

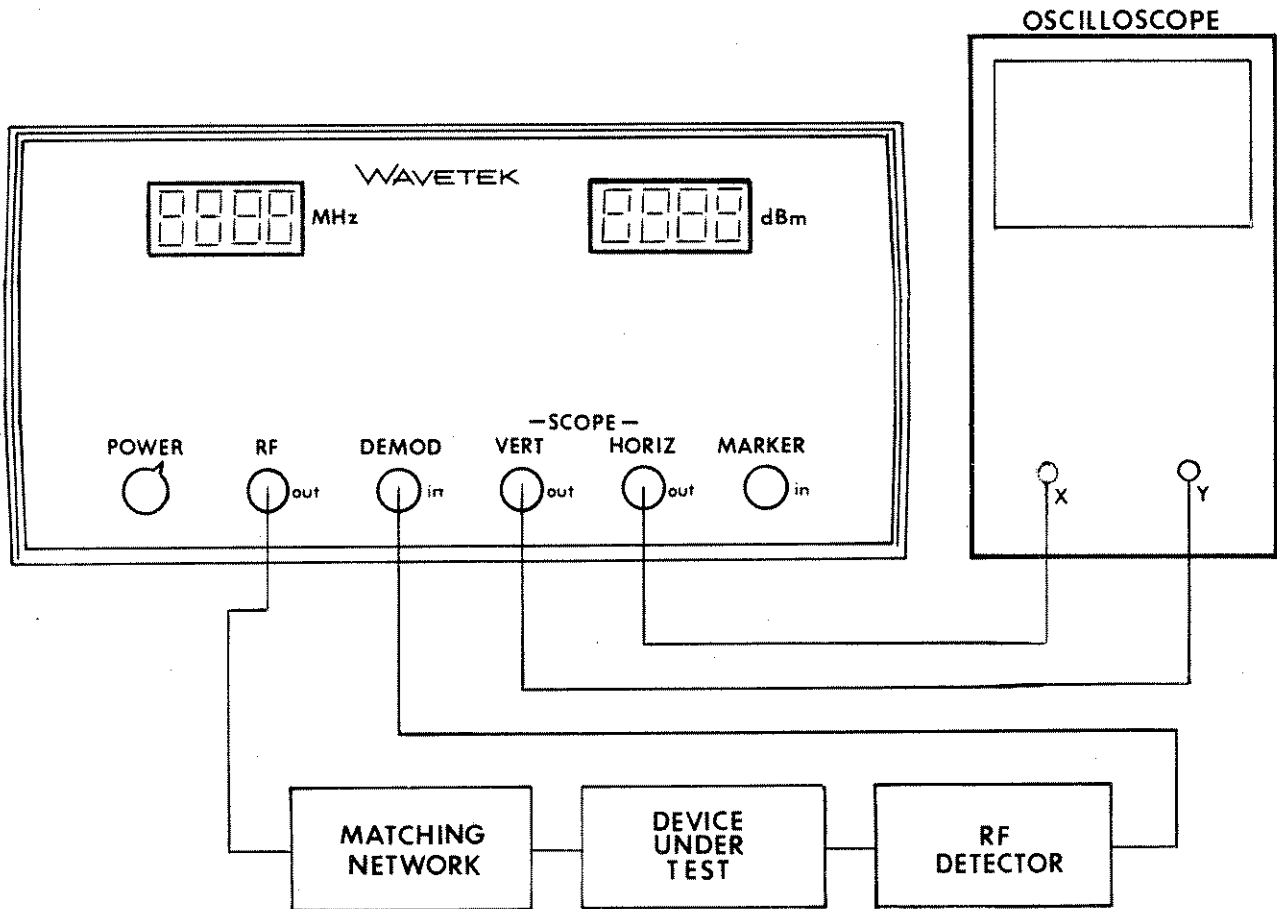


Figure 2-4. Typical Set-up

2.6 TYPICAL OPERATING SETUP

When initially setting up the instrument, first check the rear-panel LINE VOLTAGE switches and FUSE to ensure that the instrument is set for operation with the available AC mains.

Make connections between the sweep generator, the device under test, and the oscilloscope as shown in Figure 2-4. Since hum, RF leakage, and spurious signal pickup must be kept to a minimum, it is essential that good connections and grounds be maintained throughout the entire setup. Use coaxial cables with the appropriate RF connectors wherever possible. The RF output cable is especially critical. It should match the output impedance of the sweep generator and should be kept as short as practical (under 3 feet). If the input impedance of the device under test is not the same as the sweep generator output impedance, a matching network should be used to ensure that a constant-amplitude input signal is sent to the device under test (see Figure 2-4). After the RF signal passes through the RF circuit of the device under test, it must be demodulated before being connected to the sweep generator DEMOD IN connector. If a demodulator is not a part of the device under test, one must be added externally (see Figure 2-4). The input impedance of the demodulator must present the proper load to the RF circuit being tested. The Wavetek Model D151 RF Detector is recommended for 50 ohm applications.

Turn the front-panel POWER switch on. Lighted front-panel LEDs should indicate operation. After completing the setup, adjust the instrument controls for the required output frequency, output amplitude, and sweep rate. Turn the desired markers on and adjust their size and width. Allow a 5 minute warmup period before using to allow the auto-zero circuitry to stabilize. If the instrument is to be used at the extreme limits of its specifications, allow a 30 minute warmup period.

NOTE

When the unit is turned on or if there is an interruption of AC power, the following front-panel control settings will be automatically selected:

FUNCTION	FS
SWEEP RATE	Fast
MODE	Recur
MARKERS FREQUENCY	100 and 10 MHz
WIDTH	Wide

All other instrument control settings, including the RF output frequency and level, will remain as they were set prior to turn-on.

2.7 SPECIAL OPERATING NOTES

2.7.1 ERRORS FROM SWEEP RATE EFFECTS

When sweeping RF circuits having rapid amplitude changes, errors may occur, due mainly to detector delays. Decreasing the detector output time constant will minimize this effect. Figure 2-5 illustrates sweep rate effect.

To check for sweep rate effect, first set the sweep width to its lowest practical setting, then reduce the sweep time while closely observing the swept output response. Any change in the response indicates the sweep rate is too fast for a true response. When a further reduction of sweep time does not change the response, a true response has been obtained.

2.7.2 EFFECT FROM OVERLOADING

The use of excessive input signals to the device under test can cause overloading, which yields a distorted response. To prevent this, first set the OUTPUT controls for minimum output amplitude. Gradually increase the output amplitude until a response is obtained. Further increasing the output amplitude should not change the configuration of the response envelope except in amplitude. If the response envelope does change (such as flattening at the top) decrease the output amplitude just enough to restore the proper configuration.

2.7.3 MAKING MEASUREMENTS AT LOW LEVELS

When making measurements at low levels, radiation and ground loops can become problems. Using double-shielded cables for RF signals helps minimize the radiation problem. Ground loops causing hum pick up can sometimes be eliminated by completing only one ground connection between each instrument. This applies particularly to the oscilloscope horizontal input. If the ground connection is made at the vertical input terminal, an additional ground at the horizontal input terminal will often result in hum pick-up.

2.7.4 MARKER GENERATION BY LEVEL MODULATION

Birdy markers may be used to modulate the RF output signal. This permits viewing the frequency markers on the detected response without having to return the detected signal to the marker adder circuitry in the sweep generator. This is useful when the detector and scope display are remote to the sweep generator.

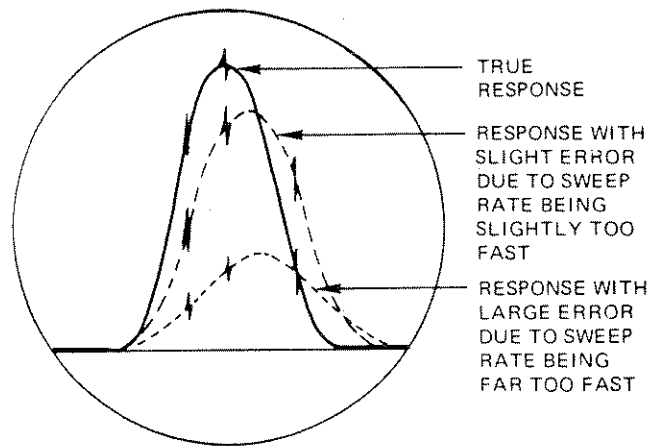


Figure 2-5. Sweep Rate Effects

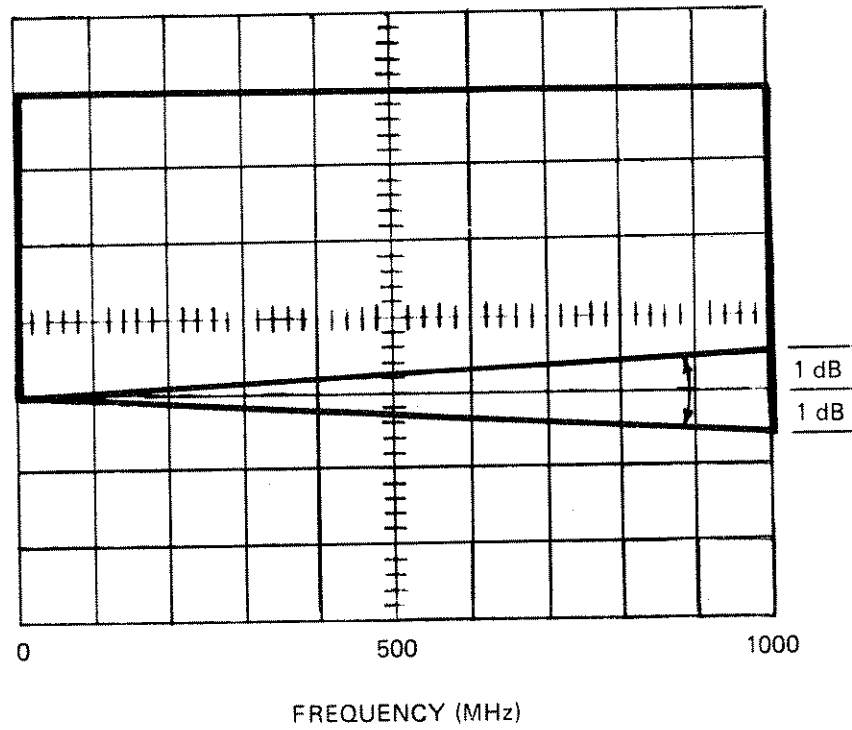


Figure 2-6. Slope

To employ this technique, connect the front-panel SCOPE VERT OUT to pin 15 of the rear-panel INTERFACE connector. The front-panel MARKERS WIDTH and SIZE controls will provide modulation adjustment.

The variable "pinball" marker in FS mode can also be generated by level modulation. It is normally an intensity marker generated by momentarily stopping the sweep ramp; however, at slow sweep rates, the momentary delay becomes indistinguishable, and level modulation is desirable. To provide level modulation with a variable marker, remove the instrument top cover (see Section 5.2) and move the jumper jack on the Variable Marker plug (see Figure 5-10) to the on position. This will produce a 1 dB reduction of the RF output coincident with the variable marker pulses.

2.7.5 AUTO-ZERO CIRCUIT

To minimize RF output frequency drift, an auto-zero circuit is incorporated into the RF oscillator design. At approximately 5-minute intervals, the RF output frequency is shifted to 0 MHz and a corrective signal applied to the RF oscillator. This process requires approximately 5 msec. In Δf and FS modes, this interruption occurs prior to the beginning of the forward sweep where a normal delay occurs, and is therefore not noticeable on the sweep display. In CW mode, this interruption should be negligible.

2.7.6 IMPROVED FREQUENCY ACCURACY

The FREQUENCY OFFSET control, which provides adjustment of the RF output frequency independent of the FREQUENCY display reading, can be adjusted to improve the accuracy of the display to ± 1 MHz (instead of the normal ± 10 MHz) at specific frequencies. In Δf operation, for example, with the FREQUENCY display set to a specific marker frequency, the FREQUENCY OFFSET control is adjusted until the specific marker is aligned at the center of the display at a narrow sweep width. The accuracy improvement then applies to that specific frequency and a narrow range around that frequency. In FS mode, the variable ("pinball") marker can also be aligned on a specific birdy marker using the FREQUENCY control (the FREQUENCY OFFSET control has no function in FS mode) to improve the marker frequency display accuracy. Once set, the ± 1 MHz accuracy will apply at that frequency in all operating functions.

2.7.7 SLOPE CONTROL

This feature enables the RF output to be tilted in order to compensate for system tilt or to simulate cable losses. The amount of slope imposed is 0 to ± 1 dB/GHz set via the SLOPE control, with the pivot point being 1 MHz (see Figure 2-6).

NOTE

The front-panel OUTPUT vernier must be adjusted such that the imposed slope does not cause the vernier range to be exceeded.

2.8 INTERFACE CONNECTOR

The rear-panel INTERFACE connector provides connections to instrument signal sources and enables remote programming of some instrument functions. The INTERFACE connector pin functions are:

PIN	FUNCTION
1 thru 4	Unused
5, 6	Pen Lift
7	± 18 V Common
8	-18 V (10 mA)
9	+18 V (10 mA)
10	1V/100 MHz Ref
11	Blanking
12	Sweep Ramp
13	Ext Trigger
14	Ext Frequency Program
15	Ext Level Program

2.8.1 PEN LIFT

The Pen Lift feature provides a contact closure at INTERFACE plug pins 5 and 6 to operate an X-Y plotter's pen lift mechanism. The contacts are closed only during CW operation and during forward sweep when the SWEEP RATE switch is set for SLOW. At all other times, the contacts are open.

2.8.2 EXTERNAL TRIGGER

To trigger the sweep from an external source, set the SWEEP MODE switch for SINGLE and apply a TTL signal (or contact closure to ground) to INTERFACE plug pin 13. The sweep will be initiated by a high-to-low transition.

2.8.3 EXTERNAL FREQUENCY PROGRAMMING

NOTES

External frequency programming will not be indicated on the front-panel FREQUENCY display.

When the sweep generator is operating in CW mode, a filter is enabled which reduces residual FM, but prevents rapid frequency changes normally required during sweep and FM operation. External sweep and FM are possible in certain cases in CW mode, but are not recommended.

EXTERNAL CW

To remotely control the CW frequency, set the front-panel FREQUENCY control for 000 MHz and the FUNCTION switch for CW. A DC voltage (0 to +10 V) applied to INTERFACE connector pin 14 will control the CW frequency.

EXTERNAL SWEEP – CENTER FREQUENCY

To remotely control the sweep center frequency, set the FREQUENCY control for 000 MHz, the FUNCTION switch to Δf , and the SWEEP MODE switch for RECUR. An external signal applied to INTERFACE connector pin 14 will tune the center frequency (sensitivity = 100 MHz/V). The external signal may be DC, a ramp, or any other waveform between 0 and +10 V.

NOTE

If the FREQUENCY control is not set for 000 MHz, the center frequency set via INTERFACE plug pin 14 will be offset by the FREQUENCY control setting. For example, if the FREQUENCY control is set for 100 MHz and 4 V is applied to INTERFACE plug pin 14, the sweep generator will be programmed for an output frequency of 500 MHz.

2.8.4 EXTERNAL LEVEL PROGRAMMING

NOTE

External level programming will not be indicated on the front-panel OUTPUT display.

The RF output level may be remotely controlled by a DC voltage applied to INTERFACE connector pin 15 (sensitivity = 1 dB/V). The allowable input voltage range is dependent on the front-panel OUTPUT vernier setting, as follows:

MODEL 1080

OUTPUT Vernier Setting	Approximate Input Voltage Range (V)	Output Level Change (dB)
full ccw (minimum)	0 to +13	0 to +13
mid-range	-6.5 to +6.5	-6.5 to +6.5
full cw (maximum)	0 to -13	0 to -13

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OUTPUT Vernier Setting	Approximate Input Voltage Range (V)	Output Level Change (dB)
full ccw (minimum)	0 to +10	0 to +10
mid-range	-5 to +5	-5 to +5
full cw (maximum)	0 to -10	0 to -10

By applying a linear voltage ramp to INTERFACE connector pin 15, a linear dB sweep RF output may be produced.

SECTION 3

THEORY OF OPERATION

3.1 INTRODUCTION

This section first presents an overall block diagram analysis followed by a more detailed description of each circuit.

Before beginning the actual circuit description, it would be well to consider the mechanical arrangement of the instrument (Figure 3-3). This will enable the following block diagram and circuit description to be associated with its physical position, thereby providing a better understanding of the overall instrument.

3.2 FUNCTIONAL BLOCK DIAGRAM

The block diagram in Figure 3-4 shows the overall operation of the instrument and the relationships of the individual circuits.

The Power Supply provides regulated voltage sources of ± 18 , ± 10 , and $+5$ volts to supply the other circuits.

The sweep rate generator produces synchronized square waves and clipped triangular waves that have a fixed amplitude and a variable rate positive-going ramp. This circuit is controlled by the front-panel SWEEP MODE, SWEEP RATE, and SWEEP VARIABLE controls. The square wave blanks the RF output during retrace. The triangular waves are used for the oscilloscope horizontal output, and are also applied to the front-panel SWEEP WIDTH controls which adjust the sweep ramp amplitude to the oscillator.

A combination of voltages from the front-panel SWEEP WIDTH and FREQUENCY controls and the auto-zero circuit is summed, amplified, and shaped by the sweep drive circuit. (Shaping is required for FREQUENCY display linearity.) The shaped voltage is then applied to varactor diodes in a VCO (voltage-controlled oscillator). The VCO output is mixed with the output from a fixed oscillator, and the difference frequency (0 to 1000 MHz) is fed to a wideband preamplifier and amplifier. The amplifier output is leveled in a control loop consisting of a monitor diode, leveler amplifier, and VVA (variable-voltage attenuator). The RF output level is controlled by the OUTPUT vernier (part of the control loop) and the 70 dB Step Attenuator.

There are two front-panel displays, one for frequency in MHz, the other for output in dBm (Model 1080) or dBmV (Model 1081). The FREQUENCY display and A/D converter comprise a digital voltmeter with suppressed decimal points. The 0 to 1000 MHz display readings are produced by 0 to 10 volt front-panel FREQUENCY control settings. The OUTPUT display reading is a combination of the front-panel OUTPUT vernier and Step Attenuator settings. The OUTPUT vernier programs the 1's and 0.1's digit of a digital voltmeter. A rotary switch on the Step Attenuator programs the 10's digit. By shaping the OUTPUT vernier voltage to the reference input of the leveler amplifier, and by programming via the Step Attenuator position, the OUTPUT display matches the RF output level of the instrument.

The auto-zero circuit acts to correct the drift of the zero beat of the RF output frequency. It has two operating modes, a search mode which requires a few milliseconds, and a hold mode (normal operation) which lasts approximately 5 minutes. In the search mode, the normal sweep drive inputs are turned off, and a digital ramp sweeping from $+0.9$ to -0.9 volts is applied to the sweep drive input. When the monitor diode in the RF output senses the zero beat of the VCO and the fixed oscillator, the digital ramp stops, holds its voltage, and the normal sweep inputs are turned back on. This voltage is "held" as a correction voltage until the next search.

Harmonic birdy markers at 100, 10, and 1 MHz are referenced to a 10 MHz crystal oscillator. Markers at 10 MHz intervals are produced by a step-recovery or snap diode. Markers at 1 MHz intervals are generated by a divide-by-10 circuit and a side-band marker technique. Markers at 100 MHz intervals are generated by controlling the amplitude of the appropriate 10 MHz markers. A standing wave on the sweep sample line produced by a delay line is used to generate gate pulses at 100 MHz intervals. These pulses are used to open a switch, which increases the amplitude of the 10 MHz markers at 100 MHz intervals.

During full sweep operation, an intensity ("pinball") marker is generated by delaying the ramp of the sweep rate generator.

The following circuit descriptions are referenced to the schematics in Section 7.

3.3 POWER SUPPLY

The Power Supply provides regulated outputs of ± 18 , ± 10 , and +5 volts DC, and unregulated outputs of 20 and 2 VAC.

Power transformer T1 is wound with a tapped dual primary and three center-tapped secondary windings. Nominal primary voltages are selected by two rear-panel switches, S2 and S3. The line input of 100, 115, 200, or 230 VAC is selected by parallel and series connections of the primary.

The ± 18 supplies are dual regulators whose inputs are derived from the secondary of T1 via a grounded center tap and a bridge rectifier (CR101-CR104) connected in dual, full-wave configuration. Capacitor filtering (C101 and C103) is used at each regulator input. Each half of the dual regulator consists of an IC regulator (IC102 for +18 V; IC103 for -18 V). Each regulator is current and temperature limiting. Reverse-polarity protection is provided for each output (CR107 for +18 V; CR108 for -18 V).

The ± 10 outputs provide the most stable regulation in the system. A +10 V monolithic voltage reference (IC105) is powered by the +18 V regulator. The +10 V output is obtained from operational amplifier follower IC106A, and -10 V output from operational amplifier inverter IC106B. The reference is adjustable at R111. The ± 10 V regulators provide the reference for the ± 18 V error amplifiers.

The +5 V supply consists of a separate secondary with grounded center tap, full-wave rectifier CR105 and CR106, filter capacitor C105, and series voltage regulator IC104. This IC regulator is current and temperature limiting.

The 2 VAC supply is derived from a separate grounded center-tap secondary. This output supplies the filament power for the two front-panel vacuum fluorescent displays.

The 20 VAC unregulated input to the bridge rectifier is also the line-sync input for the sweep rate generator and the timer input in the auto-zero circuit.

3.4 SWITCHING AND LOGIC CIRCUITS

Front-panel pushbutton switches sequentially select the operating modes of the instrument circuitry. Switching is implemented by TTL logic. Each selection is indicated by a lit LED on the front panel. The FUNCTION selector and its associated logic will be described. The logic circuits associated with the other front-panel pushbutton switches operate in a similar manner.

The FUNCTION switch logic consists of pushbutton switch S3, debouncing flip-flop IC7A/B, two D-type flip-flops with preset and clear (IC8A/B), and six NAND gates. LEDs CR7 through CR9 are lit individually when the appropriate output is low.

At turn-on, pins 4 and 13 of IC8 are at logic low by the action of initializing circuit Q1, which presets the D flip-flops. Q of IC8A will thus be high, and Q of IC8B will be low. This results in IC9 pin 11 being high, pin 6 low, and pin 8 high. With pin 6 low, CR9 will be lit, and the instrument will be in FS mode. This will always occur at turn-on.

When C3 is fully charged, the collector of Q1 will go high, causing IC8 pins 4 and 13 to go high. With the preset and clear lines of IC8A/B high, the logic states will sequence as shown in Table 3-1. Sequencing begins when S3 is depressed and a clock pulse is transmitted to pins 3 and 11 of IC8. The flip-flops are positive-edge triggered. When clocking occurs, the bit on the D (data) input will be transferred to the Q output of the flip-flop.

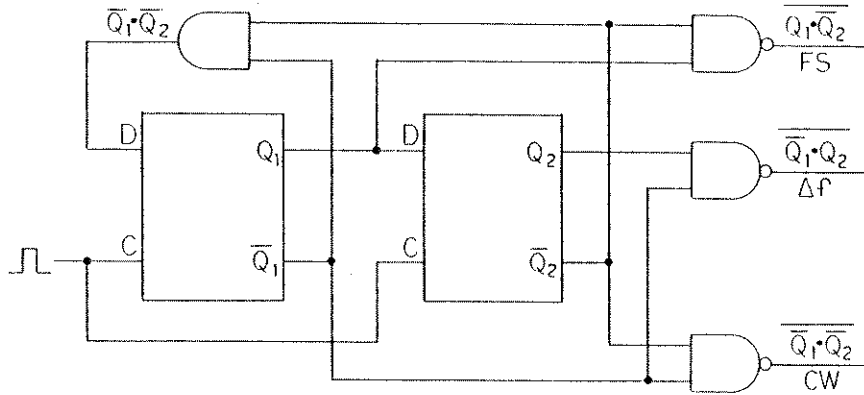
3.5 SWEEP RATE GENERATOR

The sweep rate generator is controlled by TTL signals from the Front-Panel Board. It is normally free-running (recur mode) but it can also be synchronized to the AC line (line rate mode), or manually triggered (single sweep mode). Except in line rate mode, it can be operated at any sweep time from 10 milliseconds to 100 seconds in two ranges (fast rate: 0.01 to 1 sec; slow rate: 1 to 100 sec). Speed within these ranges is controlled by the SWEEP VARIABLE control (R56) on the front panel. During CW operation, the sweep rate generator is disabled.

The sweep rate generator is basically a hysteresis oscillator. It consists of a comparator (IC205A) which produces the blanking square-wave plus another square-wave which is fed back to an inverting integrator (IC204A). This integrator produces a 0 to 10 V ramp which is the input to the comparator and the scope horizontal output.

The reference (inverting input) of the comparator stays at zero volts. When the non-inverting input goes below ground, the output goes negative. When the non-inverting input rises above ground, the output goes positive. Hysteresis is provided by resistively adding a negative potential to the non-inverting input during the negative portion of the square-wave output. This is accomplished by switch Q203. This negative bias must be overcome by the positive-rising ramp acting through resistor R424. By the time the ramp reaches +10 V, the voltage at the non-inverting input of the hysteresis switch should become slightly positive, which will flip the output positive and shut off switch Q203. Since very little current is flowing

Table 3-1. FUNCTION Switch Logic Diagram And Truth Table



Logic State	Low = 0 High = 1	Q1, Q2	1.0	0.1	0.0	1.0
Output	FS	$\overline{Q1} \cdot \overline{Q2}$	0	1	1	0
	ΔF	$\overline{Q1} \cdot Q2$	1	0	1	1
	CW	$Q1 \cdot Q2$	1	1	0	1
FF1	D1	$Q2 \cdot \overline{Q1}$	0	0	1	0
	Q1	----	1	0	0	1
	$\overline{Q1}$	----	0	1	1	0
FF2	Q2	Q1	1	0	0	1
	$\overline{Q2}$	----	0	1	0	0
	Q2	----	1	0	1	1

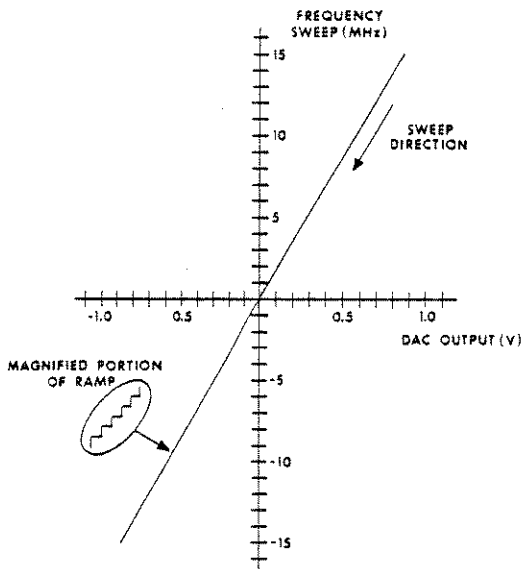


Figure 3-1. Auto Zero Digital Ramp

through R424, the voltage on the non-inverting input of the hysteresis switch will be approximately the same as the ramp voltage (+10 V); and since the input to the inverting integrator is positive, the ramp will start descending until it hits 0 V, at which time the hysteresis switch will go negative again, restarting the cycle.

The cycle can be interrupted on the rising portion of the ramp by a positive voltage applied to the gate of FET switch Q202. This interrupts the negative portion of the square wave feedback to the integrator, stopping the ramp. Four circuits can apply this interrupt voltage:

1. Front-panel selection of CW mode via the FUNCTION switch sends a TTL low signal to IC203 pin 4, producing +18 VDC at IC203 pin 2 which is applied to Q202 through CR239.
2. An open-collector TTL signal from the auto-zero circuit produces a +18 V pulse which is applied to Q202 through Q214 and CR238 to momentarily disable the sweep rate generator.
3. The start of each sweep is delayed approximately 4 msec by an interrupt pulse produced by an RC timing circuit acting on IC206B. The positive voltage is applied to Q202 through CR237.
4. The frequency program ramp is compared to the marker frequency voltage (in FS mode) by comparator IC204B which triggers a timing circuit acting on IC206B, producing an interrupt pulse. This results in an intensity marker on the display. The positive voltage is applied to Q202 through CR237.

When the sweep rate generator is operating in either of the triggered modes (line rate or single sweep), TTL lows from the front panel to the input of IC203C drive the output of that comparator to -18 V, which closes FET switch Q204 and removes the positive disarming voltage from the clamp circuitry. Switch Q204 allows a slight positive bias to be added to the positive input of the hysteresis switch (IC205A) during the descending portion of the ramp (the positive portion of the square wave), which forces the ramp to go more negative than ground before the hysteresis switch will flip and allow the circuit to run free. The ramp is prevented from reaching this more negative voltage by the action of the clamp circuitry. The ramp is monitored by Q208, and, when it reaches 0 V, Q208 is turned on and biases FET Q206, clamping the ramp off. At the same time, Q207 and Q208 are turned on, charging C205 to 18 V and arming the trigger. C205 can be suddenly discharged by either Q223 and Q212 on receipt of a trigger. This passes a negative spike through Q243, C206, and Q204 to the

non-inverting input of the hysteresis switch, causing it to change states and start another sweep cycle.

The frequency of the sweep rate generator is determined by potentiometers and resistors in the square-wave feedback loop of the integrator. These resistors control the amount of current allowed to charge and discharge the integrator's capacitor.

In fast rate mode, the output of IC203B is driven negative by the action of a TTL low signal from the front-panel sweep rate circuitry. This negative voltage acts to close FET switch Q209, thus bypassing R396 and allowing the smaller resistance of R307 to control the feedback current. During retrace blanking, Q226 places R393 and R452 in parallel with R397. Since they are much larger than R397, there is very little difference in the charge and discharge times as long as front-panel SWEEP VARIABLE control R56 is fully right (maximum feedback current). When R56 is fully left, the retrace is speeded up by the current through R393 and R452.

In slow rate mode, FET switch Q209 is open, thus inserting R396 into the feedback circuit. When R56 is fully clockwise, the retrace time is less than the sweep time. When R56 is fully left, the sweep time increases to > 100 sec, but the retrace time will increase only to about 2 sec.

In line rate mode, IC203A is driven positive by the action of a TTL signal from the front-panel sweep rate circuitry. This closes FET switch Q224, allowing R394 and R395 to be the main path for feedback current. The other resistors are still in the feedback loop, but their contribution to the total current is so small that the setting of R56 is negligible.

The positive output of IC203A also closes FET switch Q210, allowing the 20 VAC line-rate signal to trigger the circuit.

In CW mode, Q227 clamps the ramp at a slightly positive voltage, so the output is always enabled.

3.6 SWEEP DRIVE

Analog switch IC225 controls the inputs to summing amplifier IC227A. The shaper (or diode-break network) consists of diodes CR208 through CR216, potentiometers R331 through R339, biasing resistors R315 through R330, and current source Q216/IC226. This circuit shapes the linear ramp and other voltage inputs, and, after amplification by IC203A/B, provides the shaped varactor drive for the VCO to produce a linear frequency output.

During CW operation the CW line is low and the ΔF line is high. This opens the switch between pins 10 and 11 of IC225 and closes the switch between pins 14 and 15, disconnecting the SWEEP WIDTH controls which supply the ramp to the summing amp and grounding the negative side of C237.

During ΔF operation, the combined sweep and fixed inputs, including the auto zero (see Section 3.7) and FREQ OFF-SET voltages, are applied to the summing amplifier input.

During FS operation, the input from the front-panel FREQUENCY control is disconnected when the switch between pins 2 and 3 of IC225 opens. The switch between pins 6 and 7 of IC225 closes so that the full sweep ramp is applied to the summing amp. The FREQUENCY control provides an intensity marker by delaying the ramp as described in Section 3.5.

3.7 AUTO ZERO

The purpose of this circuit is to correct for RF output frequency drift.

During normal operation (auto zero hold mode), a correction voltage is maintained at IC223 pin 4 while timer IC219 counts. When IC219 times out, the clock pulse produced transfers a logic 0 to the output of a latch (D-flip-flop IC217 pin 5) and to the input of a second latch (IC217 pin 12). The logic 0 is transferred to the output of the second latch (IC217 pin 9) by either the inverted blanking signal at the start of a sweep (ΔF or FS mode) or the clear input (IC217 pin 11) being held low (CW mode). When both latch outputs are low (logic 0), NAND gate IC222D sends a high to IC216B, which caused pin 4 of IC216 to go low. When this occurs, the auto zero circuit goes into its search mode, during which the following actions occur:

- 1) All analog switches in IC225 are open.
- 2) DAC counter IC224 is reset and oscillator IC222B is started.
- 3) Preset gate IC218A is enabled.
- 4) IC218 pins 10 and 12 (sweep stop) to high, disabling the sweep rate generator.

Since all analog switches in IC225 are open, the only input to the sweep drive circuit (IC227 pin 2) is the DAC output from IC223 pin 4. As DAC counter IC224 advances its count, the DAC output steps from $\sim +0.9$ to ~ -0.9 V. This digital ramp produces a frequency sweep of $\sim \pm 15$ MHz about the nominal zero beat of the sweep oscillator (see Figure 3-1).

When a zero beat of the VCO and fixed oscillator occurs, the monitor signal (from CR504) goes slightly positive and triggers comparator IC221. This causes pin 1 of IC221 to go low and pin 10 of NOR gate IC222C to go low. Since pin 9 of IC222C is also low, pin 14 of IC222C will go high. With pin 14 high, the latches in IC217 will be preset high (returning analog switch IC225 to its former state); oscillator IC222B turned off, the DAC counter stopped, and the digital ramp voltage held constant at IC223 pin 4, the DAC output. This terminates the search mode cycle. The constant voltage at the output of the DAC is applied through a voltage divider to the input of the summing amplifier. This correction voltage will be held until the next search cycle.

The search cycle requires approximately 5 milliseconds depending upon the correction voltage. The length of the hold cycle depends upon timer IC219 reaching a 2^{14} (16,384) count. At 60 Hz, the elapsed time is 4 minutes and 33.1 seconds.

By moving jumper J201 (see Figure 5-10) to its CAL position, the counting rate increases so that the hold mode lasts only a few seconds. The auto zero circuit may be disabled by moving jumpers J202 and J204 to their CAL positions.

3.8 SWEEP OSCILLATOR

The sweep oscillator is a heterodyne oscillator which tunes from 0 to 1000 MHz. It consists of a VCO (2650 to 3650 MHz), a fixed-frequency oscillator (2650 MHz) and a double-balanced mixer (MX501). The VCO includes Q506 and varactor diodes CR510 and CR511. The varactor diodes are hyperabrupt devices with relatively high Q and relatively low tuning voltage range.

The fixed oscillator is similar to the VCO with the exception of the tuning capacitor, which is fixed.

The oscillators are enclosed in separate compartments.

The oscillator outputs are mixed in MX501, and the difference frequency (0 to 1000 MHz) is applied to the wideband amplifier (see Section 3.9), producing the instrument RF output.

3.9 RF OUTPUT

The RF output circuits include the wideband amplifier, leveler circuits, Step Attenuator, and OUTPUT display.

The wideband amplifier consists of five stages, including a two-stage preamplifier. It includes RF integrated-circuit amplifiers IC501 through IC504, and output transistor

Q504. The final three stages are leveled by pin diodes CR502 and CR503 in a VVA. The cathode of monitor diode CR504 in the wideband amplifier output is a zero impedance point, thus R524 provides the nominal generator output impedance.

The second through fourth stages of the wideband amplifier are turned off by the blanking square wave applied to Q502. The leveler amplifier on the Main Board (IC231A) is turned off by the blanking square wave applied through CR217.

A sweep sample is taken from the second stage of the wideband amplifier and is amplified by IC506. This amplified sweep sample is then leveled by VVA CR507/CR508, monitor diode CR506, and leveler amplifier IC505. Leveler amplifier IC505 is turned off by the blanking square wave applied to Q505.

The OUTPUT vernier circuit consists of potentiometer R48, level shifting operational amplifiers IC14A/B and IC15A/B, and relay K1A/B. This circuit is located on the Front Panel Board. There are two outputs from this circuit, and two ranges. One output is measured by a DVM (1's and 0.1's digits). This DVM consists of A/D converter IC232, inverters IC213 and IC214, and front-panel display DS1. The other output is fed to shaper IC229. The vernier voltage also depends upon the Step Attenuator position. These voltage relationships are as follows:

Step Attenuator Setting (dB)	Vernier Range (volts)	DVM Input (volts)	Shaper Input (volts)
0	+3 to -11	+13 to -1	-13 to +1
10 to 70	0 to -11	0 to -11	-10 to +1

The shaper, consisting of IC229A, IC231B, and adjustable diode-break network CR219 through CR222, feeds the shaped output to the reference input of the leveler amplifier, IC231A pin 2, to control the gain of the wideband amplifier, and so the RF output level. The rotary switch mounted on the rear of the Step Attenuator selects a voltage to be added to the OUTPUT vernier voltage to drive the DVM. The reading on the OUTPUT display is thus a combination of the OUTPUT vernier and Step Attenuator settings.

3.10 MARKER CIRCUIT

A 10 MHz crystal oscillator (Q602/X601) provides the basic reference frequency for all internal 100, 10, and 1 MHz harmonic markers in a birdy-bypass system. At instrument turn-on, 100 and 10 MHz markers are turned on by the action of Front Panel Board initializing circuit Q1, which presets pins 4 and 13 of IC2.

When the front-panel MARKERS FREQUENCY push-button switch is depressed, a logic low is transmitted via TTL gates on the Front Panel Board to one or more internal marker circuits. A low at the base of Q601 on the Marker Generator Board enables the 10 MHz crystal oscillator and 100 MHz markers. A low at pin 9 of IC110 on the Power Supply/Marker Board enables the 10 MHz markers, and a low at the base of Q607 on the Marker Generator Board enables the 1 MHz markers.

10 MHz MARKERS

Harmonics of the 10 MHz crystal oscillator are generated by step-recovery diode CR603 in emitter-follower amplifier Q603. The harmonics of 10 MHz are then mixed with the sweep sample in CR604. The sweep sample is filtered to limit frequencies below approximately 5.5 MHz. Wide birdies or beats are generated as the sweep sample mixes with the harmonics of 10 MHz, producing dual bands of frequencies less than and greater than the harmonic frequencies. The wide birdies are filtered to reduce bandwidth, and 10 MHz CW is trapped out. The filtered birdies are amplified by Q604 and added to a second sweep sample which is used to produce 1 MHz harmonic markers below 5 MHz. The amplified output is passed through a band-reject filter to limit 10 MHz CW.

The 10 MHz birdies and, if present, an external birdy marker from mixer CR601 are fed to a two-stage birdy amplifier, Q605/Q606. The amplified birdies are then fed to variable-bandpass amplifier Q101 on the Power Supply/Marker Board. Amplitude saturation of 10 MHz markers is determined by the setting of R116 at the input of this stage. Selectable filters are enabled by the MARKERS WIDTH pushbutton and associated TTL gates on the Front Panel Board. Open-collector hex buffers (IC107) are programmed via the TTL gates to switch in and short out filter components to provide wide (~ 400 kHz), medium, and narrow (~ 100 kHz) birdy markers.

In addition, one section of analog switch IC109 selects an additional capacitor for active filter IC108. The filtered birdies are then clamped to a ±5 volt level by complementary amplifier Q102/Q103. Low-level noise below ±0.5 volts is reduced by direct connection of the two emitters. The 10 MHz birdy markers are then summed with the 1 MHz markers, and amplified by common-base amplifier Q107. The resulting marker combinations are fed to MARKER SIZE control R6 on the Front Panel Board, and to the SCOPE VERT connector for external display.

1 MHz MARKERS

1 MHz harmonic markers are produced by two related techniques. For markers from 1 to 5 MHz, 1 MHz square

pulses are generated by dividing the 10 Hz crystal oscillator frequency. IC603 is configured in the divide-by-ten mode, and a 10 MHz signal is used as a clock input to pin 8.

Square pulses at a 1 MHz rate are provided at pin 12, one for every tenth clock pulse. A logic "high" at pin 13 enables the output. The square pulses are differentiated to provide odd and even harmonics up to 5 MHz. These are mixed with the sweep sample fed through sideband amplifier Q609 and a 10 MHz CW-reject filter. The mixing that occurs in transformer T601 and diode CR605 provides birdy markers at 1 MHz intervals up to 5 MHz.

For frequencies above 5 MHz, the marker sidebands at each 10 MHz interval in the 10 to 1000 MHz range are mixed with the 1 MHz harmonics as the sidebands sweep from 5 to 0 to 5 MHz. This produces a sequence of 1 MHz birdies around each 10 MHz interval.

Once generated, the 1 MHz harmonic birdy markers are processed in a manner similar to that used for the 10 MHz harmonic markers.

100 MHz MARKERS

100 MHz harmonic markers are produced by selectively increasing the amplitude of selected 10 MHz harmonic markers. An amplified sweep sample is fed to the open end of a shorted line whose length is a half-wave length at 100 MHz. This amplified sweep sample is detected by CR602, with the detected signal being amplified and applied to pin 7 of voltage comparator IC110. The resulting signal pattern peaks at 100 MHz intervals throughout the 0 to 1000 MHz range (see Figure 3-2).

The gated pulses at the comparator output, whose width is set by the comparator reference R143, cause an analog switch section in IC109 to open only when 100 MHz markers are selected, thus increasing the marker amplitude. When the switch section is closed, R164 loads the 10 MHz harmonic signal, decreasing the marker amplitude.

When 10 MHz markers are not programmed via the front-panel, pin 14 of IC109 must be grounded. Gate pulses are routed through IC110 to ensure that the appropriate switch section of IC109 opens when 100 MHz markers are selected, and then closes to ground the 10 MHz markers.

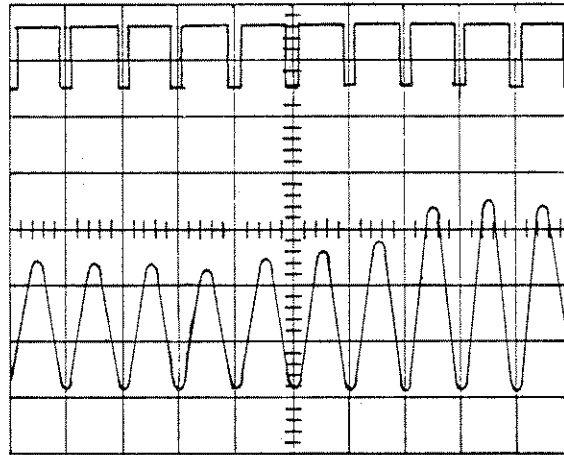


Figure 3-2. 100 MHz Markers Sweep Sample and Gated Pulses



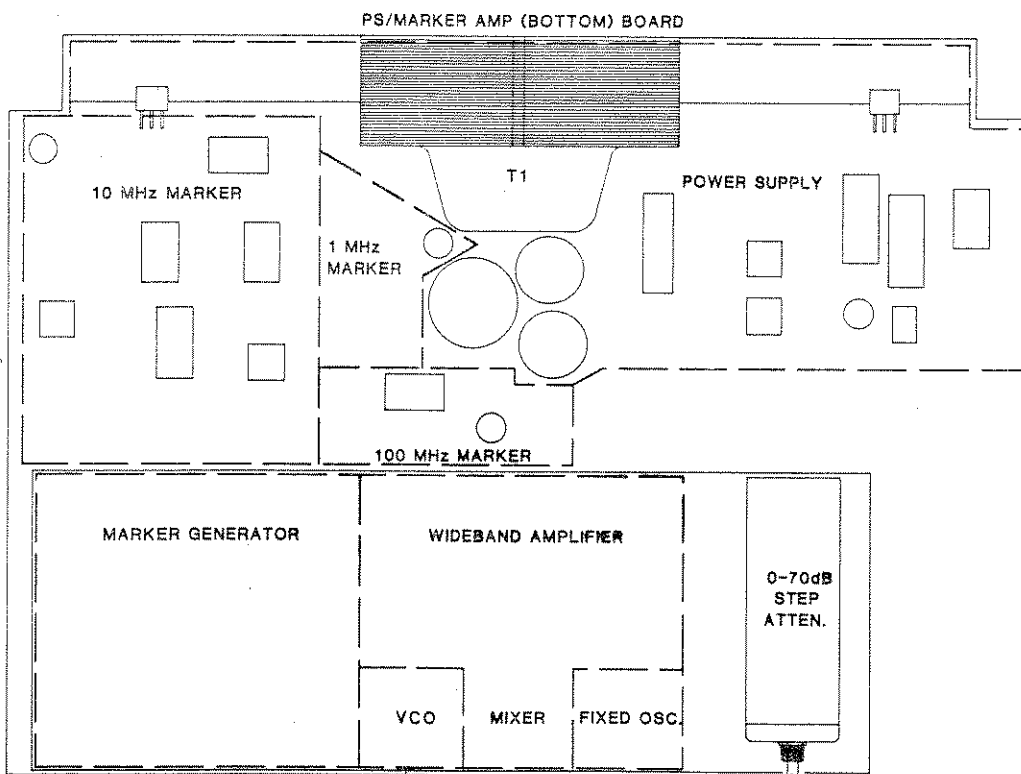
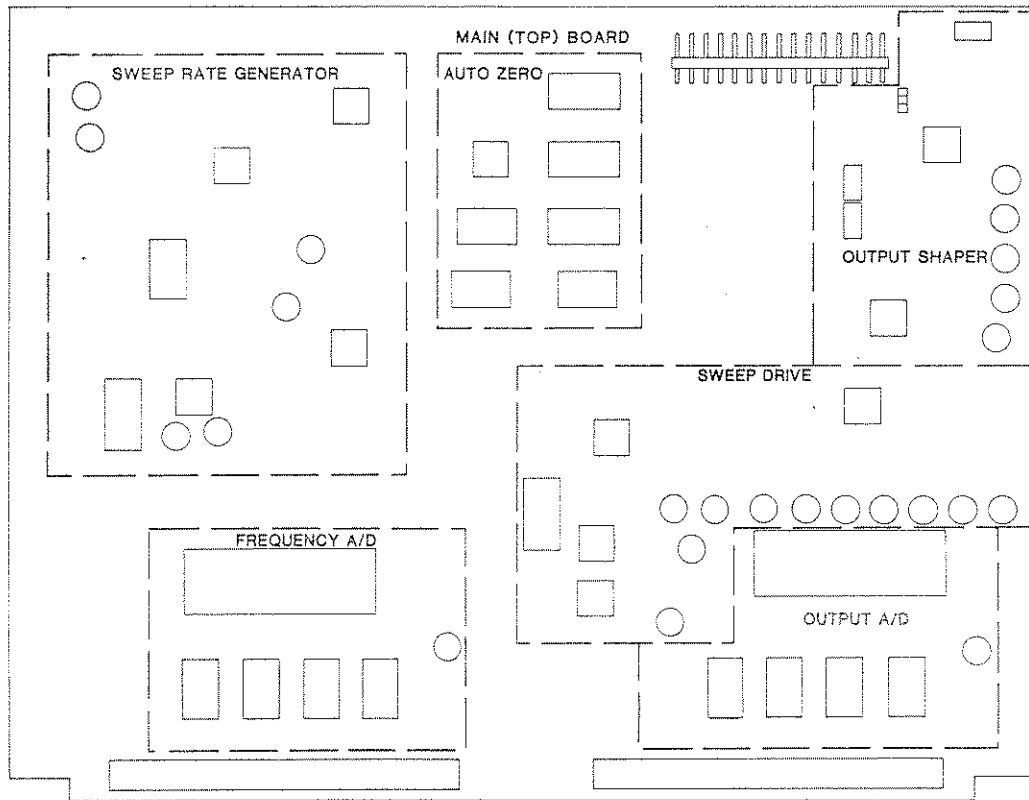


Figure 3-3. Circuit Arrangement

SECTION 4

PERFORMANCE TESTS

4.1 INTRODUCTION

The purpose of the performance tests in the following sections is to verify that the instrument meets its published specifications (Section 1.2). Critical specifications for each item of test equipment are listed in Table 4-1, Recommended Test Equipment. Except as detailed, settings of test equipment apply to performance test procedures. All other test equipment operating details are omitted.

The instrument should have its top cover removed for the

performance tests. Before applying power to the instrument, see Section 2 for details of electrical installation. The line voltage should be maintained at 115 or 230 volts $\pm 10\%$, 50 to 400 Hz throughout the tests. The performance test procedures are begun after a 30 minute minimum warmup of the instrument in a $+20$ to $+30^\circ$ C ambient temperature range.

If the instrument does not meet its published specifications, refer to Section 5, Maintenance, for troubleshooting and calibration information.

TABLE 4-1. RECOMMENDED TEST EQUIPMENT

INSTRUMENT	CRITICAL REQUIREMENT	RECOMMENDED
Oscilloscope	DC coupled 1, mV/cm sensitivity	HP 1200A
Digital Voltmeter	.04% Accuracy	Dana Model 4200
Power Meter	Frequency Range: 1 MHz to 1000 MHz	HP 435A/8482A
Modulation Meter	Frequency Range: 1 MHz to 1000 MHz	Wavetek 4101
Spectrum Analyzer	Frequency Range: 1 MHz to 2000 MHz	HP 8555A/8552B/141T
Precision Attenuator Pads	10, 20, and 40 dB	Weinchel 50-10, 50-20, 50-40
CW Signal Generator	Adjustable from 1 MHz to 1000 MHz, 0.1 VRMS output, ± 10 MHz Accuracy	Wavetek 3010
RF Detector	Frequency Range: 1 MHz to 1000 MHz	Wavetek D152
Frequency Counter	Frequency Range: to 500 MHz	HP 5300B/5303B

4.2 FREQUENCY RANGE AND ACCURACY TEST

4.2.1 ΔF OPERATION

With the FREQUENCY control set fully ccw, the FREQUENCY display should read 0 or 1 MHz.

Connect the equipment as shown in Figure 4-1.

Set the instrument front-panel controls as follows:

FUNCTION	ΔF
FREQUENCY control	full ccw
SWEEP WIDTH step	0 MHz
SWEEP WIDTH vernier	mid-range
OUTPUT	+13 dBm (Model 1080) or +60 dBmV (Model 1081)
SWEEP MODE	RECUR
SWEEP RATE	FAST
SWEEP VARIABLE	full right
MARKERS FREQUENCY	100/10 MHz
MARKERS WIDTH	WIDE
MARKERS SIZE	mid-range

Set the oscilloscope for X-Y operation and adjust the oscilloscope's vertical and horizontal controls for a display pattern similar to Figure 4-2 centered on the graticule.

Adjust the FREQUENCY control and SWEEP WIDTH vernier to position the zero lock-in point at the extreme left graticule line and the 10 MHz marker at the extreme right graticule line.

Turn on the 1 MHz markers. Visually count the markers from 10 to 1 toward the zero lock-in point. The 1 MHz marker should be clearly definable on the display.

Note that the top line of the detected display is at approximately 0 volts. This is the result of blanking the RF output during retrace. (Viewing of markers may be enhanced by decreasing the SWEEP VARIABLE control setting.)

Set the MARKERS SIZE control fully left, the FREQUENCY control fully ccw, and adjust the FREQ OFFSET control to center the zero lock-in point on the display (see Figure 4-3).

Adjust the FREQUENCY control clockwise and center markers at 100 MHz intervals on the display from 100 to 1000 MHz. The FREQUENCY display should read the marker frequencies within 10 MHz at each interval.

Set the FREQUENCY display to read 700 MHz. Adjust the FREQ OFFSET control until the 700 MHz marker is centered on the scope display. Reset the FREQ OFFSET control to mid-range and center the 700 MHz marker on the display using the FREQUENCY control. Turn on the

1 MHz markers. Adjust the FREQ OFFSET from fully cw to fully ccw and observe that the displayed 700 MHz marker is shifted approximately ± 10 MHz. This test may be performed at any other frequency in the 1 to 1000 MHz range.

The above tests for frequency range and accuracy are performed in ΔF operation at 10 MHz sweep width. Since the FREQUENCY control operates identically in CW mode, a CW test need not be performed unless desired.

4.2.2 FS OPERATION

Set the FUNCTION switch to FS. The zero lock-in point should be visible no more than 10 MHz in from the extreme left graticule line, and the 1000 MHz marker should be visible no more than 10 MHz in from the extreme right graticule line.

4.3 DISPLAY LINEARITY TEST

Display linearity is measured at maximum sweep width in ΔF mode. (In FS mode the sweep drive is the same except for centering, and when fine adjusted, the display linearity should be identical.) The horizontal linearity of the oscilloscope must be $< 0.5\%$.

Set up the equipment as shown in Figure 4-1, and set the instrument front-panel controls as follows:

FUNCTION	ΔF
FREQUENCY	500 MHz
SWEEP WIDTH step	900 MHz
SWEEP WIDTH vernier	full cw
SWEEP RATE	LINE
SWEEP MODE	RECUR
MARKERS FREQUENCY	100 MHz
MARKERS WIDTH	WIDE
MARKERS SIZE	mid-range

Set the oscilloscope for X-Y operation, and adjust the horizontal and vertical sensitivity and position controls to produce a display pattern similar to Figure 4-4. Fine adjust the FREQ OFFSET control and the oscilloscope's horizontal sensitivity and position controls until all markers in the comb coincide with corresponding vertical graticule lines within 0.5 minor division. This allowable error is equivalent to 1% display linearity.

4.4 SWEEP WIDTH TEST

Use the equipment setup and control settings of Section 4.3. Adjust the FREQ OFFSET control to position the zero lock-in point on the extreme left graticule line. Locate the 900 MHz marker. Turn on the 10 MHz markers and count up from the 900 MHz marker. The 990 MHz marker must be visible on the display, while the 1010 MHz marker must not.

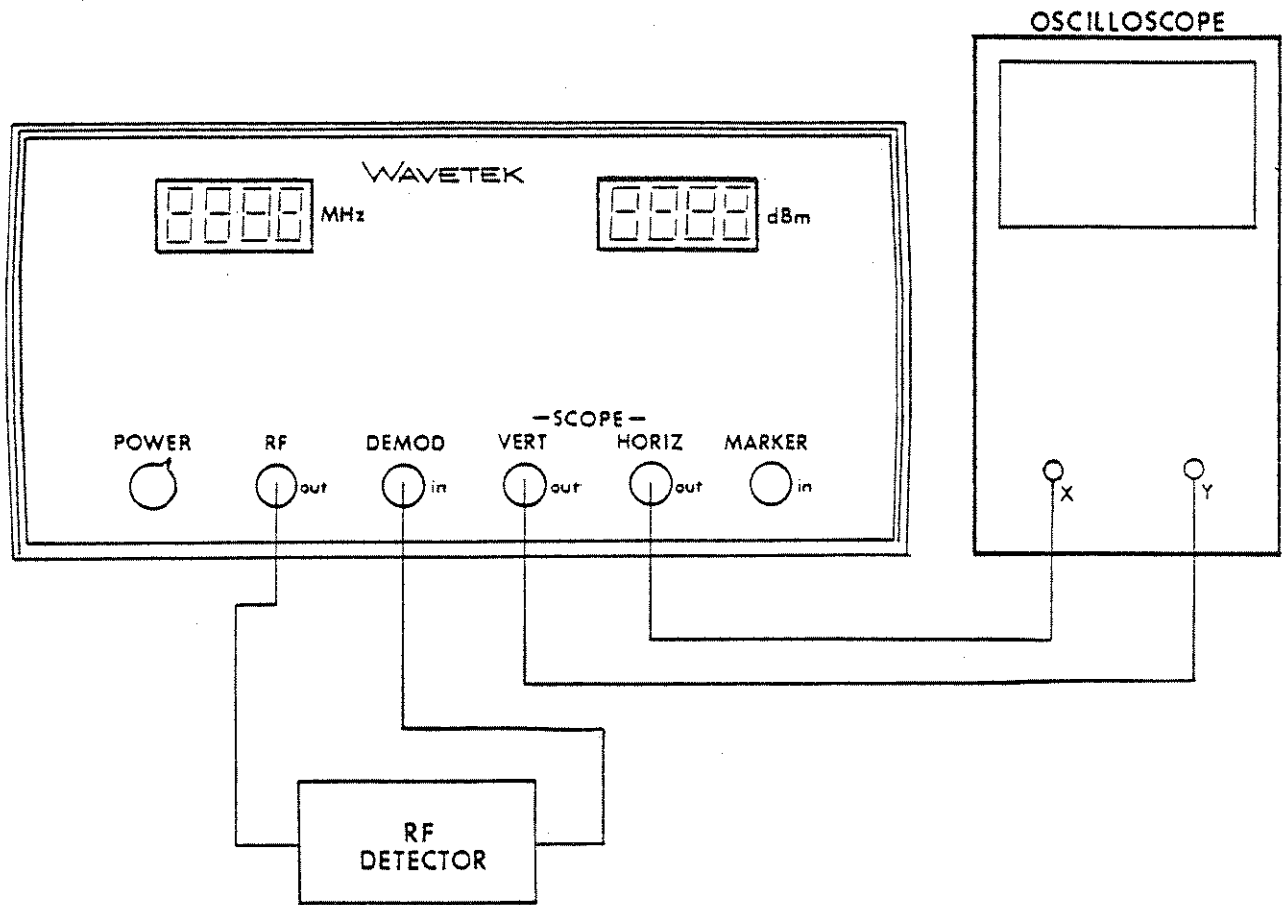


Figure 4-1. Test Set-up

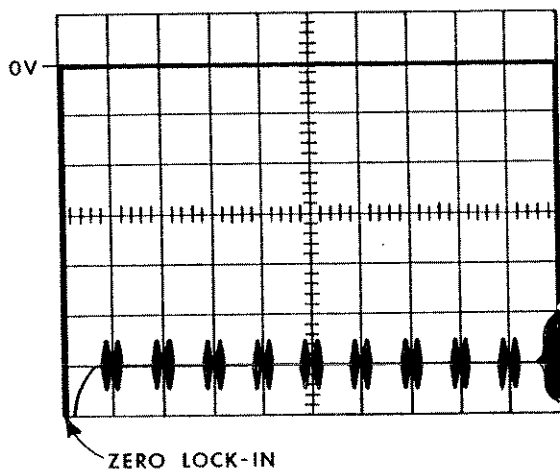


Figure 4-2. Frequency Range (Low End)

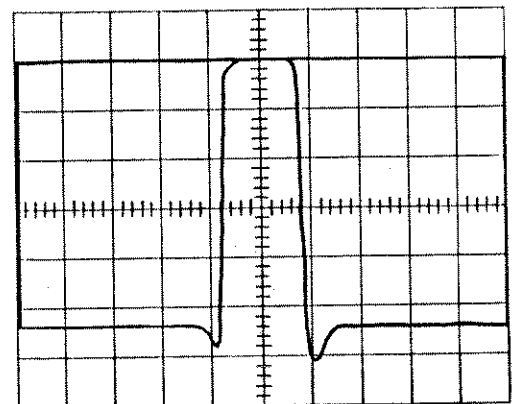


Figure 4-3. Zero Lock-in

Set the SWEEP WIDTH VERNIER fully cw. The 890 MHz marker must be visible, while the 910 MHz marker must not.

Repeat the above procedure for SWEEP WIDTH STEP control settings of 800, 700, 600, 500, 400, 300, 200, and 100 MHz. The sweep width accuracy of ± 10 MHz is verified by noting the visible markers as listed below.

SWEEP WIDTH STEP SETTING (MHz)	MARKER TO BE VISIBLE (MHz)	MARKER TO BE NOT VISIBLE (MHz)
800	790	810
700	690	710
600	590	610
500	490	510
400	390	410
300	290	310
200	190	210
100	90	110

Turn on the 1 MHz markers and set the MARKERS WIDTH to medium. Adjust the FREQUENCY and SWEEP WIDTH controls for a sweep width of exactly 1 MHz at any frequency between 1 and 1000 MHz. Fine adjust the FREQ OFFSET to center a 1 MHz marker on the display. The marker width on the display should be approximately 200 kHz (see Figure 4-9).

Next adjust the SWEEP WIDTH vernier toward minimum. The 200 kHz wide marker should expand to fill the display. This procedure verifies minimum sweep width specifications.

4.5 FREQUENCY DRIFT TEST

Frequency drift may be measured at any desired frequency in the 1 to 1000 MHz range. Use the setup and control settings in Section 4.1.1, except calibrate the display's sweep width to exactly 1 MHz (use the instrument's 1 MHz harmonic markers) and center the test marker on the display with the SWEEP RATE set for LINE. Read the frequency drift directly from the scope display by noting the change in the marker's horizontal position with time. Each major division represents 100 kHz.

4.6 SIGNAL PURITY TEST

4.6.1 HARMONICS AND NON-HARMONICS

Set the FUNCTION for CW operation and turn off all markers. Connect the instrument RF output to the RF input of the spectrum analyzer.

Vary the instrument output frequency between 1 and 1000 MHz and observe the harmonics on the spectrum analyzer. When tuning from 1 to 2 MHz, the harmonics

should be at least 15 dB below the fundamental. When tuning from 2 to 10 MHz, the harmonics should be at least 25 dB below the fundamental. When tuning from 10 to 1000 MHz, the harmonics should be at least 30 dB below the fundamental.

Vary the instrument output frequency between 1 and 1000 MHz and observe the non-harmonics on the spectrum analyzer. When tuning from 1 to 400 MHz, the non-harmonics should be at least 50 dB below the fundamental. When tuning from 1 to 1000 MHz, the non-harmonics should be at least 30 dB below the fundamental.

4.6.2 RESIDUAL FM

An automatic modulation meter is used to measure residual FM. Set the FUNCTION switch for CW, the OUTPUT controls for 0 dBm, and turn off all markers. Connect the instrument RF output to the 50 ohm input of the modulation meter. Set the modulation meter to measure residual FM in the + peak mode. Vary the instrument output frequency from 1 to 1000 MHz and note the maximum modulation meter reading.

Repeat the above procedure with the modulation meter in the - peak mode. The maximum peak residual FM in either + peak or - peak mode should be less than 10 kHz.

4.7 RF OUTPUT TESTS

4.7.1 FLATNESS TEST

Flatness is the variation in RF output level versus frequency. It is usually measured with a power meter, but it can also be measured with a broadband negative-polarity detector in a setup similar to Figure 4-1. Although the detector method takes less time to perform, the power-meter method is preferred since its flatness is better than most detectors. The power meter also registers total power rather than responding to only the negative half-cycle as with the detector.

Use the equipment set-up of Section 4.2.1. (Model 1081 requires a matching pad to match the 50 ohm input of the power meter.) Turn on the 1 MHz harmonic markers and center the 1 MHz marker on the scope display. Set the FUNCTION switch for CW and the OUTPUT controls for +13 dBm or +60 dBmV output.

Calibrate and zero the power meter and sensor and connect the sensor to the instrument RF output. Vary the output frequency from 1 to 1000 MHz and note the maximum and minimum power meter readings. The difference between the two readings divided by 2 is the flatness.

Repeat the above procedure with the OUTPUT controls set for 0.0 dBm or +50 dBmV.

NOTE

If there is a noticeable difference between power meter readings at minimum and maximum frequencies, the rear-panel SLOPE control may require readjustment.

Flatness may be measured at other output levels if desired.

4.7.2 OUTPUT VERNIER TEST

OUTPUT vernier accuracy is measured with a power meter. Model 1080 has two separate vernier ranges, 0 to +13 dB, and -10 to 0 dB. A nominal 1 dB overlap at the end of each range is not calibrated. The higher (0 to +13 dB) range operates only when the OUTPUT step control is set for 0 dB producing the 0 to +13 dBm output. The lower range operates at all other settings of the OUTPUT step control. Model 1081 has only a 0 to -10 dB range. To minimize the effect of the Step Attenuator, the OUTPUT vernier accuracy will be measured at the highest output range.

Set the FUNCTION switch for CW, the FREQUENCY control for 50 MHz, and the OUTPUT controls for +13 dBm or +60 dBmV on the display. Calibrate and zero the power meter and sensor and connect the power meter to the instrument RF OUT connector. (Use a matching pad on Model 1081.) The power meter should read $+13 \pm 0.5$ dBm on Model 1080, or the equivalent of +60 dBmV minus matching pad loss on Model 1081.

Adjust the OUTPUT vernier display readings in 1.0 dB steps (read on the OUTPUT display) and read the power meter at each step. The power meter should read within 0.5 dB of the OUTPUT display reading at each 1.0 dB step. For Model 1080, repeat the above procedure on the 0 to -10 dBm range. The ± 0.5 dB accuracy applies at each dB step. (This step is not necessary on Model 1081.)

NOTE

If the measurement error on the low range exceeds ± 0.5 dB, it may be due to Step Attenuator error. The Step Attenuator test (Section 4.7.3) should be performed, and the combined OUTPUT vernier and Step Attenuator accuracies checked against the specifications in Section 1.2.2.

4.7.3 STEP ATTENUATOR TEST

The accuracy of the Step Attenuator can be measured by using a suitable attenuation test set or by directly substituting precision RF attenuator pads for each 10 dB step of the Attenuator. The difference between the two outputs represents the Attenuator error. An RF detector can be used to recover the signal at levels down to approximately -40 dBm. Below this level, an RF amplifier or sensitive

receiver (spectrum analyzer) must be used. This error is that produced by the Step Attenuator alone and does not include the basic flatness or OUTPUT vernier error.

To minimize the effect of the OUTPUT vernier, measurements should be referenced to the 0.0 dBm or +50 dBmV setting of the OUTPUT display (0 dB Step Attenuator setting). When the Step Attenuator is set for 10 dB or more of attenuation, fine adjust the OUTPUT vernier setting such that the least significant digit is a zero on the OUTPUT display.

4.8 SWEEP TESTS

4.8.1 SWEEP TIME (HORIZONTAL OUTPUT) IN ΔF OPERATION

FAST RATE – Connect the SCOPE HORIZ output to the oscilloscope's vertical input. Adjust the oscilloscope controls for an automatic, internally generated and triggered sweep of 1 msec/div, and a vertical sensitivity of 2 V/div. Set the FUNCTION switch for ΔF , SWEEP MODE for RECUR, SWEEP RATE for FAST, and the SWEEP VARIABLE control fully right. Adjust the oscilloscope's position controls and trigger level for a waveform similar to Figure 4-5. The peak voltage of the waveform should be $+10 \pm 0.2$ V. The clipped (minimum) level should be 0 ± 0.2 V.

The sweep time (rise time of the ramp) should be approximately 10 msec. The retrace time (fall time) should also be approximately 10 msec. Set the SWEEP VARIABLE control fully left. The sweep time should be approximately 1 second.

Set the SWEEP MODE for SINGLE, the SWEEP VARIABLE control fully right, and set the oscilloscope to trigger on an external signal. Press the TRIG pushbutton. The oscilloscope should display a single sweep trace similar to Figure 4-5, and then blank.

SLOW RATE – Set the SWEEP MODE for RECUR, the SWEEP RATE to SLOW, and SWEEP VARIABLE control fully right. Set the oscilloscope for internal triggering. The sweep time (rise time of ramp) should be approximately 1 second, but less than the slowest sweep time for fast rate. Set the SWEEP VARIABLE control fully left. Set the oscilloscope horizontal for approximately 1 msec/div so that the rising portion of the ramp appears as a rising horizontal line. The rise time of the ramp should be greater than 100 seconds.

Set the SWEEP MODE for SINGLE, and the SWEEP VARIABLE control fully right. Set the oscilloscope for external triggering. Press the TRIG pushbutton. The scope should display a single sweep trace, and then blank.

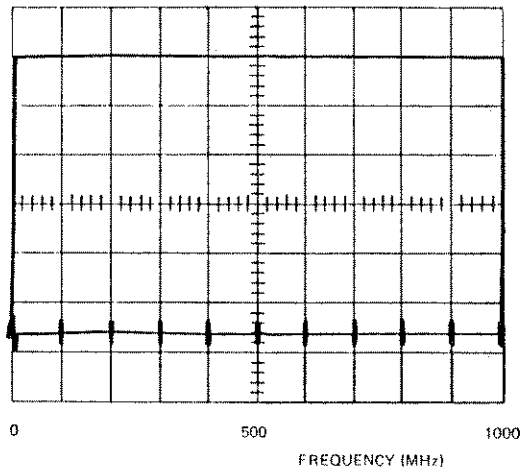


Figure 4.4. Linearity

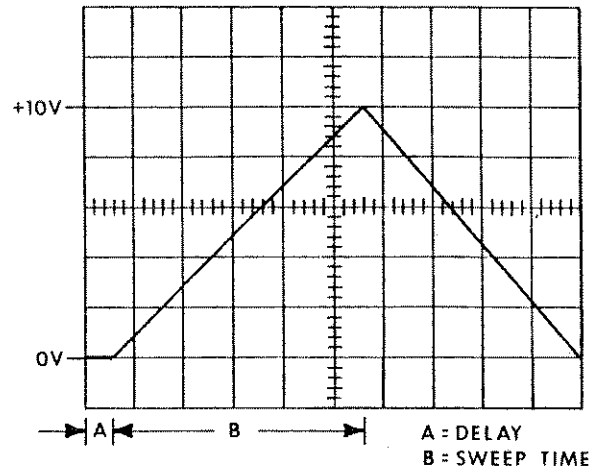


Figure 4.5. Sweep Time (Fast)

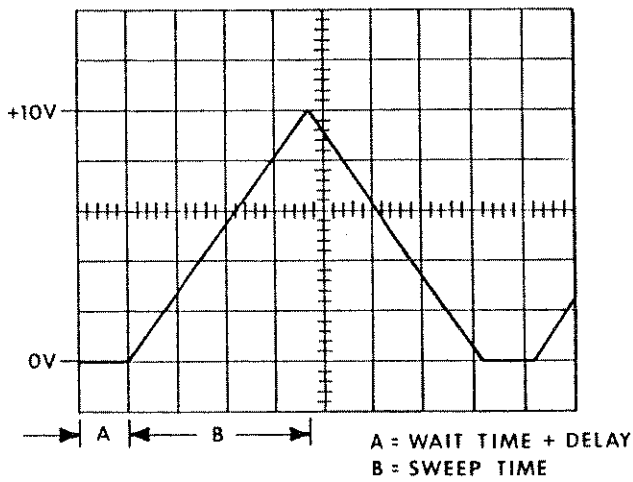


Figure 4.6. Sweep Time (Line)

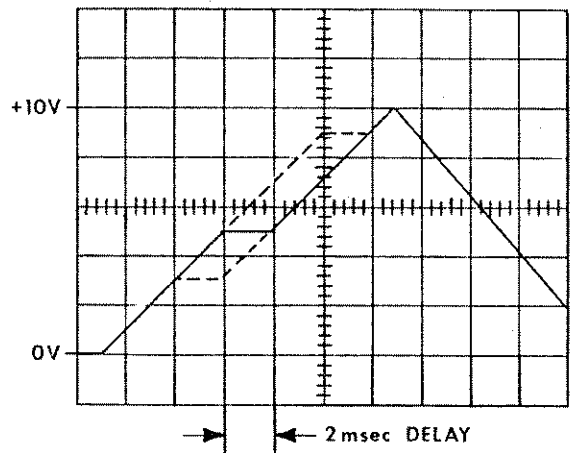


Figure 4-7. Sweep Time (FS)

LINE RATE — Set the **SWEEP MODE** for **RECUR**, the **SWEEP RATE** for **LINE**, and set the oscilloscope to trigger at line rate. The oscilloscope pattern should be similar to Figure 4-6. The sweep time should be approximately 5.3 msec at 60 Hz (7 msec at 50 Hz) line frequency.

NOTE

Single sweep is not possible when the SWEEP RATE is set for LINE.

4.8.2 HORIZONTAL OUTPUT IN FULL SWEEP OPERATION

Use the initial setup and control settings in Section 4.8.1 except set the **FUNCTION** switch to **FS** and the **FREQUENCY** control to 000 MHz. Increase the output frequency from 0 to 1000 MHz, and observe how the 2 msec delay for the **FS** intensity marker rises on the positive ramp from 0 to 10 volts (see Figure 4-7).

4.9 MARKER TESTS

4.9.1 MARKER ACCURACY TEST

All birdy markers are derived from a 10 MHz crystal oscilloscope whose accuracy is 0.005%. It is usually unnecessary to measure marker accuracy because of the high stability of this crystal oscillator.

Connect the equipment as shown in Figure 4-8 and set the instrument controls as follows:

FUNCTION	ΔF
FREQUENCY	500 MHz
SWEEP WIDTH step	0 MHz
SWEEP WIDTH vernier	for 1 MHz
OUTPUT	+13 dBm or +60 dBmV
SWEEP MODE	RECUR
SWEEP RATE	LINE
SWEEP VARIABLE	N/A
MARKERS FREQUENCY	100/10/1 MHz
MARKERS WIDTH	medium
MARKERS SIZE	mid-range

Center the 500 MHz marker and adjust the frequency of the external CW generator until the external marker coincides with the 500 MHz marker. The frequency counter should read 500.000 \pm .025 MHz.

4.9.2 MARKER AMPLITUDE AND WIDTH TEST

Connect the equipment as shown in Figure 4-1. Set the instrument controls as in Section 4.9.1, except set the **FREQUENCY** control for 900 MHz and the **MARKERS SIZE** fully right.

Set the oscilloscope for X-Y operation and set the vertical sensitivity to 0.2 V/div. Adjust the **FREQUENCY** and **SWEEP WIDTH** controls and the oscilloscope position controls to align the 900 MHz marker with the extreme right graticule line and the 890 MHz marker with the extreme left graticule line (see Figure 4-9). The amplitude of the 900 MHz marker should be approximately 1.5 Vpp. Reduce the **MARKERS SIZE** control setting until the 900 MHz marker amplitude is 1.2 Vpp. The amplitude of the 890 MHz marker should be approximately 0.8 Vpp. The amplitude of the 891 through 899 MHz markers should be approximately 0.4 Vpp.

Set the **MARKERS SIZE** control fully left. The 900 MHz marker amplitude should be \leq 1 mVpp.

4.9.3 MARKER WIDTH TEST

Reduce the **SWEEP WIDTH** to 1 MHz and set the **MARKER WIDTH** to **WIDE**. Center the 891 MHz marker on the display. The marker width (measured at the half-amplitude points) should be approximately 400 kHz as shown in Figure 4-10. Reduce the **MARKERS WIDTH** in steps from **WIDE** to **NARROW**. The displayed marker width should decrease to approximately 200 kHz in medium and approximately 100 kHz in **NARROW**. Center the 890 MHz marker on the display and repeat the above procedure. Center the 900 MHz marker on the display and repeat the above procedure. The width of the 890 and 900 MHz markers should be approximately twice that of the 891 MHz marker.

4.9.3 EXTERNAL MARKER TEST

Use the equipment setup and control settings in Section 4.3 except turn off all instrument markers. Connect the CW output of a signal generator operating at 0.1 V level (-7 dBm) to the instrument **MARKER IN** connector.

Tune the signal generator from 1 to 1000 MHz and note the movement of the external birdy marker on the display. The marker should be clearly visible throughout the range. Harmonics of the external CW signal may also be visible on the display.

Turn on the 100/10/1 MHz markers and repeat the above procedure. The external marker's amplitude should be approximately equal to the 10 MHz harmonic marker's amplitude; but, within approximately 5 MHz of any 100 MHz harmonic marker, the external marker amplitude should be approximately equal to 100 MHz harmonic marker's amplitude.

4.10 EXTERNAL PROGRAMMING

Rear-panel outputs and external programming inputs are not normally checked at incoming inspection unless these special functions are to be used in a particular application. The program input signals, necessary external controls, and input pin connections are covered in Section 2.

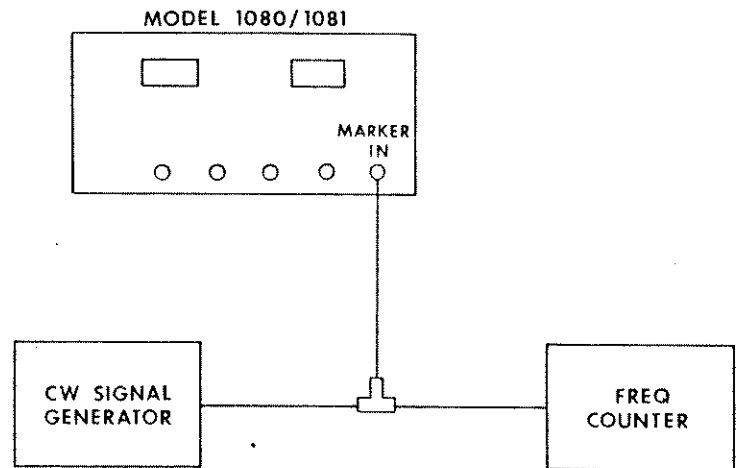


Figure 4-8. Marker Accuracy Set-up

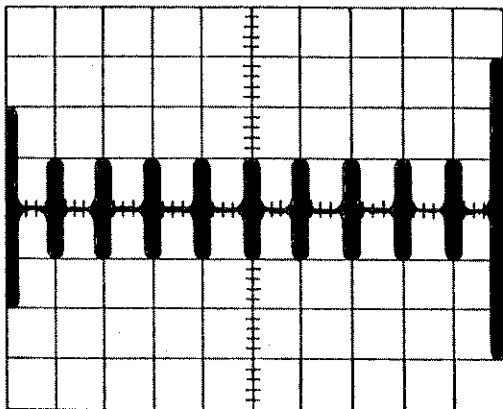


Figure 4-9. Marker Amplitude

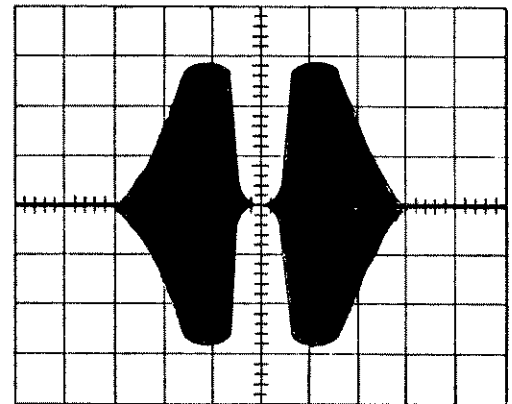


Figure 4-10. Marker Width

SECTION 5

MAINTENANCE

5.1 INTRODUCTION

This section provides information for servicing, calibrating, and troubleshooting the Wavetek Model 1080/1081 Sweep/Signal Generators.

5.2 SERVICE INFORMATION

NOTE

The DIP ribbon jumpers and coaxial RF cables (except W4) are long enough to allow separation of the instrument's internal assemblies for testing and servicing. If the RF assembly is removed, W4 should be replaced with a longer, flexible coaxial cable. The DIP ribbon jumpers and coaxial RF cables may be disconnected if required, but must be reconnected as shown in Figure 5-10.

TOP COVER REMOVAL

Disconnect the line cord from the AC mains. Turn the instrument upside down and remove the four screws in the bottom of the unit. Hold both top and bottom covers and turn the unit right side up. Lift off the top cover. Internal assemblies are shown in Figure 5-1. Calibration can be performed without further disassembly.

BOTTOM COVER REMOVAL

Remove the two screws in the front corners of the Power Supply/Marker (bottom) Board which secure the board to the bottom cover. Holding both front and rear panels, lift the unit from the bottom cover and place the unit in an upright position.

FRONT-PANEL AND MAIN BOARD ASSEMBLY REMOVAL

Remove the two screws from the rear edge of the Main (top) Board which secure it to the rear panel. Pry off the knob caps and unscrew and remove the knobs from the front-panel POWER switch and OUTPUT STEP control. Lift the Front-Panel and Main Board assemblies forward to clear the POWER switch and Step Attenuator shafts.

The Main Board can be disconnected from the Front-Panel assembly by removing the screw between the two 36-pin connector sections which form J101 and unplugging J101 from the Front-Panel assembly.

RF ASSEMBLY REMOVAL

Remove the four screws securing the RF assembly as shown in Figure 5-10. The RF assembly covers can be removed for access to the RF circuits.

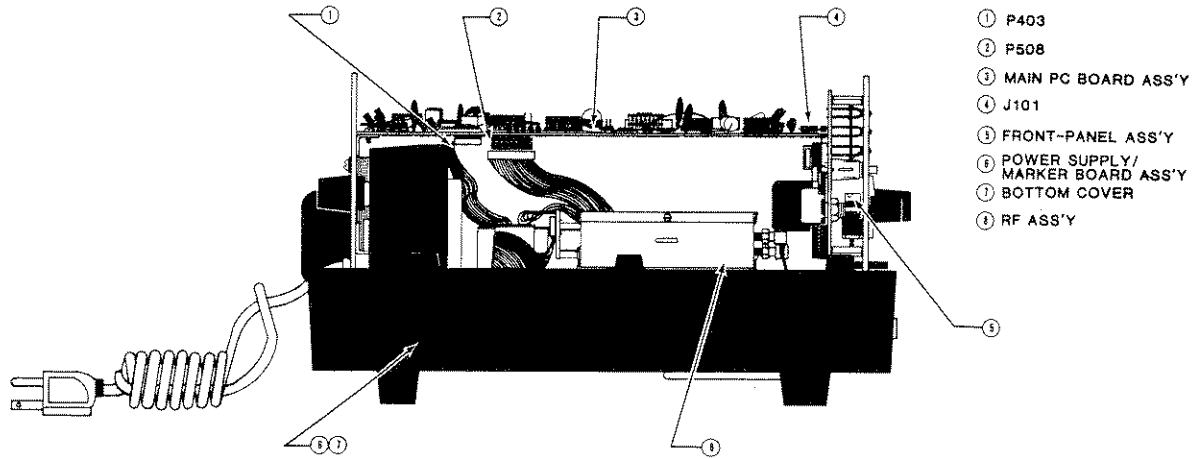


Figure 5-1. Side View

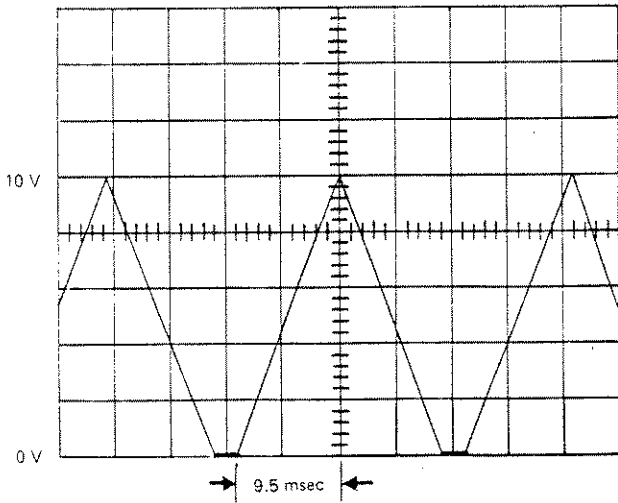


Figure 5-2. Sweep Rate

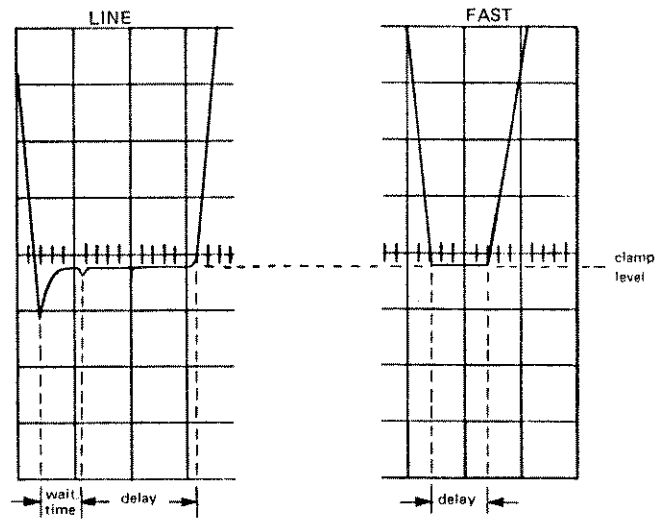


Figure 5-3. Wait Time/Delay/Clamp Level

5.3 CALIBRATION PROCEDURE

NOTE

Refer to Figure 5-10 for control and test point location. Test points and controls are designated by the following symbols which identify the circuit to be calibrated.

- ◆ → Power Supply
- ♣ → Sweep Rate
- ▲ → Frequency
- → Output Level
- ★ → Markers

Remove the instrument top cover per Section 5.2. This permits access to test points and adjustments on the Main Board assembly. With power applied to the instrument, allow a 30-minute warmup. For recommended test equipment, refer to Table 4-1. Calibration should be performed in the sequence given.

5.3.1 POWER SUPPLY

CALIBRATE +10 V SUPPLY

Connect the DVM volts input to TP ◆A and the common to TP ◆a. Adjust the +10 V control ◆1 to produce a DVM reading of +10.00 V.

CHECK -10 V SUPPLY

Connect the DVM volts input to TP ◆B and the common to TP ◆b. The DVM should read -10 ± 0.01 V.

CHECK +18 V SUPPLY

Connect the DVM volts input to TP ◆C and the common to TP ◆c. The DVM should read $+18 \pm 0.15$ V.

CHECK -18 V SUPPLY

Connect the DVM volts input to TP ◆D and the common to TP ◆d. The DVM should read -18 ± 0.15 V.

CHECK +5 V SUPPLY

Connect the DVM volts input to TP ◆E and the common to TP ◆e. The DVM should read $+5 \pm 0.25$ V.

With nominal AC line input, the unregulated input to the ± 18 volt regulator is typically ± 25 V with 1 Vpp ripple. The unregulated input to the 5 V regulator is 8.5 V with 1 Vpp ripple. Regulator ripple is typically < 1 mV on the ± 10 and ± 18 V supplies, and < 3 mV on the 5 V supply (in CW mode).

5.3.2 SWEEP RATE (PART 1)

Set Auto Zero jumper jacks A and C to calibrate position, and jumper jack B to operate position (see Figure 5-10a).

RAMP SIZE

Connect the front-panel SCOPE HORIZ output to the oscilloscope horizontal input.

Set the instrument front-panel controls as follows:

FUNCTION	Δf
FREQUENCY	500 MHz
SWEEP WIDTH step	100 MHz
SWEEP WIDTH vernier	full ccw
SWEEP VARIABLE	full right
SWEEP RATE	FAST
SWEEP MODE	RECUR

Set the oscilloscope vertical sensitivity to 2 V/div (DC coupled), and position the trace (0 V input) on the bottom graticule line. Set the oscilloscope horizontal time base to 5 msec/div (internally triggered). Connect the SCOPE VERT output to TP ♣A, and the common to TP ♣B. The display should be similar to Figure 5-2.

Use the Size control ♣1 to adjust the 10 V peak level. Required accuracy is ± 0.2 V at the 0 and 10 V levels.

FREQUENCY

Use the Frequency control ♣2 to set the rise time of the ramp to ~ 9.8 msec (see Figure 5-2).

LINE

Set the FUNCTION switch to FS and the SWEEP RATE to LINE. Set the oscilloscope vertical sensitivity to 0.2 V/div and the oscilloscope horizontal time base to 2 msec/div. Use the oscilloscope's vertical position control to align a 0 V trace on the center graticule on the display. Preset the Wait control ♣4 fully cw. Set the front-panel SWEEP RATE switch for LINE, and adjust the oscilloscope sync for negative triggering. The oscilloscope display should be similar to Figure 5-3. Adjust the Clamp control ♣3 to vertically position the clamp level to the identical level as when set for FAST rate. Check the level while alternating between LINE and FAST rates to ensure proper adjustment. Adjust the Wait control ♣4 until wait time = 1 msec.

NOTE

Wait time will be ~ 3 msec in Δf mode.

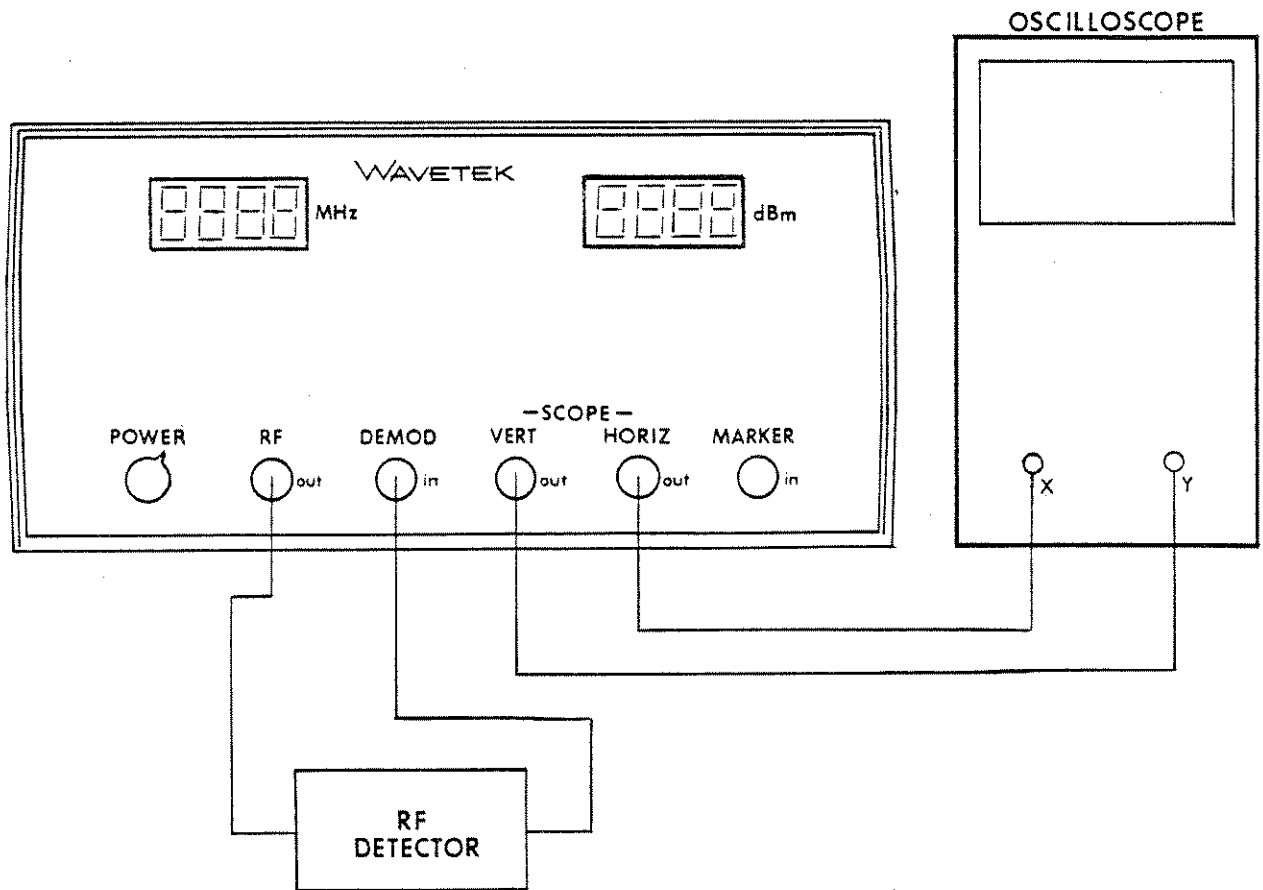


Figure 5-4. Test Set-up

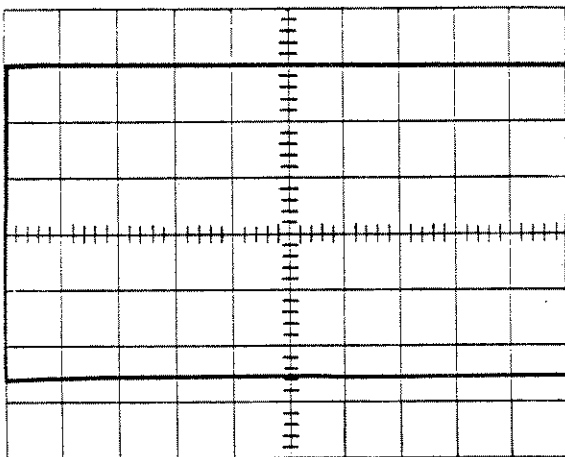


Figure 5-5. Typical Detected Output

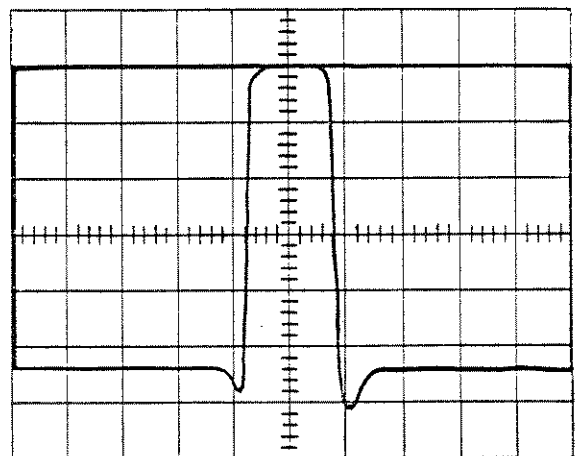


Figure 5-6. Zero Lock-in Point

5.3.3 FREQUENCY

FRONT-PANEL FREQUENCY DISPLAY

Set the front-panel FREQUENCY control fully ccw. The FREQUENCY display must read 000 or 001 MHz. Connect the DVM volts input to TP ▲A and the common to TP ▲B. Adjust the FREQUENCY control until the DVM reads +10.000 V. Adjust the Cal control ▲1 until the FREQUENCY display reads 1000 MHz.

REF ADJUST

Set the front-panel FREQ OFFSET control to mid-range and the FUNCTION switch for CW. Connect the DVM volts input to TP ▲C, and the common to TP ▲B. Set the FREQUENCY control to read 910 MHz on the FREQUENCY display. Adjust the Ref control ▲2 until the DVM reads -0.1 ± 0.05 V.

OUTPUT FREQUENCY

Connect the equipment as shown in Figure 5-4. Set the instrument controls as follows:

FUNCTION	Δf
SWEEP WIDTH step	0 MHz
OUTPUT	+13 dBm
FREQ OFFSET	mid-range
SWEEP RATE	LINE
SWEEP MODE	RECUR
MARKERS FREQUENCY	100/10 MHz
MARKERS SIZE	full left

Set the oscilloscope for X-Y mode and adjust the vertical and horizontal controls to produce a scope pattern similar to Figure 5-5.

Set the MARKERS WIDTH and SIZE switches to produce an easily read display. Adjust the SWEEP WIDTH vernier to produce a display approximately 10 MHz wide. Turn on the 100 MHz harmonic markers.

Set the front-panel FREQUENCY control to read 000 MHz on the display. Adjust ▲3 until the zero lock-in point is centered on the scope display as shown in Figure 5-6. Move Auto Zero jumper jack B to the calibrate (speed up) position, and jumper jacks A and C to the operate position. If the zero lock-in point shifts slightly, move jumper jack A back to the calibrate position and readjust ▲3 until any shift caused by changing jumper jack A from the calibrate to operate position is negligible. Leave jumper jack A in the operate position.

Set the FREQUENCY control to read 100 MHz on the display. Adjust control ▲4 to center the 100 MHz marker on the oscilloscope display.

Adjust the FREQUENCY control in 100 MHz increments and adjust controls ▲5 through ▲13 in the same manner as control ▲4. The final adjustment (control ▲13) will center the 1000 MHz marker on the oscilloscope display.

5.3.4 SWEEP RATE (PART 2)

Connect the equipment as shown in Figure 5-4. Set the instrument controls as follows:

FUNCTION	FS
FREQUENCY	any
SWEEP WIDTH	N/A
OUTPUT	+13 dBm
SWEEP RATE	LINE
SWEEP MODE	RECUR
MARKERS FREQUENCY	100 MHz

Adjust MARKERS SIZE and WIDTH to produce an easily readable display. Adjust the Bal control ▲5 until the zero lock-in point is just visible at extreme left edge of the display, and adjust the Size control ▲6 until the 1000 MHz marker is just visible at the extreme right edge of the display.

Set the FUNCTION switch for Δf and adjust the FREQUENCY control for 500 MHz. Set the SWEEP WIDTH controls for maximum sweep width and adjust the Bal control ▲7 to align the 100 MHz harmonic markers with the graticule lines on the display, as shown in Figure 5-7.

5.3.5 OUTPUT LEVEL

SLOPE

Set the rear-panel SLOPE control to mid-range and adjust the Slope control on the Main Board (R449) to produce a flat oscilloscope display trace.

OUTPUT DISPLAY (MODEL 1080)

Set the front-panel OUTPUT controls for maximum output. Connect the DVM volts input to TP ●A and the common to TP ●B. The DVM should read -13 ± 0.02 V. Adjust the Cal control ●1 such that the OUTPUT display reads +13.0 dBm. Set the OUTPUT step control fully ccw (70 dB of attenuation). Adjust the Cal control ●9 such that the OUTPUT display reads -60 dBm. Verify that each step of the OUTPUT step control changes the OUTPUT display reading by 10.0 dB except for the most cw step, which should change the display reading by 13.0 dB.

Figure 5-7. Sweep Linearity

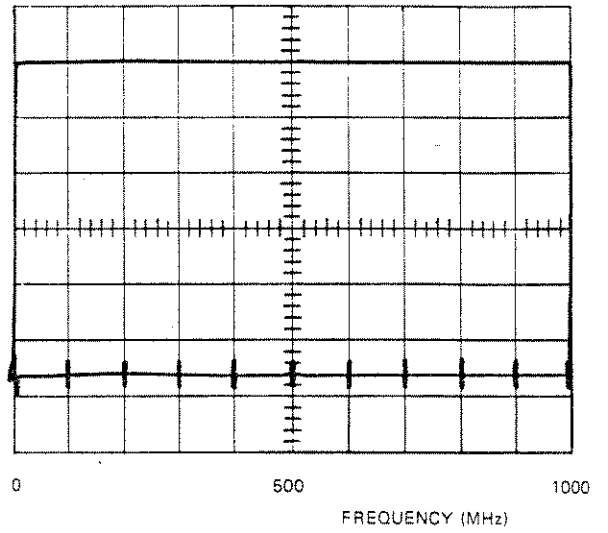


Figure 5-8. Marker Amplitude

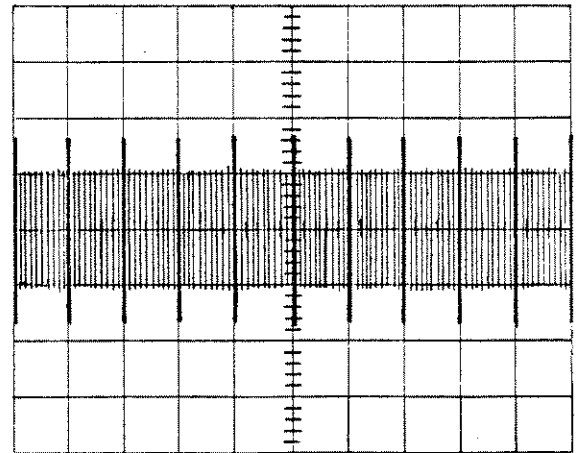
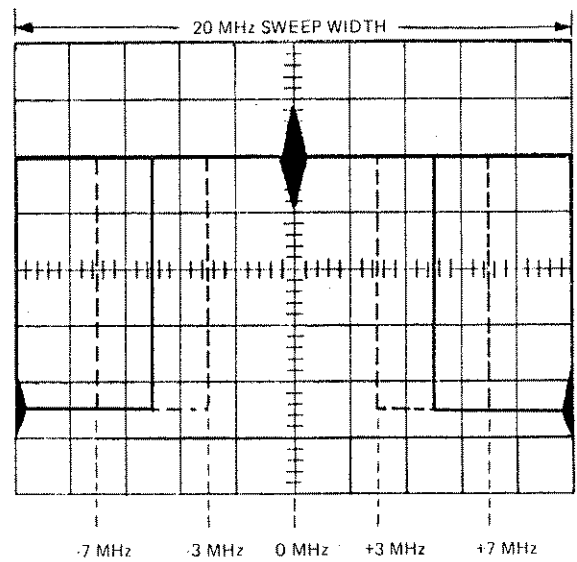


Figure 5-9. Gate Pulse



OUTPUT DISPLAY (MODEL 1081)

Set the OUTPUT vernier fully cw and the step control two steps cw from its fully ccw position (50 dB of attenuation). Connect the DVM volts input to TP ●A and the common to TP ●B. The DVM should read -10 ± 0.02 V. Adjust the Cal control ●1 such that the OUTPUT display reads +10.0 dBmV. Set the OUTPUT step control fully ccw and adjust Cal control ●9 such that the OUTPUT display reads -10.0 dBmV. Set the OUTPUT step control fully cw and adjust the Cal control ●10 such that the OUTPUT display reads +60.0 dBmV. Verify that each step of the OUTPUT step control changes the OUTPUT display reading by 10.0 dB.

REF ADJ

Set the FUNCTION switch for CW, the FREQUENCY control for 50 MHz, and the OUTPUT controls for +10.5 dBm (Model 1080) or +57.5 dBmV (Model 1081). Connect the DVM volts input to TP ●C (Model 1080) or the cathode of CR221 (Model 1081), and the common to TP ●B. Adjust the Ref control ●2 such that the DVM reads 0.1 V.

OUTPUT LEVEL

Set the OUTPUT controls for maximum output. Connect a power meter to the front-panel RF output. (Ensure the power meter is properly warmed up, calibrated, and zeroed. Adjust power meter ranges as necessary.) Adjust the OUTPUT vernier until the OUTPUT display reads 00.0 dBm (Model 1080) or +50 dBmV (Model 1081). Adjust control ●3 such that the power meter reading matches the OUTPUT display reading. Repeat the above procedure for the following output levels.

MODEL 1080 OUTPUT LEVEL (dBm)	MODEL 1081 OUTPUT LEVEL (dBmV)	CONTROL
+1.0	+51.0	●4
+4.0	+54.0	●5
+7.0	+57.0	●6
+10.0	+60.0	●7
+13.0	---	●8

5.3.6 MARKER CALIBRATION

Connect the equipment as shown in Figure 5-4, except disconnect the RF detector from the DEMOD IN connector. Set the oscilloscope for X-Y mode with vertical and horizontal sensitivities of 1 V/div.

Set the instrument controls as follows:

FUNCTION	Δf
FREQUENCY	500 MHz

SWEEP WIDTH step	900 MHz
SWEEP WIDTH vernier	full cw
OUTPUT	any
SWEEP RATE	FAST
SWEEP VARIABLE	for slight flicker on scope
MARKERS FREQUENCY	10 MHz
MARKERS WIDTH	WIDE
MARKERS SIZE	full right

10 MHz ADJUSTMENT

Adjust the 10 MHz saturation control ★1 until all 10 MHz markers are of constant amplitude. This is accomplished by first adjusting the control clockwise until amplitude differences are visible, then ccw just enough to produce a constant amplitude across the entire band (see Figure 5-8). Adjusting the control further ccw will cause over-saturation, a condition which can produce a distorted, overly wide marker when viewed at a narrow sweep width, and also spurious markers at incorrect frequencies. When properly set, the 10 MHz marker amplitude should be ~ 100 mVpp at the wiper and ~ 200 mVpp at the top of control ★1.

1 MHz ADJUSTMENT

Turn on the 1 MHz harmonic MARKERS. Reduce the sweep width to ~ 100 MHz. Adjust the 1 MHz saturation control ★2 in the same manner as the 10 MHz adjustment above. Tune the front-panel FREQUENCY control throughout the entire 0 to 1000 MHz range to ensure the 1 MHz markers are saturated at all frequencies. When properly set, the 1 MHz marker amplitude should be ~ 200 mVpp at the wiper and ~ 400 mVpp at the top of control ★2.

100 MHz ADJUSTMENT

Connect the front-panel DEMOD IN input to TP ★C and the common to TP ★B.

Set the instrument controls as follows:

FUNCTION	Δf
FREQUENCY	1000 MHz
SWEEP WIDTH step	0 MHz
SWEEP WIDTH vernier	20 MHz
SWEEP RATE	LINE
MARKERS FREQUENCY	10 MHz
MARKERS WIDTH	WIDE
MARKERS SIZE	full right

Center the 1000 MHz marker on scope display with the FREQUENCY control. Adjust the 1000 MHz comparator ★3 to produce a gate pulse 10 MHz wide. The ideal gate pulse and maximum limits (dashed) are shown in Figure 5-9. Check each 100 MHz point (1000 to 0 MHz) to ensure each gate pulse meets specifications. Reset the 1000 MHz

comparator if necessary. (Centering of the pulses to their 100 MHz points is accomplished by varying the length of the delay line, and should not require readjustment.)

VARIABLE MARKER AT 10 MHz

Connect the equipment as shown in Figure 5-4. Set the instrument controls as follows:

FUNCTION	FS
FREQUENCY	10 MHz
FREQUENCY OFFSET	mid-range
SWEEP WIDTH	N/A
OUTPUT	+13 dBm
SWEEP RATE	LINE
SWEEP MODE	RECUR
MARKERS FREQUENCY	100/10 MHz
MARKERS WIDTH	WIDE

Adjust the 10 MHz FS control ★4 on the Main Board until the variable marker (bright spot) or "pinball" coincides with the 10 MHz birdy marker.

VARIABLE MARKER AT 900 MHz

Use the previous setup except set the instrument controls as follows:

FREQUENCY	900 MHz
MARKERS FREQUENCY	100 MHz

Adjust the 900 MHz FS control ★5 until the variable marker coincides with the 900 MHz birdy marker.

Return all Auto Zero jacks to operate position. Calibration is complete.

5.4 TROUBLESHOOTING

Effective troubleshooting requires a thorough understanding of the block diagram and circuit descriptions located in Section 3 of this manual. The performance test and calibration procedures will then aid in localizing the trouble symptom to a particular assembly. Once this has been accomplished, the assembly can be replaced or repaired with the aid of the proper schematic and parts layout diagram. In general, it is preferable to replace a defective assembly.

Equipment troubles are frequently due simply to improper control settings; therefore, before engaging in a troubleshooting procedure, be sure front- and rear-panel controls are set in proper operating position. Refer to Section 2 of this manual for complete explanation of each control's function along with typical operating instructions.

After verifying that the trouble is not improper setting of the controls or test set-up, make a thorough visual inspection

of the instrument for such obvious defects as loose or missing screws, broken wires, loose RF cables, and burned or broken components.

After localizing the problem, voltage and resistance checks will help find any defective components. If substitute assemblies are available, this provides an easy method of verifying if a suspected assembly is defective.

A problem in a power supply may cause many symptoms pointing to other areas, and should be checked when the symptom does not indicate a specific problem. Performance of the power supplies is indicated in the calibration procedure.

NOTE

For troubleshooting purposes, it is permissible to operate the instrument in a semi-disassembled state. Refer to Section 5.2 for service and disassembly information.

5.4.1 TROUBLESHOOTING HINTS

Following is a list of several typical symptoms, accompanied by the possible cause(s) or a troubleshooting procedure. It is assumed the instrument has been properly calibrated previously, and that a warmup period will precede troubleshooting.

INTERMITTENT OPERATION

Check for loose RF cables or DIP ribbon jumpers. If none, check for broken feed-thru capacitors on the RF assembly.

FREQUENCY UNSTABLE (JITTER)

Check for loose screws causing bad ground, especially on the RF assembly. Check for excessive ripple on power supplies.

Operating the instrument in a strong magnetic field, such as sitting on top of, or adjacent to, another instrument containing a large power transformer can produce line-rate hum modulation.

NO RF OUTPUT

Defective Step Attenuator or cable between the RF Assembly and the Step Attenuator, or between the Step Attenuator and the front panel. Check also for a broken feed-thru capacitor on the RF Assembly.

RF OUTPUT NOT FLAT

The most common cause is the external RF detector being defective. Another cause is a defective monitor diode (CR504 on the Sweep Oscillator Board). This is a point contact diode and can be damaged if the RF output is

momentarily connected to a B+ voltage. A good monitor diode will produce a negative detected voltage (at Sweep Oscillator feed-thru capacitor A) approximately twice the amplitude of the external detector. For example, at an RF output of +10 dBm, an external RF detector will read approximately 0.8 V. The internal monitor will read approximately -1.6 V.

INCORRECT HORIZONTAL OUTPUT OR SWEEP RATE

The probable cause is a defective component in the sweep rate generator section of the Main Board or defective wiring to the front-panel SWEEP controls.

NO RF SWEEP

Check both Δf and FS modes. If only one mode is inoperative, the problem is probably in the wiring to the front-panel MODE switch or a switching circuit on the Main Board. If both modes are inoperative, check the sweep drive signal at J504 of the RF Assembly. If this signal is correct ($\sim +3$ to $+13$ V in FS mode), the problem is on the Sweep Oscillator Board in the RF Assembly. If the voltage is not correct at J504, check it at J404 of the Main Board. If it is correct at J404, but not at J504, cable W4 is defective or loose. If the sweep drive signal is not correct at J404, the problem is probably a defective component in the sweep drive circuitry on the Main Board.

MARKER PROBLEMS

To isolate the cause of a marker problem when the symptom does not clearly indicate a specific circuit or component, first, turn on the 100/10/1 MHz harmonic markers. The birdy outputs at Marker Generator feed-thru capacitors B and C should be at least .5 Vpp. If they are, the Marker Generator Board is operating properly, and the

trouble is either on the Power Supply/Marker Amp Board or in connecting wiring.

If the birdy outputs are not present, check for the proper power supply voltages at Marker Generator feed-thru capacitors D, E, and F.

If the supply voltages are correct, check the output signal at Marker Generator feed-thru capacitor A. If the instrument is sweeping, a scalloped pattern (~ 15 Vp, one scallop per 100 MHz) should be present. If it is, the sweep sample circuitry is working properly. If the scalloped pattern is not present, check for the sweep sample at IC505 pin 3 on the Sweep Oscillator Board (~ 40 mV detected signal). If the detected sweep sample is correct, but the scalloped pattern is not present, the problem is a defective component in the vicinity of IC602 on the Marker Generator Board. (Be sure shorted line W601 is tightly secured to J601.)

If the scalloped pattern is present at Marker Generator feed-thru capacitor A, but the 10 and 1 MHz harmonic marker birdy outputs at feed-thru capacitors B and C are not present, check the markers disable signal at feed-thru capacitor G. If this signal is less than 0.5 V, check IC603 pin 8 for a 10 MHz signal to verify that the oscillator is working. If the 10 MHz oscillator is working, but the birdy output is not present at feed-thru capacitor C, the problem is a defective component between Q603 and feed-thru capacitor C.

If the 10 MHz harmonic markers work but the 1 MHz harmonic markers do not work, check the 1 MHz marker disable signal at Marker Generator feed-thru capacitor H. If this signal is less than 0.5 V, the problem is a defective component between Q608 and feed-thru capacitor B.

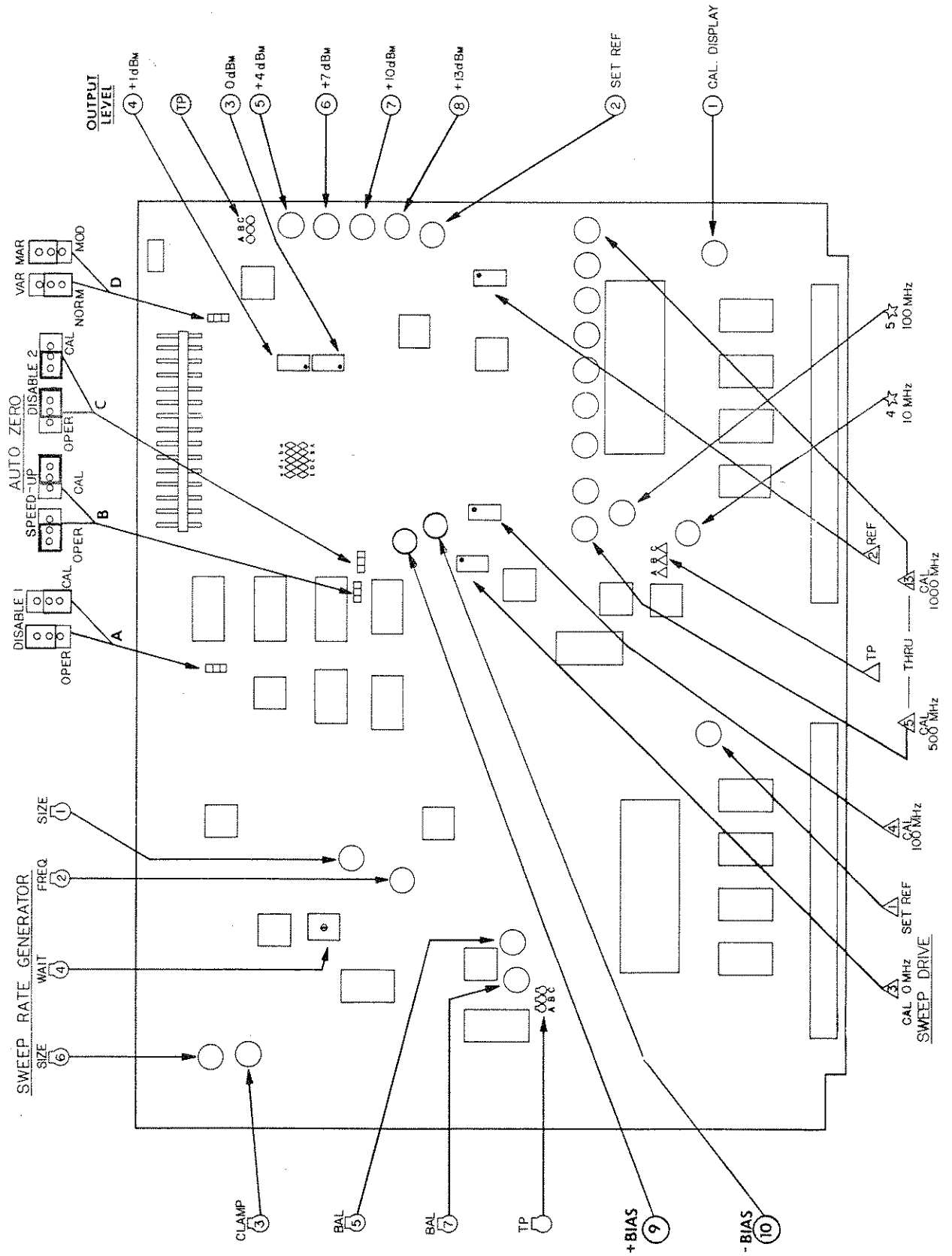


Figure 5-10a. Test Points And Controls (Main Bd.)