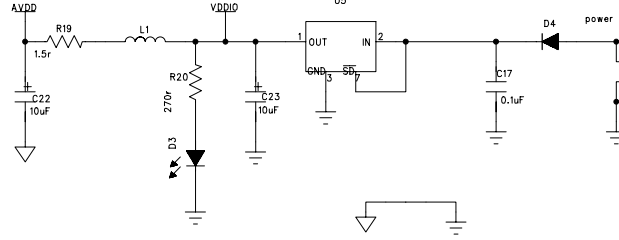
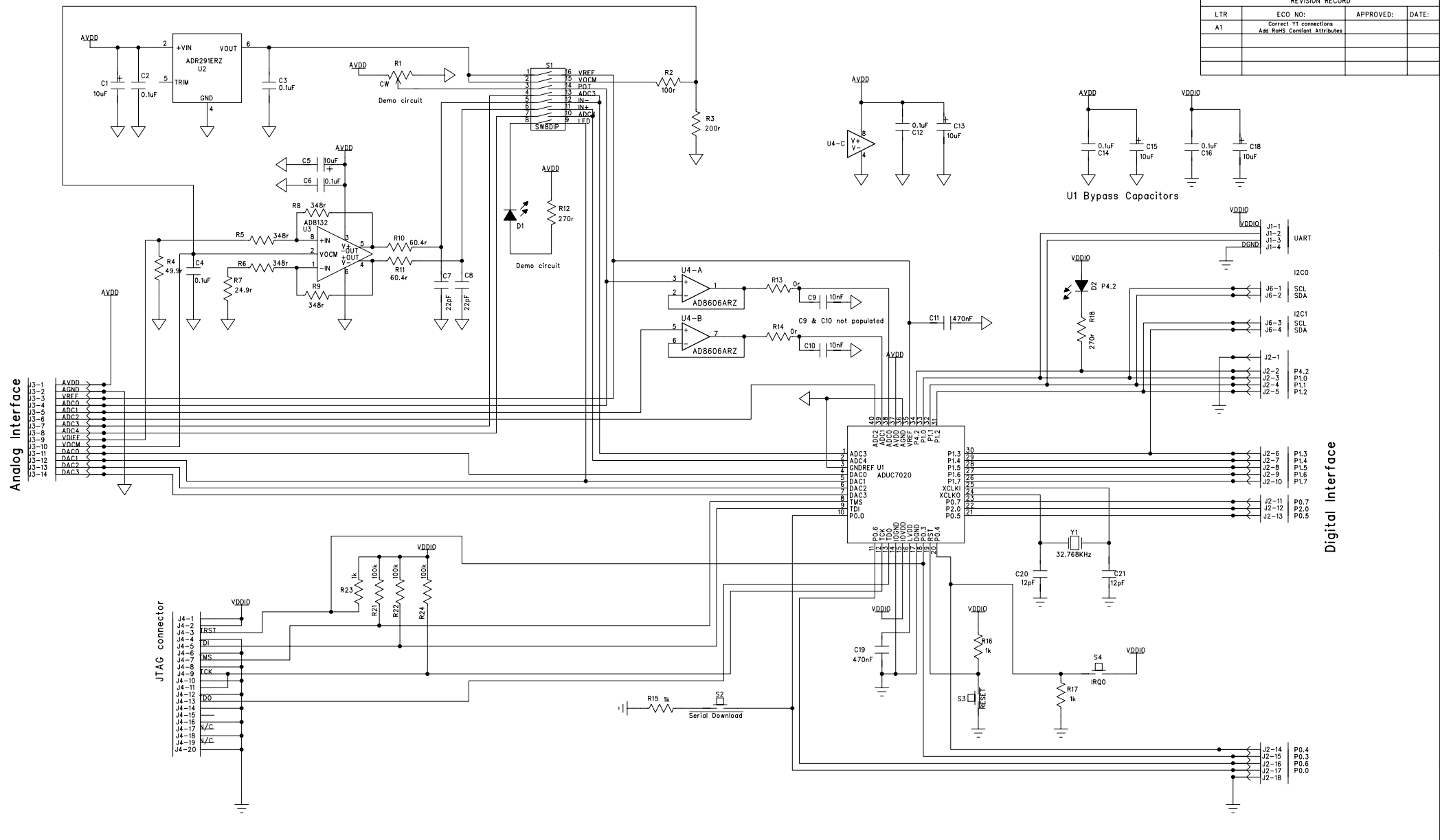
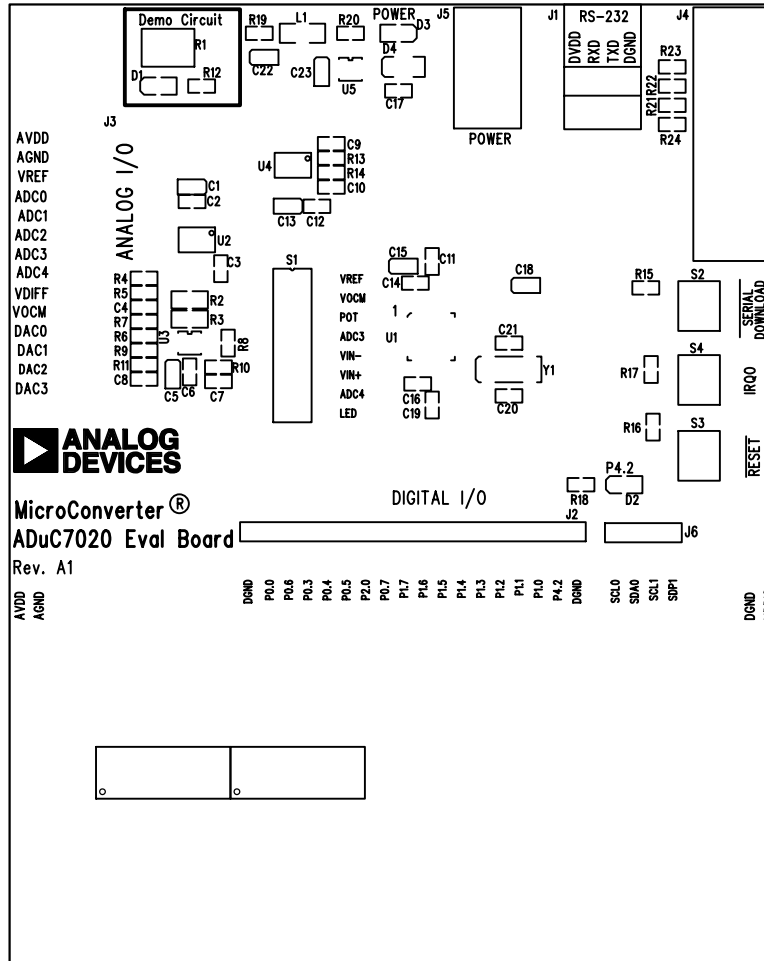


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
A1	Correct T1 connections Add Resistor Constant Attributes		

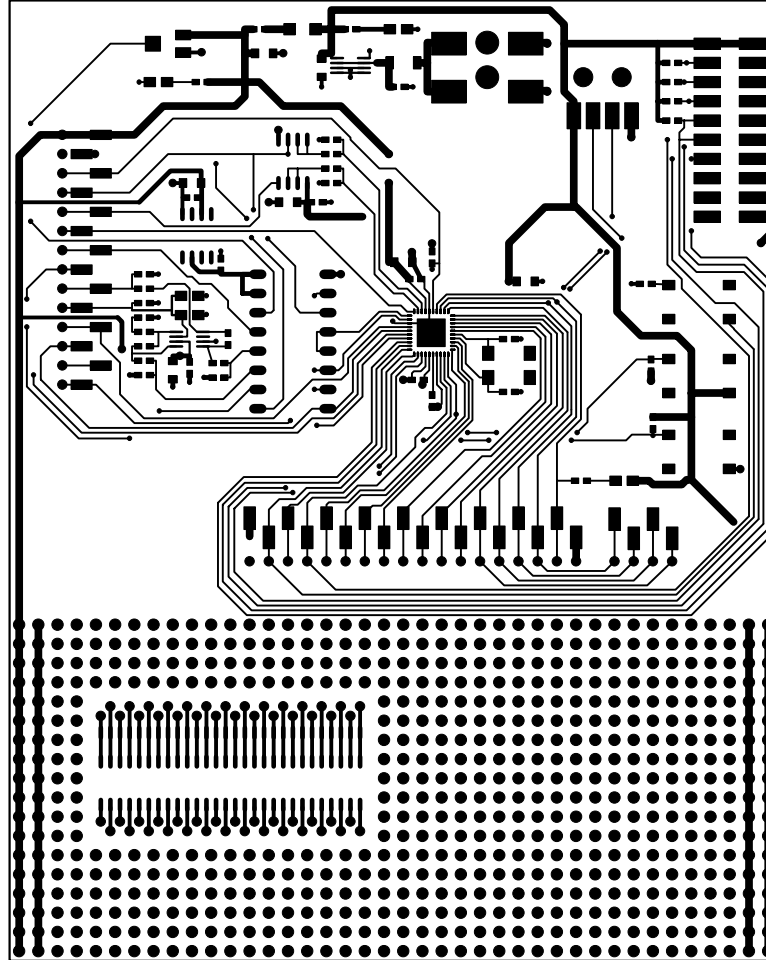


COMPANY:		Analog Devices Inc.			
TITLE:		ADUC7020 Development System			
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
Pat Sheahan	Sept 05			EVAL-ADUC7020QS	A1
CHECKED:	DATED:	SCALE:		SHEET: 1 OF 1	
Aude Richard	Sept 05				
QUALITY CONTROL:	DATED:				
RELEASED:	DATED:				



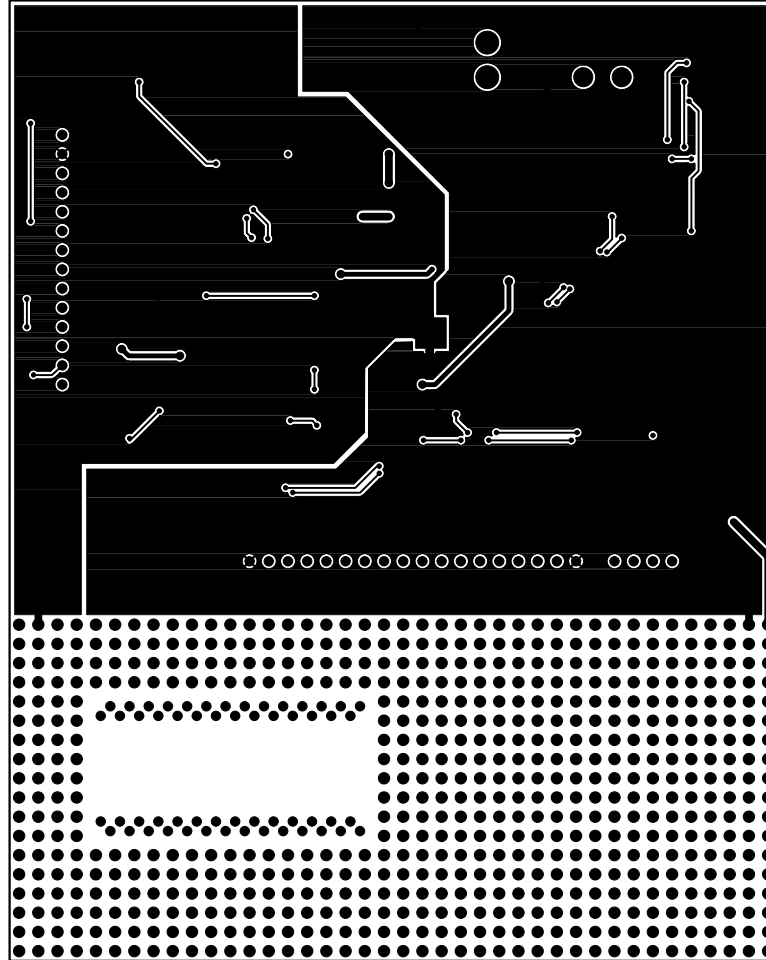
ADuC7020 EVALUATION BOARD Rev. A1 – Component Side View

Silkscreen Top



ADuC7020 EVALUATION BOARD Rev. A1 - Component Side View

Component Side



AduC7020 EVALUATION BOARD Rev. A1 - Component Side View

Solder Side