



DS2164Q G.762 ADPCM Processor

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REVISION A1 ERRATA

The errata listed below describe situations where DS2164Q revision A1 components perform differently than expected or differently than described in the data sheet. Dallas Semiconductor intends to correct these errata in subsequent die revisions.

This errata sheet only applies to DS2164Q revision A1 components. Revision A1 components are branded on the top side of the package with a six-digit code in the form yywwA1, where yy and ww are two-digit numbers representing the year and workweek of manufacture, respectively. To obtain an errata sheet on another DS2164Q die revision, visit our website at www.maxim-ic.com/errata.

1. 16kbps COMPRESSION ALGORITHM IS NOT FUNCTIONAL IN HARDWARE MODE.

Description:

The 64kbps to/from 16kbps compression algorithm is not functional in hardware mode. When the part is configured for the hardware operating mode by setting Pin 12 (SPS) = V_{SS} and the 64kbps to/from 16kbps compression mode is selected by connecting $A1 = V_{DD}$ and $A4 = V_{DD}$, the part will not function correctly resulting in bit errors.

Work Around:

Configure the part for Serial Interface mode and select the 64kbps to/from 16kbps operating mode from the Control Register, bits AS[2:0].