19-6360; Rev 0; 6/12

ERRATA SHEET
DS2483

Revision A2 Errata

The errata listed below describe situations where DS2483 revision A2 components perform differently than expected or differently than described in the data sheet. Maxim Integrated Products, Inc., intends to correct these errata when the opportunity to redesign the product presents itself.

This errata sheet only applies to DS2483 revision A2 components.

Revision A2 components are branded on the topside of a TDFN package with a five-digit code in the form yMXX, where y and M are one-digit numbers representing the year and month of manufacture, respectively. The XX represents the revision and the asterisks * represents the factory code.*

Revision A2 components are branded on the topside of a SOT package with a four-digit code in the form of dc$$, where d and c are the family code (i.e. 3G is DS2483) and $$ represents the SOT revision (i.e. J2 is revision A2).

*To obtain an errata sheet on another DS2483 die revision, visit our website at **www.maxim-ic.com/errata**.*

1) I2C OR REPEATED START DOES NOT FUNCTION PROPERLY.

Description:
Performing an I2C start or repeated start when the APU bit is set during a 1-Wire Reset/Presence Pulse Cycle can result in Presence Pulse contention between the DS2483 driving active high and the 1-Wire slave device driving active low.

Workaround:
Do not perform any I2C start or repeated start after issuing the command “1-Wire Reset” (B4h) until the 1-Wire Reset/Presence Pulse Cycle has completed. Or do not use the Active Pullup (APU) feature.

REVISION HISTORY

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	6/12	Initial release	—