



## ERRATA SHEET DS26504

### Revision B2 Errata

The errata listed below describe situations where DS26504 revision B2 components perform differently than expected or differently than described in the data sheet. Maxim Integrated Products, Inc., intends to correct these errata when the opportunity to redesign the product presents itself.

This errata sheet only applies to DS26504 revision B2 components. Revision B2 components are branded on the topside of the package with a six-digit code in the form yywwB2, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively. To obtain an errata sheet on another DS26504 die revision, visit our website at [www.maxim-ic.com/errata](http://www.maxim-ic.com/errata).

#### 1) GR378 AND G.703 OPTION B COMPOSITE CLOCK TRANSMIT WAVEFORMS COULD SHOW A 20MHz, 500mV PEAK-TO-PEAK OSCILLATION

##### Description:

When the transmit path is configured for GR378 or G.703 Option B composite clock modes, the transmit waveforms could show a 20MHz oscillation with a 500mV peak-to-peak amplitude.

##### Workaround:

This oscillation can be eliminated by setting several factory test registers as shown below. These registers should only be set if the transmit path is set to GR378 or G.703 Option B composite clock operating modes and the oscillation is present. This workaround is not available in hardware mode.

REGISTER ADDRESS (HEX)	VALUE (HEX)
F2	40
F4	C0
F7	80

#### 2) TS\_8K\_4 PIN JTAG FUNCTIONALITY DOES NOT WORK PROPERLY IN TRANSMIT 6312kHz HARDWARE MODE

##### Description:

When the part is configured for transmit 6312kHz hardware mode, the TS\_8K\_4 pin will always sample a logic 1 in JTAG mode. This pin is not used in the transmit 6312kHz operating mode.

##### Workaround:

None.