Revision D5 may be identified by the date/revision brand yywwD5, where yy and ww are the year and workweek of manufacture, respectively. This errata sheet is valid only when used in conjunction with the most current version of the data sheet available from Dallas Semiconductor via the Internet.

This document contains the following types of information:

Errata: These are design errors which deviate from published specifications. Errata are intended to be fixed in subsequent revisions of the device.

Specification Modifications: These are changes to the published specifications and will be reflected in the next update of the documentation and apply to all subsequent revisions of the device.

Documentation Changes: This information includes typographical mistakes, errors, omissions or clarifications of device operation. Items listed in this section will be reflected in the next update of the documentation.

These errata, specification modifications and documentation changes apply to all memory configurations and speed grades unless otherwise noted.

ERRATA

1. Under the following conditions, MOVC instructions may return incorrect data:
    a. Port 0 must be programmed to 00h,
    b. The memory being addressed must be on the embedded bus,
    c. The data being read from memory must be non-zero,
    d. The MCON register must be 98h, A8h, B8h, or C8h,
    e. V_{CC} must be greater than 5.0 V.

Work Around: Avoid the above mentioned conditions.

SPECIFICATION MODIFICATIONS

1. NONE

DOCUMENTATION CHANGES

1. NONE