DS5002FP
Secure Microprocessor Chip

REVISION A3 ERRATA

The errata listed below describe situations where DS5002FP revision A3 components perform differently than expected or differently than described in the data sheet. Dallas Semiconductor intends to fix these errata in subsequent die revisions.

This errata sheet only applies to DS5002FP revision A3 components. Revision A3 components are branded on the topside of the package with a six-digit code of the form yywwA3, where yy and ww are two-digit numbers representing the year and work-week of manufacture, respectively. To obtain an errata sheet on another DS5002FP die revision, visit the website at www.maxim-ic.com.

1. STATE OF CARRY BIT CAN CAUSE HIGH STOP MODE CURRENT

Description:
Selection of Stop mode with the carry bit set will cause the I_{STOP} parameter to be as high as 450µA.

Work Around:
Clear the carry bit before selecting Stop mode.

2. BOOTSTRAP LOADER COMMAND "N" MAY NOT OPERATE PROPERLY

Description:
There is a possibility that the bootstrap loader command (N), which places the unit into freshness mode, may fail to activate freshness mode. Also affected are all revisions of Dallas Semiconductor Secure Microcontroller products DS2251T and DS2252T.

Work Around:
Verify freshness mode by removing V_{CC} and then measuring the voltage between V_{CCO} and GND at a convenient point. If V_{CCO} is at least 1V less than V_{LI}, then the freshness mode is in effect. If V_{CCO} = V_{LI} -0.7V (approx.), then the freshness mode is not in effect. Repeat the freshness command and verification as required.