



## DS5002FPM Secure Microprocessor Chip

[www.maxim-ic.com](http://www.maxim-ic.com)

### REVISION A3 ERRATA

The errata listed below describe situations where DS5002FPM revision A3 components perform differently than expected or differently than described in the data sheet. Dallas Semiconductor intends to fix these errata in subsequent die revisions.

This errata sheet only applies to DS5002FPM revision A3 components. Revision A3 components are branded on the top side of the package with a six-digit code of the form yywwA3, where yy and ww are two-digit numbers representing the year and work-week of manufacture, respectively. To obtain an errata sheet on another DS5002FPM die revision, visit the website at [www.maxim-ic.com](http://www.maxim-ic.com).

The M character at the terminus of the part number indicates that the device is a special version of the DS5002FP manufactured with a special top coating to discourage microprobing.

#### 1. STATE OF CARRY BIT CAN CAUSE HIGH STOP MODE CURRENT

**Description:**

Selection of Stop mode with the carry bit set will cause the  $I_{STOP}$  parameter to be as high as  $450\mu A$ .

**Work Around:**

Clear the carry bit before selecting Stop mode.

#### 2. BOOTSTRAP LOADER COMMAND “N” MAY NOT OPERATE PROPERLY

**Description:**

There is a possibility that the bootstrap loader command (N), which places the unit into freshness mode, may fail to activate freshness mode. Also affected are all revisions of Dallas Semiconductor Secure Microcontroller products DS2251T and DS2252T.

**Work Around:**

Verify freshness mode by removing  $V_{CC}$  and then measuring the voltage between  $V_{CC0}$  and GND at a convenient point. If  $V_{CC0}$  is at least 1V less than  $V_{LI}$ , then the freshness mode is in effect. If  $V_{CC0} = V_{LI} - 0.7V$  (approx.), then the freshness mode is not in effect. Repeat the freshness command and verification as required.