



DS80C410 Networked Microcontroller

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REVISION A1 ERRATA

The errata listed below describe situations where DS80C410 revision A1 components perform differently than expected or differently than described in the data sheet. Dallas Semiconductor intends to correct these errata in subsequent die revisions.

This errata sheet only applies to DS80C410 revision A1 components. Revision A1 components are branded on the topside of the package with a six-digit code in the form yywwA1, where yy and ww are two-digit numbers representing the year and workweek of manufacture, respectively. To obtain an errata sheet on another DS80C410 die revision, visit the website at www.maxim-ic.com/errata.

There are no errata for this revision.

DOCUMENTATION ADVISORIES:

The following alerts users to important updates in the documentation for this device. These are not errata because they may reflect permanent changes to the devices. Refer to the respective document(s) for more information.

High-Speed Microcontroller User's Guide: Network Microcontroller Supplement

The following clarification has been added to *Section 20: Arithmetic Accelerator* under the paragraph *Normalize*:

"Software must ensure that the input value for the normalize operation is not zero or the function will not complete. Compilers such as the one from Keil Software have updated their libraries and compensate for this condition."