



# PNU65010ER

650 V, 1 A ultrafast recovery rectifier

1 August 2022

Product data sheet

## 1. General description

High power density, ultrafast switching time recovery rectifier with high-efficiency planar technology, encapsulated in a small and flat lead CFP3 (SOD123W) Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Reverse voltage  $V_R \leq 650$  V
- Forward current  $I_F \leq 1$  A
- Typical switching time  $t_{rr}$  of 35 ns
- Pt doped life time control
- Low inductance
- Power and flat lead SMD plastic package
- High power capability due to clip-bond technology
- Planar die design

## 3. Applications

- AC/DC converter
- SMPS / UPS
- PFC
- Battery charger
- Inverter
- Freewheeling applications

## 4. Quick reference data


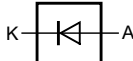
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	$\delta = 0.5$ ; $f = 20$ kHz; square wave; $T_{sp} \leq 141$ °C		-	-	1	A
$V_{RRM}$	repetitive peak reverse voltage	$T_j = 25$ °C		-	-	650	V
$V_R$	reverse voltage			-	-	650	V
$V_F$	forward voltage	$I_F = 1$ A; $T_j = 25$ °C	[1]	-	1	1.2	V
		$I_F = 1$ A; $T_j = 125$ °C	[1]	-	0.93	1.06	V
$I_R$	reverse current	$V_R = 650$ V; $T_j = 25$ °C	[1]	-	-	1	$\mu$ A
		$V_R = 650$ V; $T_j = 125$ °C	[1]	-	0.5	10	$\mu$ A

[1] Very short pulse, in order to maintain a stable junction temperature.

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 CFP3 (SOD123W)	 006aab040
2	A	anode		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
<a href="#">PNU65010ER</a>	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	<a href="#">SOD123W</a>

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PNU65010ER	ER

## 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 601134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{RRM}$	repetitive peak reverse voltage	$T_j = 25\text{ °C}$		-	650	V
$V_R$	reverse voltage			-	650	V
$V_{RMS}$	RMS voltage			-	460	V
$I_F$	forward current	$\delta = 1; T_{sp} \leq 138\text{ °C}$		-	1.4	A
$I_{F(AV)}$	average forward current	$\delta = 0.5; f = 20\text{ kHz};$ square wave; $T_{sp} \leq 141\text{ °C}$		-	1	A
$I_{FSM}$	non-repetitive peak forward current	$t_p = 8.3\text{ ms};$ single half sine wave (applied at rated load condition); $T_{j(init)} = 25\text{ °C}$		-	30	A
$P_{tot}$	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	625	mW
			[2]	-	1	W
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-55	150	°C
$T_{stg}$	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	200	K/W
			[2]	-	-	125	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[3]	-	-	8	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [3] Soldering point of cathode tab.

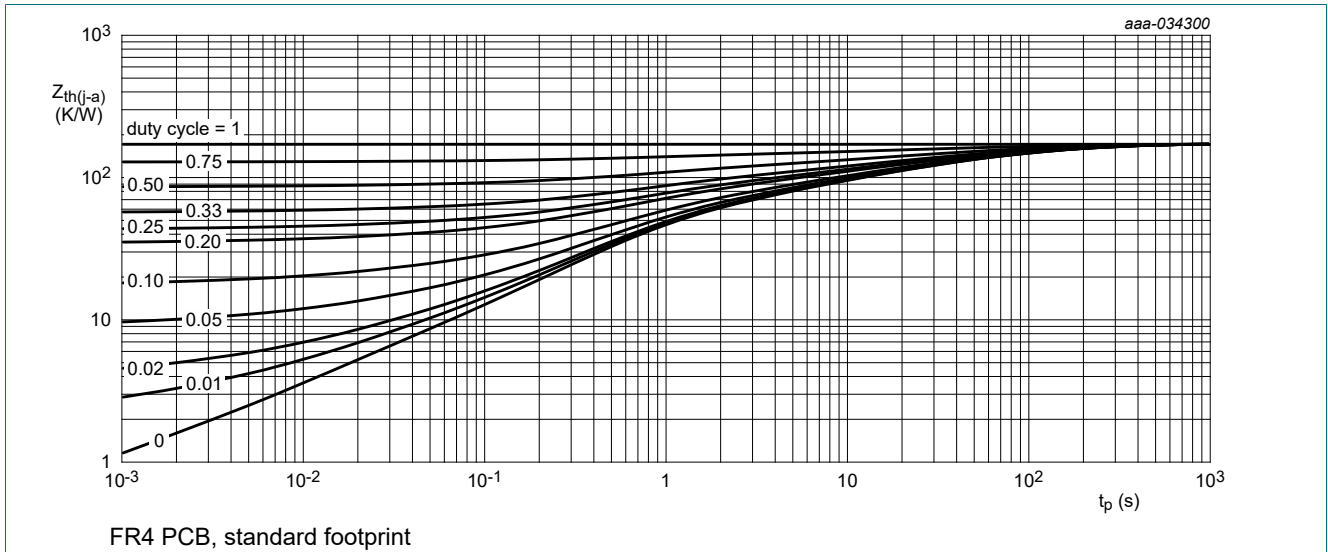


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

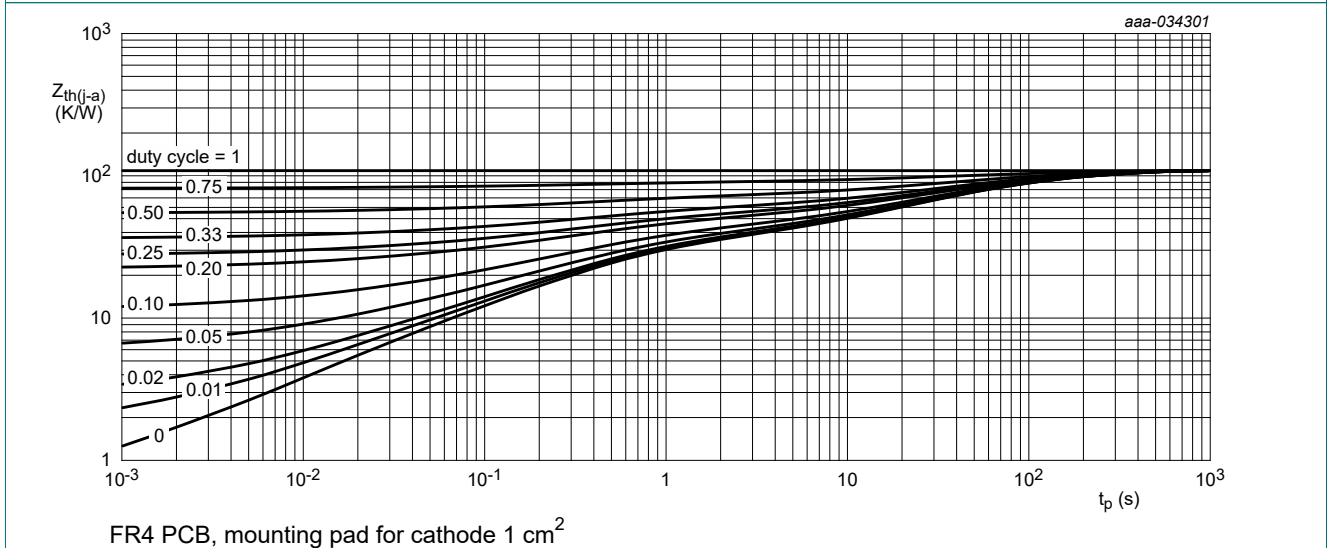


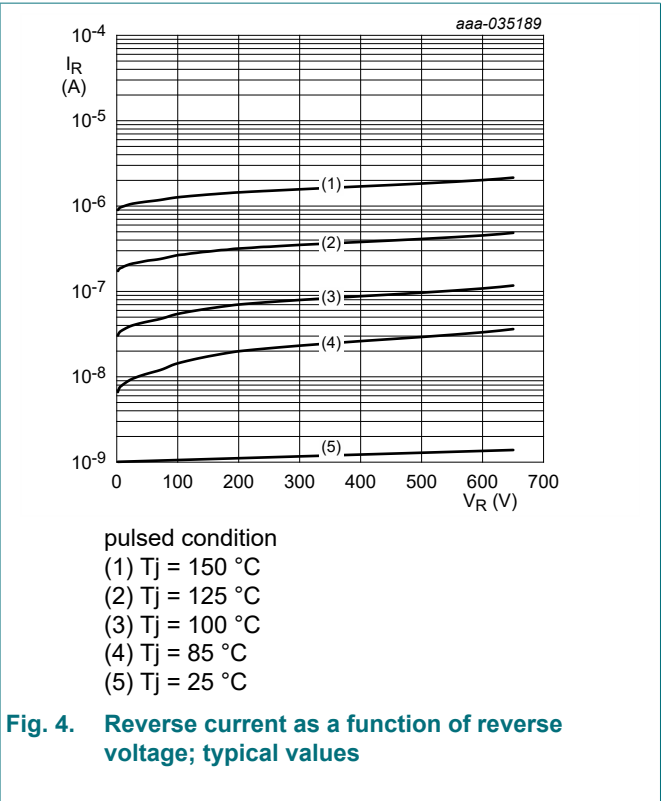
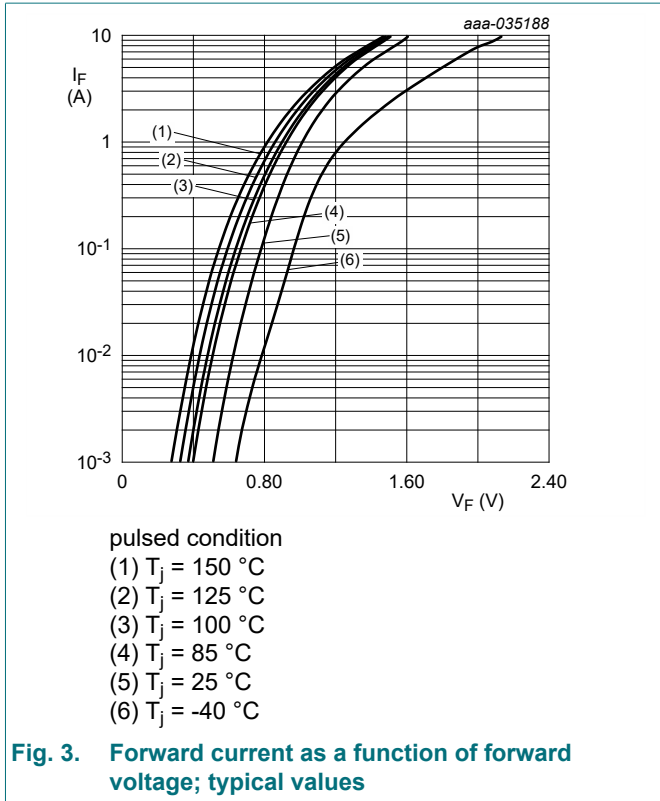
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

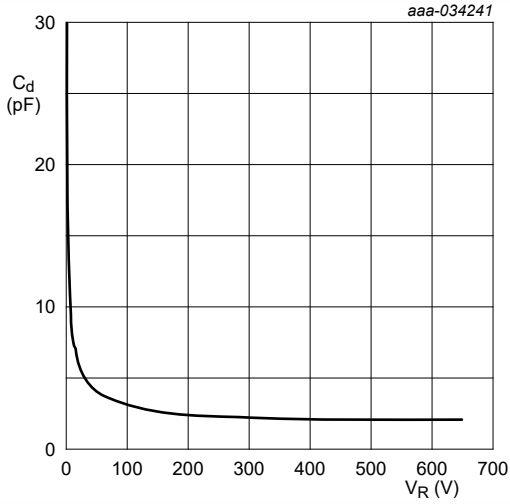
### 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
$V_{(BR)R}$	reverse breakdown voltage	$I_R = 100 \mu A; T_j = 25 \text{ }^\circ C$	[1]	650	-	V	
$V_F$	forward voltage	$I_F = 1 A; T_j = 25 \text{ }^\circ C$	[1]	-	1	1.2	V
		$I_F = 1 A; T_j = 125 \text{ }^\circ C$	[1]	-	0.93	1.06	V
$I_R$	reverse current	$V_R = 650 V; T_j = 25 \text{ }^\circ C$	[1]	-	-	1	$\mu A$
		$V_R = 650 V; T_j = 125 \text{ }^\circ C$	[1]	-	0.5	10	$\mu A$
$C_d$	diode capacitance	$V_R = 4 V; f = 1 \text{ MHz}; T_j = 25 \text{ }^\circ C$		-	11	pF	
$t_{rr}$	reverse recovery time ; step recovery	$I_F = 0.5 A; I_R = 1 A; I_{R(meas)} = 0.25 A; T_j = 25 \text{ }^\circ C$		-	35	65	ns
		$I_F = 1 A; dI_F/dt = 50 A/\mu s; V_R = 30 V; T_j = 25 \text{ }^\circ C$		-	39	85	ns
		$I_F = 1 A; dI_F/dt = 100 A/s; V_R = 30 V; T_j = 25 \text{ }^\circ C$		-	26	-	ns
$I_{RM}$	peak reverse recovery current	$I_F = 1 A; dI_F/dt = 100 A/\mu s; V_R = 30 V; T_j = 25 \text{ }^\circ C$		-	1.5	A	
$Q_{rr}$	reverse recovery charge			-	20	nC	
$V_{FRM}$	peak forward recovery voltage	$I_F = 1 A; dI_F/dt = 50 A/\mu s; T_j = 25 \text{ }^\circ C$		-	5.2	V	

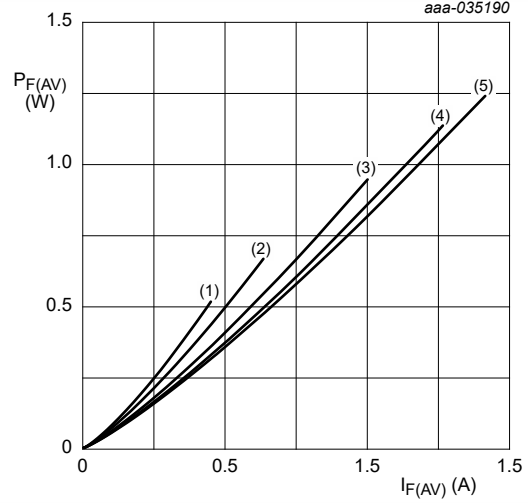
[1] Very short pulse, in order to maintain a stable junction temperature.





$f = 1 \text{ MHz}; T_{\text{amb}} = 25 \text{ }^\circ\text{C}$

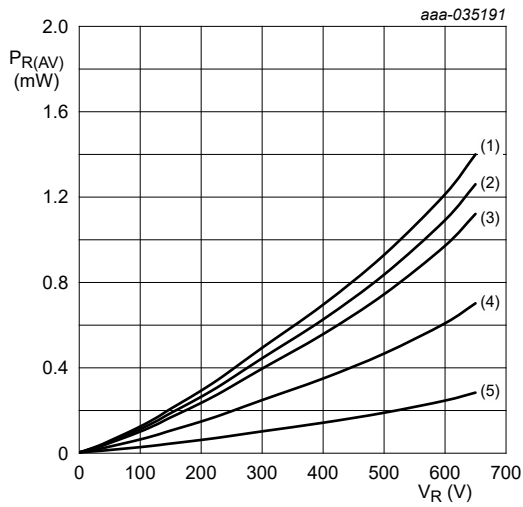
**Fig. 5. Diode capacitance as a function of reverse voltage; typical values**



$T_j = 150 \text{ }^\circ\text{C}$

- (1)  $\delta = 0.1$
- (2)  $\delta = 0.2$
- (3)  $\delta = 0.5$
- (4)  $\delta = 0.8$
- (5)  $\delta = 1 \text{ (DC)}$

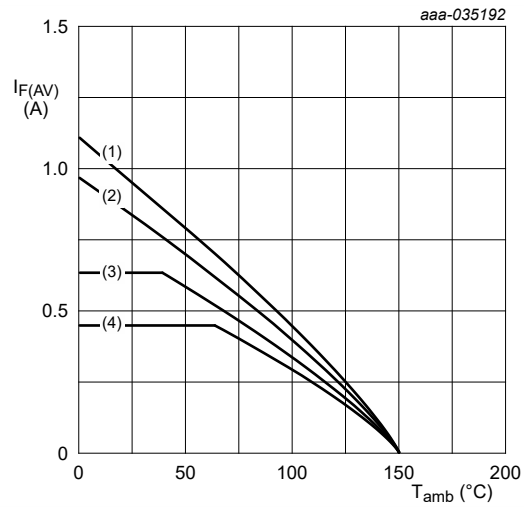
**Fig. 6. Average forward power dissipation as a function of average forward current; typical values**



$T_j = 150 \text{ }^\circ\text{C}$

- (1)  $\delta = 1; \text{DC}$
- (2)  $\delta = 0.9$
- (3)  $\delta = 0.8$
- (4)  $\delta = 0.5$
- (5)  $\delta = 0.2$

**Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values**

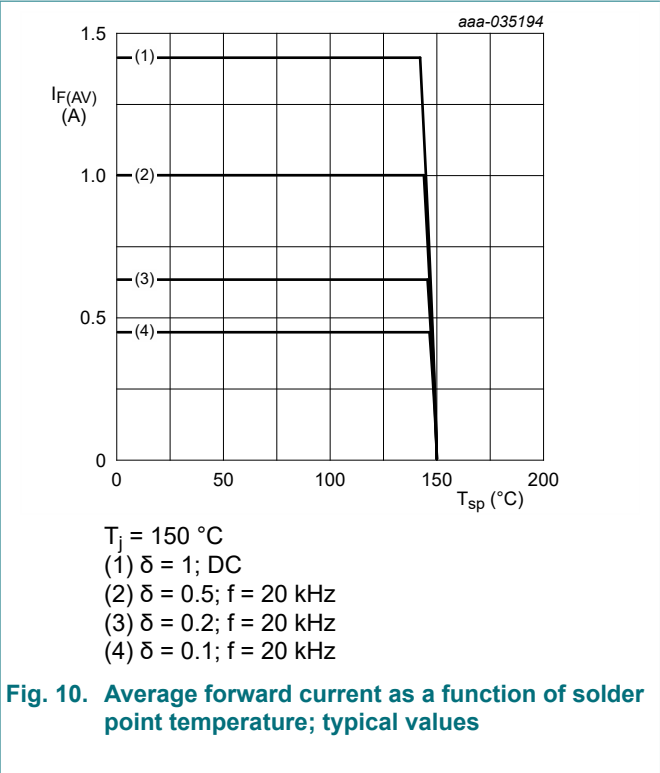
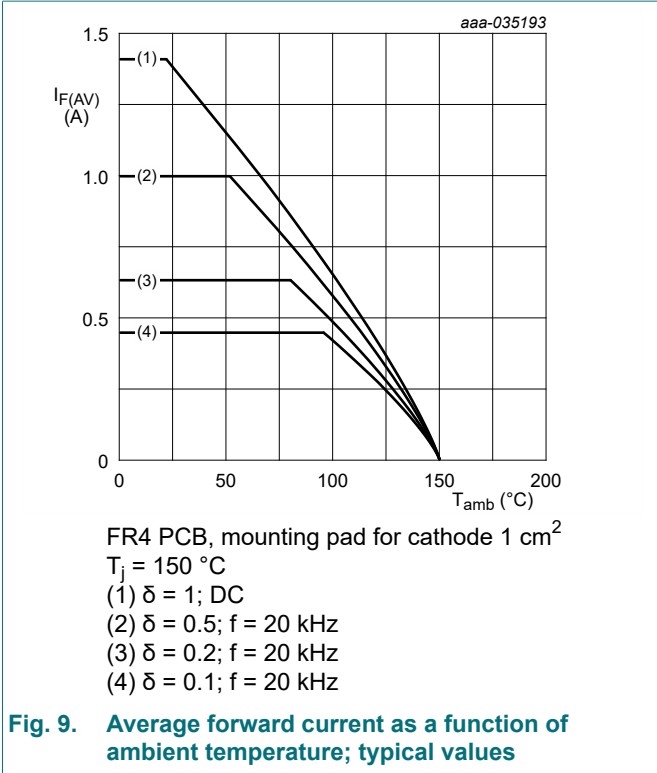


FR4 PCB, standard footprint

$T_j = 150 \text{ }^\circ\text{C}$

- (1)  $\delta = 1; \text{DC}$
- (2)  $\delta = 0.5; f = 20 \text{ kHz}$
- (3)  $\delta = 0.2; f = 20 \text{ kHz}$
- (4)  $\delta = 0.1; f = 20 \text{ kHz}$

**Fig. 8. Average forward current as a function of ambient temperature; typical values**



### 11. Test information

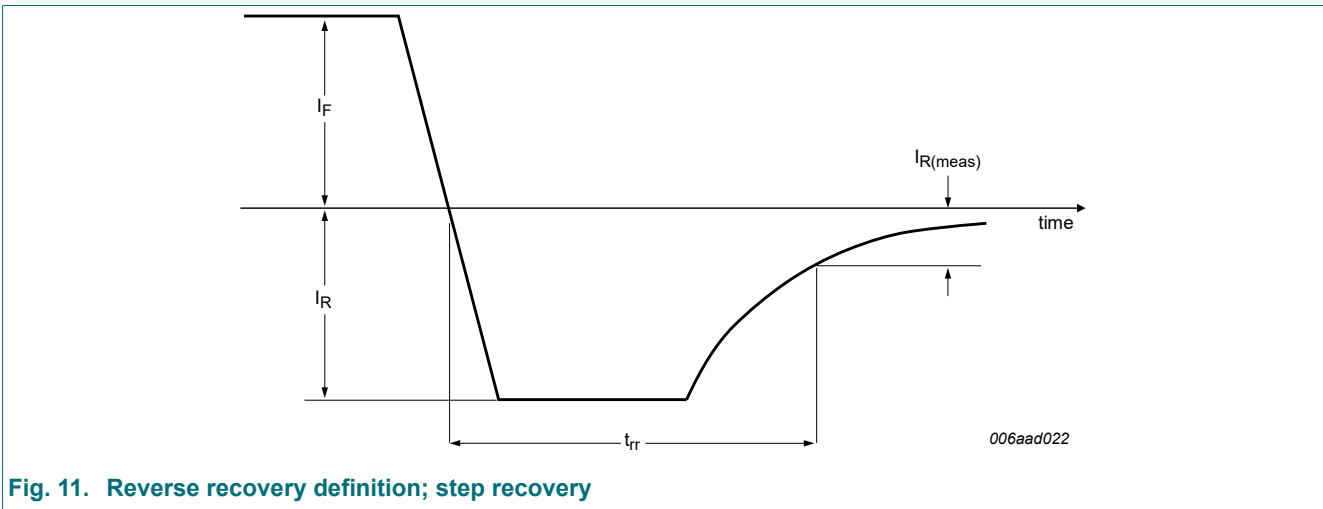




Fig. 12. Reverse recovery definition; ramp recovery



Fig. 13. Forward recovery definition



Fig. 14. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

$$I_{F(AV)} = I_M \times \delta \text{ with } I_M \text{ defined as peak current}$$

$$I_{RMS} = I_{F(AV)} \text{ at DC, and } I_{RMS} = I_M \times \sqrt{\delta}$$

with  $I_{RMS}$  defined as RMS current.

## 12. Package outline

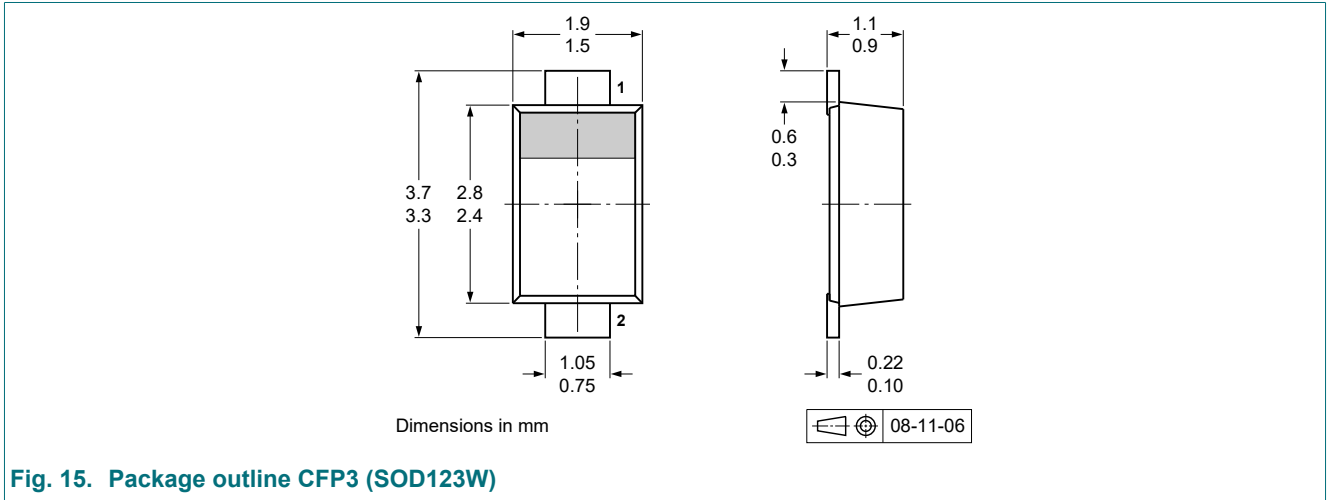


Fig. 15. Package outline CFP3 (SOD123W)



### 13. Soldering

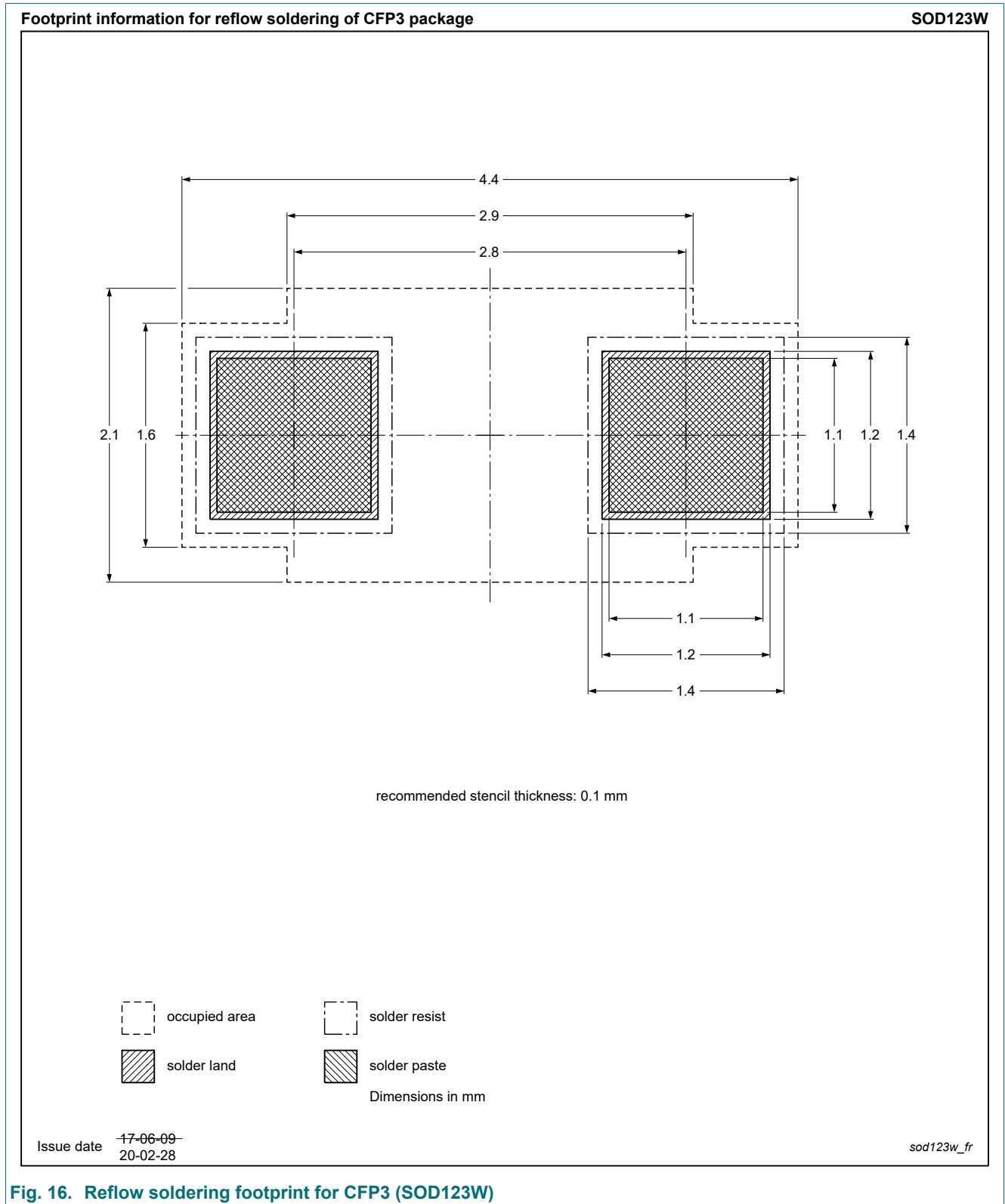
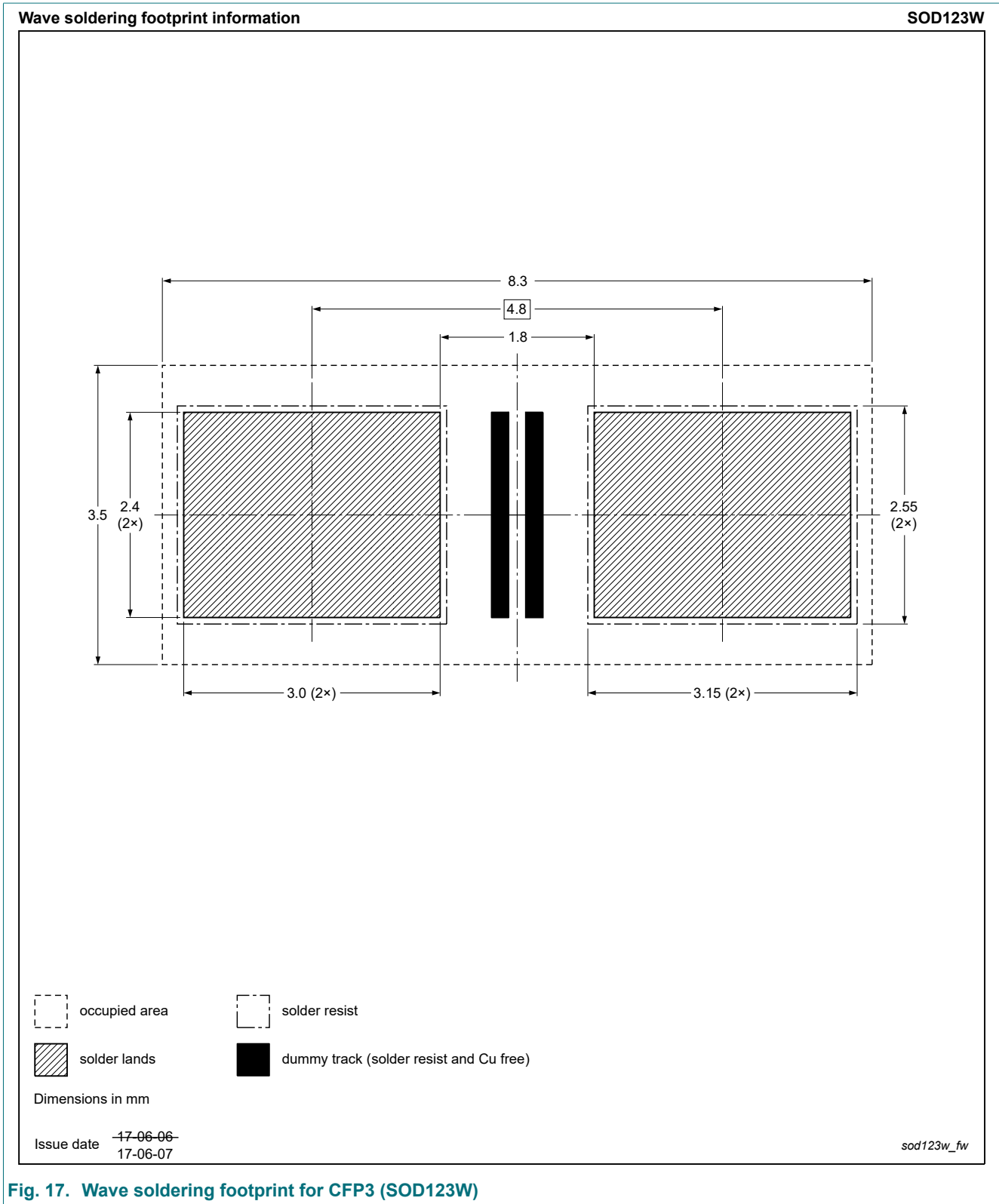


Fig. 16. Reflow soldering footprint for CFP3 (SOD123W)



**Fig. 17. Wave soldering footprint for CFP3 (SOD123W)**

## 14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PNU65010ER v.3	20220801	Product data sheet	-	PNU65010ER v.2
Modifications:	<ul style="list-style-type: none"><li>• Specification adapted for a maximum temperature of 150 °C</li><li>• Product status changed</li></ul>			
PNU65010ER v.2	20220629	Preliminary data sheet	-	PNU65010ER v.1
PNU65010ER v.1	20211222	Objective data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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