

## Single, dual, or triple gate logic functions in small footprint $\leq 10$ -pin leaded packages

Our PicoGate portfolio comprises single-, dual-, and triple-gate functions in small 5-, 6-, 8- or 10-pin leaded packages. Compared to traditional quad-gate solutions, PicoGate allows to select the specific number of gate functions required. Intricate line layout patterns can be created while saving up to 85% board space compared to equivalent standard packages. Logic families available in PicoGate leaded packages include AXP, AUP, AHC(T), AVC, CBTLV(D), HC(T), LVC and LV1T.

### Features

- › Small footprint
- › 0.95 mm, 0.65 mm and 0.5 mm pitch options
- › Profile height of 1.0 mm and 1.1 mm
- › Pb-free, RoHS and dark green compliant
- › Fully specified from -40 to 125 °C
- › AEC-Q100 options

### Benefits

- › Simplified board layout
- › Low power consumption
- › Surface mount

### Applications

- › Space-constrained applications
- › Consumer
- › Portable
- › Industrial
- › Automotive

Mini Logic is a portfolio composed of MicroPak leadless packages and PicoGate leaded packages. PicoGates are available in technology families AXP, AUP, AVC, LVC, AHC(T), HC(T), LV1T and CBTLV(D). Our PicoGate packages house the same logic functions as the larger SO, TSSOP & DQFN packages, but in single gates rather than using one gate of a quad. With the extensive portfolio of solutions, board space can be saved and lower-power consumption can be achieved.

These products are all Pb-free, RoHS and dark green compliant, and designed for use at ambient temperatures between -40°C and 125°C. Automotive variants that meet the AEC-Q100, grade 1 standard are available for a range of PicoGate solutions. PicoGate packages have a pitch of 0.95 mm, 0.65 mm, or 0.5 mm.








## PicoGate portfolio

Our PicoGate portfolio is the industry's broadest and includes gates, level-shifters/translators, analog switches, buffers/inverters/drivers, bus switches, decoders/demultiplexers, flip-flops, multiplexers, latches and Schmitt-Trigger devices. PicoGate solutions are available in eight technology families.

Family	HC(T)	AHC(T)	AUP	AVC	AXP	CBT(D)	CBTLV	LV1T
Supply voltage (V)	2 to 6.0	2 to 5.5	0.8 to 3.6	1.2 to 3.6	0.7 to 2.75	4.5 to 5.5	2.3 to 3.6	1.6 to 5.5
Propagation delay, typ (ns)	9	5	3.4	3.5	2.9	0.15	0.15	4.6
Output drive (mA)	±8	±8	±1.9	±8	±4.5	N/A	N/A	±8
Standby current (µA)	80	40	0.9	12	0.6	3	10	10
Temperature range (°C)	-40 to +125	-40 to +125	-40 to +125	-40 to +125	-40 to +85	-40 to +85	-40 to +125	-40 to +125
Automotive option	•	•	•	•		•	•	•
<b>Features</b>								
Over-voltage tolerant input	•	•	•	•	•	•	•	
Schmitt-trigger inputs	•	•	•		•			•
Low-threshold inputs	•	•	•		•			•
Input clamp diodes	•							•
TTL inputs	•	•				•		
Bus hold								
Power-off leakage (I <sub>off</sub> )			•	•	•			
Source termination								
Open-drain outputs	•	•	•		•			•
Low-delay isolation						•	•	

## PicoGate packages

Our PicoGate package range includes TSOP, TSSOP and VSSOP leaded packages (5 to 10 pins).

Package suffix	GW	GV	GW	GV	DP	DC	DP
	TSSOP5	TSOP5	TSSOP6	TSOP6	TSSOP8	VSSOP8	TSSOP10
							
Package	SOT353-1	SOT753	SOT363	SOT457	SOT505-2	SOT765-1	SOT552-1
Width (mm)	1.25	1.5	1.25	1.5	3	2.3	3.3
Length (mm)	2.1	2.9	2.1	2.9	3	2	3.3
Height (mm)	0.95	1	0.95	1	1.1	1	1.1
Pitch (mm)	0.65	0.95	0.65	0.95	0.65	0.50	0.50

For more information about PicoGate solutions from Nexperia, visit: [nexperia.com/products/logic/family/PICOGATE/](https://www.nexperia.com/products/logic/family/PICOGATE/)

## Buffers/inverters/drivers

Type number	Description	V <sub>CC</sub> (V)	I <sub>O</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin	
						SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DP)	SOT765-1 (DC)
74AHC1G04	single inverter	2.0 - 5.5	+/- 8	3.1	-40~125	•	•				
74AHC1G07	single buffer; open-drain	2.0 - 5.5	8	2.5	-40~125	•	•				
74AHC1G125	single buffer/line driver (3-state)	2.0 - 5.5	+/- 8	3.4	-40~125	•	•				
74AHC1G126	single buffer/line driver (3-state)	2.0 - 5.5	+/- 8	3.4	-40~125	•	•				
74AHC1GU04	single inverter; unbuffered	2.0 - 5.5	+/- 8	2.6	-40~125	•	•				
74AHC2G125	dual buffer/line driver (3-state)	2.0 - 5.5	+/- 8	3.4	-40~125					•	•
74AHC2G126	dual buffer/line driver (3-state)	2.0 - 5.5	+/- 8	3.4	-40~125					•	•
74AHC2G241	dual buffer/line driver (3-state)	2.0 - 5.5	+/- 8	3.4	-40~125					•	•
74AHC3G04	triple inverter	2.0 - 5.5	+/- 8	3.1	-40~125					•	•
74AHC3GU04	triple inverter; unbuffered	2.0 - 5.5	+/- 8	2.5	-40~125					•	•
74AHCT1G04	single inverter; TTL enabled	4.5 - 5.5	+/- 8	3.4	-40~125	•	•				
74AHCT1G125	single buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 8	3.4	-40~125	•	•				
74AHCT1G126	single buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 8	3.4	-40~125	•	•				
74AHCT1G14	single inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 8	4.1	-40~125	•	•				
74AHCT1G17	single buffer Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 8	4.1	-40~125	•	•				
74AHCT2G125	dual buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 8	3.4	-40~125					•	•
74AHCT2G126	dual buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 8	3.4	-40~125						•
74AHCT2G241	dual buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 8	3.4	-40~125						•
74AHCT3G04	triple inverter; TTL enabled	4.5 - 5.5	+/- 8	3	-40~125						•
74AHCT3G14	triple inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 8	4.1	-40~125					•	•
74AUP1G04	single inverter	1.1 - 3.6	+/- 1.9	4	-40~125	•	•				
74AUP1G06	single inverter; open-drain	1.1 - 3.6	1.9	4.5	-40~125	•					
74AUP1G07	single buffer; open-drain	1.1 - 3.6	1.9	4.4	-40~125	•					
74AUP1G125	single buffer/line driver (3-state)	1.1 - 3.6	+/- 1.9	4.3	-40~125	•					
74AUP1G126	single buffer/line driver (3-state)	1.1 - 3.6	+/- 1.9	4.3	-40~125	•					
74AUP1G16	single buffer/line driver (3-state)	1.1 - 3.6	+/- 1.9	3.9	-40~125	•					
74AUP1G240	single inverter/line driver (3-state)	1.1 - 3.6	+/- 1.9	4.2	-40~125	•					
74AUP1G34	single buffer	1.1 - 3.6	+/- 1.9	3.9	-40~125	•					
74AUP1GU04	single inverter; unbuffered	1.1 - 3.6	+/- 1.9	2.3	-40~125	•					
74AUP2G04	dual inverter	1.1 - 3.6	+/- 1.9	4	-40~125			•			
74AUP2G06	dual inverter; open-drain	1.1 - 3.6	1.9	4.5	-40~125			•			
74AUP2G0604	inverter with open-drain and inverter	1.1 - 3.6	+/- 1.9	4	-40~125			•			
74AUP2G07	dual buffer; open-drain	1.1 - 3.6	1.9	4.4	-40~125			•			
74AUP2G125	dual buffer/line driver (3-state)	1.1 - 3.6	+/- 1.9	4.3	-40~125						•
74AUP2G126	dual buffer/line driver (3-state)	1.1 - 3.6	+/- 1.9	4.3	-40~125						•
74AUP2G16	dual buffer	1.1 - 3.6	+/- 1.9	3.9	-40~125			•			
74AUP2G240	dual inverter/line driver (3-state)	1.1 - 3.6	+/- 1.9	4.2	-40~125						•

## Buffers/inverters/drivers (continued)

Type number	Description	V <sub>CC</sub> (V)	I <sub>O</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin	
						SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DIP)	SOT765-1 (DC)
74AUP2G241	dual buffer/line driver (3-state)	1.1 - 3.6	+/- 1.9	4.3	-40~125						•
74AUP2G34	dual buffer	1.1 - 3.6	+/- 1.9	3.9	-40~125			•			
74AUP2GU04	dual inverter; unbuffered	1.1 - 3.6	+/- 1.9	2.3	-40~125			•			
74AUP3G04	triple inverter	1.1 - 3.6	+/- 1.9	4	-40~125						•
74AUP3G0434	dual inverter and single buffer	1.1 - 3.6	+/- 1.9	4	-40~125						•
74AUP3G07	triple buffer; open-drain	1.1 - 3.6	+/- 1.9	4.4	-40~125						•
74AUP3G16	triple buffer	1.1 - 3.6	+/- 1.9	3.9	-40~125						•
74AUP3G34	triple buffer	1.1 - 3.6	+/- 1.9	3.9	-40~125						•
74AUP3G3404	dual buffer and single inverter	1.1 - 3.6	+/- 1.9	4	-40~125						•
74HC1G04	single inverter	2.0 - 6.0	+/- 2.6	7	-40~125	•	•				
74HC1G125	single buffer/line driver (3-state)	2.0 - 6.0	+/- 2.6	9	-40~125	•	•				
74HC1G126	single buffer/line driver (3-state)	2.0 - 6.0	+/- 2.6	9	-40~125	•	•				
74HC1GU04	single inverter; unbuffered	2.0 - 6.0	+/- 2.6	5	-40~125	•	•				
74HC2G04	dual inverter	2.0 - 6.0	+/- 5.2	8	-40~125			•	•		
74HC2G125	dual buffer/line driver (3-state)	2.0 - 6.0	+/- 5.2	10	-40~125					•	•
74HC2G16	dual buffer	2.0 - 6.0	+/- 5.2	9	-40~125			•	•		
74HC2G34	dual buffer	2.0 - 6.0	+/- 5.2	9	-40~125			•	•		
74HC2GU04	single inverter; unbuffered	2.0 - 6.0	+/- 2.6	5	-40~125			•	•		
74HC3G04	triple inverter	2.0 - 6.0	+/- 5.2	8	-40~125					•	•
74HC3G06	triple inverter; open-drain	2.0 - 6.0	5.2	9	-40~125						•
74HC3G07	triple buffer; open-drain	2.0 - 6.0	5.2	9	-40~125					•	•
74HC3G16	triple buffer	2.0 - 6.0	+/- 5.2	9	-40~125					•	
74HC3G34	triple buffer	2.0 - 6.0	+/- 5.2	9	-40~125					•	•
74HC3GU04	triple inverter; unbuffered	2.0 - 6.0	+/- 5.2	6	-40~125					•	•
74HCT1G04	single inverter; TTL enabled	4.5 - 5.5	+/- 2	8	-40~125	•	•				
74HCT1G125	single buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 2	10	-40~125	•	•				
74HCT1G126	single buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 2	10	-40~125	•	•				
74HCT2G04	dual inverter; TTL enabled	4.5 - 5.5	+/- 4	10	-40~125			•	•		
74HCT2G125	dual buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 4	12	-40~125					•	•
74HCT2G16	dual buffer; TTL enabled	4.5 - 5.5	+/- 4	10	-40~125			•	•		
74HCT2G34	dual buffer; TTL enabled	4.5 - 5.5	+/- 4	10	-40~125			•	•		
74HCT3G04	triple inverter; TTL enabled	4.5 - 5.5	+/- 4	10	-40~125					•	•
74HCT3G06	triple inverter; open-drain; TTL enabled	4.5 - 5.5	4	9	-40~125						•
74HCT3G07	triple buffer; open-drain; TTL enabled	4.5 - 5.5	4	9	-40~125					•	•
74HCT3G34	triple buffer; TTL enabled	4.5 - 5.5	+/- 4	10	-40~125					•	•
74LVC1G04	single inverter	1.65 - 5.5	+/- 32	2	-40~125	•	•				
74LVC1G06	single inverter; open-drain	1.65 - 5.5	32	2.3	-40~125	•	•				

## Buffers/inverters/drivers (continued)

Type number	Description	V <sub>CC</sub> (V)	I <sub>O</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin	
						SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DP)	SOT765-1 (DC)
74LVC1G07	single buffer; open-drain	1.65 - 5.5	32	2.2	-40~125	•	•				
74LVC1G125	single buffer/line driver (3-state)	1.65 - 5.5	+/- 32	2.1	-40~125	•	•				
74LVC1G126	single buffer/line driver (3-state)	1.65 - 5.5	+/- 32	2	-40~125	•	•				
74LVC1G16	single buffer	1.65 - 5.5	+/- 32	2	-40~125	•					
74LVC1G34	single buffer	1.65 - 5.5	+/- 32	2	-40~125	•	•				
74LVC1GU04	single inverter; unbuffered	1.65 - 5.5	+/- 32	1.6	-40~125	•	•				
74LVC2G04	dual inverter	1.65 - 5.5	+/- 32	2.7	-40~125			•	•		
74LVC2G06	dual inverter; open-drain	1.65 - 5.5	32	2.3	-40~125			•	•		
74LVC2G07	dual buffer; open-drain	1.65 - 5.5	32	2.6	-40~125			•	•		
74LVC2G125	dual buffer/line driver; TTL enabled (3-state)	1.65 - 5.5	+/- 32	2.3	-40~125					•	•
74LVC2G126	dual buffer/line driver; TTL enabled (3-state)	1.65 - 5.5	+/- 32	2.4	-40~125					•	•
74LVC2G16	dual buffer	1.65 - 5.5	+/- 32	2.5	-40~125			•			
74LVC2G240	dual inverter/line driver (3-state)	1.65 - 5.5	+/- 32	2.5	-40~125					•	•
74LVC2G241	dual buffer/line driver (3-state)	1.65 - 5.5	+/- 32	2.6	-40~125					•	•
74LVC2G34	dual buffer	1.65 - 5.5	+/- 32	2.2	-40~125			•	•		
74LVC2GU04	dual inverter; unbuffered	1.65 - 5.5	+/- 32	2.3	-40~125			•	•		
74LVC3G04	triple inverter	1.65 - 5.5	+/- 32	2.7	-40~125					•	•
74LVC3G06	triple inverter; open-drain	1.65 - 5.5	32	2	-40~125					•	•
74LVC3G07	triple buffer; open-drain	1.65 - 5.5	32	2.1	-40~125					•	•
74LVC3G16	triple buffer	1.65 - 5.5	+/- 32	3.2	-40~125					•	
74LVC3G34	triple buffer	1.65 - 5.5	+/- 32	2.2	-40~125					•	•
74LVC3GU04	triple inverter; unbuffered	1.65 - 5.5	+/- 32	2.3	-40~125					•	•
XC7SET04	single inverter; TTL enabled	4.5 - 5.5	+/- 8	3.5	-40~125	•	•				
XC7SET125	single buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	+/- 8	3.4	-40~125	•	•				
XC7SH04	single inverter	2.0 - 5.5	+/- 8	3.5	-40~125	•	•				
XC7SH125	single buffer/line driver (3-state)	2.0 - 5.5	+/- 8	3.4	-40~125	•	•				
XC7SHU04	single inverter; unbuffered	2.0 - 5.5	+/- 8	3.5	-40~125	•	•				
XC7WH126	dual buffer/line driver (3-state)	2.0 - 5.5	+/- 8	3.4	-40~125					•	

## Schmitt-Triggers

Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin		10-pin
						SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DP)	SOT765-1 (DC)	SOT552-1 (DP)
74AHC1G14	single inverter Schmitt-trigger	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
74AHC1G17	single buffer Schmitt-trigger	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
74AHC3G14	triple inverter Schmitt-trigger	2.0 - 5.5	+/- 8	3.2	-40~125					•	•	
74AHCT1G14	single inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 8	4.1	-40~125	•	•					
74AHCT1G17	single buffer Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 8	4.1	-40~125	•	•					
74AHCT3G14	triple inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 8	4.1	-40~125					•	•	
74AUP1G132	single 2-input NAND gate Schmitt-trigger	1.1 - 3.6	+/- 1.9	10	-40~125	•						
74AUP1G14	single inverter; Schmitt-trigger	1.1 - 3.6	+/- 1.9	4.7	-40~125	•	•					
74AUP1G17	single buffer Schmitt-trigger	1.1 - 3.6	+/- 1.9	7.8	-40~125	•	•					
74AUP1G57	configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1G58	configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1G97	configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1G98	configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.9	-40~125			•				
74AUP1T14	single supply translating Schmitt-Trigger Inverter	2.3 - 3.6	+/- 1.9	8.7	-40~125	•						
74AUP1T17	single supply translating Schmitt-Trigger Buffer	2.3 - 3.6	+/- 1.9	8.7	-40~125	•						
74AUP1T50	single supply translating Schmitt-Trigger Buffer	2.3 - 3.6	+/- 1.9	8.7	-40~125	•						
74AUP2G132	dual 2-input NAND gate Schmitt-trigger	1.1 - 3.6	+/- 1.9	10	-40~125						•	
74AUP2G14	dual inverter Schmitt-trigger	1.1 - 3.6	+/- 1.9	4.7	-40~125			•				
74AUP2G17	dual buffer Schmitt-trigger	1.1 - 3.6	+/- 1.9	7.8	-40~125			•				
74AUP2G57	dual configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125							•
74AUP2G58	dual configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125							•
74AUP2G97	dual configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125							•
74AUP2G98	dual configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.9	-40~125							•
74AUP3G14	triple inverter; Schmitt-trigger	1.1 - 3.6	+/- 1.9	4.7	-40~125						•	
74AUP3G17	triple buffer Schmitt-trigger	1.1 - 3.6	+/- 1.9	4.7	-40~125						•	
74AXP1T14	dual-supply Schmitt-trigger inverter	0.7 - 2.75	+/- 12	4.9	-40~125	•						
74AXP1T57	single dual-supply translating configurable gate; Schmitt-trigger inputs	0.7 - 2.75	+/- 12	4.8	-40~125						•	
74HC1G14	single inverter Schmitt-trigger	2.0 - 6.0	+/- 2.6	10	-40~125	•	•					
74HC2G14	dual inverter Schmitt-trigger	2.0 - 6.0	+/- 5.2	16	-40~125			•	•			
74HC2G17	dual buffer Schmitt-trigger	2.0 - 6.0	+/- 5.2	12	-40~125			•	•			
74HC3G14	triple inverter Schmitt-trigger	2.0 - 6.0	+/- 5.2	16	-40~125					•	•	
74HCT1G14	single inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 4	15	-40~125	•	•					
74HCT2G14	dual inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 4	21	-40~125			•	•			
74HCT2G17	dual buffer Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 4	21	-40~125			•	•			
74HCT3G14	triple inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 4	21	-40~125					•	•	
74LVC1G14	single inverter Schmitt-trigger	1.65 - 5.5	+/- 32	3	-40~125	•	•					
74LVC1G17	single buffer Schmitt-trigger	1.65 - 5.5	+/- 32	3	-40~125	•	•					

## Schmitt-Triggers (continued)

Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin		10-pin
						SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DP)	SOT765-1 (DC)	SOT552-1 (DP)
74LVC1G57	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	6.3	-40~125			•	•			
74LVC1G58	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	6.3	-40~125			•	•			
74LVC1G97	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	6.3	-40~125			•	•			
74LVC1G98	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	6.3	-40~125			•	•			
74LVC1G99	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	8.4	-40~125					•		
74LVC2G14	dual inverter Schmitt-trigger	1.65 - 5.5	+/- 32	3.9	-40~125			•	•			
74LVC2G17	dual buffer Schmitt-trigger	1.65 - 5.5	+/- 32	3.6	-40~125			•	•			
74LVC3G14	triple inverter Schmitt-trigger	1.65 - 5.5	+/- 32	3.2	-40~125					•	•	
74LVC3G17	triple buffer Schmitt-trigger	1.65 - 5.5	+/- 32	3.6	-40~125					•	•	
XC7SET14	single inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 8	4.1	-40~125	•	•					
XC7SH14	single inverter Schmitt-trigger	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
XC7WH14	triple inverter Schmitt-trigger	2.0 - 5.5	+/- 8	3.2	-40~125					•	•	
XC7WT14	triple inverter Schmitt-trigger; TTL enabled	4.5 - 5.5	+/- 8	4.1	-40~125					•	•	

## Voltage translators/level-shifters

Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin	6-pin	8-pin		10-pin
						SOT353-1 (GW)	SOT363 (GW)	SOT505-2 (DP)	SOT765-1 (DC)	SOT552-1 (DP)
74AUP1T00	2-input single supply translating NAND gate	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T02	2-input single supply translating NOR gate	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T04	single supply translating inverter	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T08	2-input single supply translating AND gate	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T14	single supply translating Schmitt-Trigger Inverter	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T17	single supply translating Schmitt-Trigger Buffer	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T32	2-input single supply translating OR gate	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T34	single dual supply translating buffer	1.1 - 3.6	+/- 1.9	15.2	-40~125	•				
74AUP1T45	single dual-supply voltage level translating transceiver (3-state)	1.1 - 3.6	+/- 1.9	15.6	-40~125		•			
74AUP1T50	single supply translating Schmitt-Trigger Buffer	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T86	2-input single supply translating X-OR gate	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AUP1T87	2-input single supply translating X-NOR gate	2.3 - 3.6	+/- 1.9	8.7	-40~125	•				
74AVC1T1004	dual supply clock fan-out buffer	0.8 - 3.6	+/- 12	4.9	-40~125					•
74AVC1T1022	dual supply clock fan-out buffer	0.8 - 3.6	+/- 12	4	-40~125					•

## Voltage translators/level-shifters (continued)

Voltage translators/level-shifters (continued)						5-pin	6-pin	8-pin		10-pin
Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	SOT353-1 (GW)	SOT363 (GW)	SOT505-2 (DP)	SOT765-1 (DC)	SOT552-1 (DP)
74AVC1T45	single dual-supply voltage level translating transceiver (3-state)	0.8 - 3.6	+/- 12	2.1	-40~125		•			
74AVC2T45	dual-bit dual-supply voltage level translating transceiver (3-state)	0.8 - 3.6	+/- 12	2.1	-40~125			•	•	
74AVC9112	single supply clock fan-out buffer	0.8 - 3.6	+/- 12	4.9	-40~125				•	
74AVCH1T45	single dual-supply voltage translating transceiver with bus hold (3-state)	0.8 - 3.6	+/- 12	2.1	-40~125		•			
74AVCH2T45	dual-bit dual-supply voltage translating transceiver with bus hold (3-state)	0.8 - 3.6	+/- 12	2.1	-40~125				•	
74AXP1T125	single dual-supply translating buffer (3-state)	0.7 - 2.75	+/- 12	4.7	-40~125		•			
74AXP1T14	dual-supply Schmitt-trigger inverter	0.7 - 2.75	+/- 12	4.9	-40~125	•				
74AXP1T32	single dual-supply translating 2-input OR-gate	0.7 - 2.75	+/- 12	4.6	-40~125		•			
74AXP1T34	single dual-supply translating buffer	0.7 - 2.75	+/- 12	4.7	-40~125	•				
74AXP1T57	single dual-supply translating configurable gate; Schmitt-trigger inputs	0.7 - 2.75	+/- 12	4.8	-40~125				•	
74AXP2T08	dual-supply dual translating 2-input AND gate	0.7 - 2.75	+/- 12	4.6	-40~125					•
74AXP2T3407	dual-supply translating buffer and buffer with open-drain	0.7 - 2.75	+/- 12	4.2	-40~125				•	
74LV1T00	2-input single supply translating NAND gate	1.6 - 5.5	+/- 8	14.4	-40~125	•				
74LV1T02	2-input single supply translating NOR gate	1.6 - 5.5	+/- 8	14.3	-40~125	•				
74LV1T04	single supply translating inverter	1.6 - 5.5	+/- 8	13.5	-40~125	•				
74LV1T08	2-input single supply translating AND gate	1.6 - 5.5	+/- 8	13.4	-40~125	•				
74LV1T125	single supply translating buffer (3-state)	1.6 - 5.5	+/- 8	14.7	-40~125	•				
74LV1T126	single supply translating buffer (3-state)	1.6 - 5.5	+/- 8	13.2	-40~125	•				
74LV1T32	2-input single supply translating OR gate	1.6 - 5.5	+/- 8	13.3	-40~125	•				
74LV1T34	single supply translating buffer	1.6 - 5.5	+/- 8	12.8	-40~125	•				
74LV1T86	2-input single supply translating X-OR gate	1.6 - 5.5	+/- 8	15.8	-40~125	•				
74LV1T87	2-input single supply translating X-NOR gate	1.6 - 5.5	+/- 8	15.8	-40~125	•				
74LVC1T45	single dual-supply voltage level translating transceiver (3-state)	1.2 - 5.5	+/- 24	2.5	-40~125		•			
74LVC2T45	dual-bit dual-supply voltage level translating transceiver (3-state)	1.2 - 5.5	+/- 24	2.5	-40~125				•	
74LVCH1T45	single dual-supply voltage translating transceiver with bus hold (3-state)	1.2 - 5.5	+/- 24	2.5	-40~125		•			
74LVCH2T45	dual-bit dual-supply voltage level translating transceiver with bus hold (3-state)	1.2 - 5.5	+/- 24	2.5	-40~125				•	



## Analog switches

Type number	Description	V <sub>CC</sub> (V)	R <sub>ON</sub> (Ω)	F <sub>(-3dB)</sub> (MHz)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin		10-pin
						SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DP)	SOT765-1 (DC)	SOT552-1 (DP)
74AHC1G66	Single-pole single-throw analog switch	2.0 - 5.5	40	280	-40~125	•	•					
74AHCT1G66	Single-pole single-throw analog switch	4.5 - 5.5	40	280	-40~125	•	•					
74HC1G66	Single-pole single-throw analog switch	2.0 - 9.0	105	200	-40~125	•	•					
74HC2G66	Dual single-pole single-throw analog switch	2.0 - 9.0	105	200	-40~125					•	•	
74HCT1G66	Single-pole single-throw analog switch	4.5 - 5.5	118	180	-40~125	•	•					
74HCT2G66	Dual single-pole single-throw analog switch	4.5 - 5.5	118	180	-40~125					•	•	
74LVC1G3157	2-channel analog multiplexer/demultiplexer	1.65 - 5.5	15	300	-40~125			•	•			
74LVC1G384	Single-pole single-throw analog switch	1.65 - 5.5	15	440	-40~125	•	•					
74LVC1G53	2-channel analog multiplexer/demultiplexer	1.65 - 5.5	15	300	-40~125					•	•	
74LVC1G66	Single-pole single-throw analog switch	1.65 - 5.5	15	440	-40~125	•	•					
74LVC2G3157	2-channel analog multiplexer/demultiplexer	1.65 - 5.5	15	300	-40~125							•
74LVC2G53	2-channel analog multiplexer/demultiplexer	1.65 - 5.5	15	300	-40~125					•	•	
74LVC2G66	Dual single-pole single-throw analog switch	1.65 - 5.5	15	440	-40~125					•	•	
74LVCV2G66	Dual single-pole single-throw analog switch	2.3 - 5.5	15	210	-40~125						•	

## Bus switches

Type number	Description	V <sub>CC</sub> (V)	R <sub>ON</sub> (Ω)	F <sub>(-3dB)</sub> (MHz)	T <sub>amb</sub> (°C)	5-pin		8-pin
						SOT353-1 (GW)	SOT753 (GV)	SOT765-1 (DC)
74CBTLV1G125	single bus switch	2.3 - 3.6	7	400	-40~125	•	•	
74CBTLV3306	2-bit bus switch	2.3 - 3.6	7	400	-40~125			•

## Decoders/demultiplexers

Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	6-pin	
						SOT363 (GW)	SOT457 (GV)
74AUP1G18	1-to-2 demultiplexer (3-state)	1.1 - 3.6	+/-1.9	3.2	-40~125	•	
74AUP1G19	1-to-2 decoder/demultiplexer	1.1 - 3.6	+/-1.9	3	-40~125	•	
74LVC1G18	1-to-2 demultiplexer (3-state)	1.65 - 5.5	+/- 32	2.3	-40~125	•	•
74LVC1G19	1-to-2 decoder/demultiplexer	1.65 - 5.5	+/- 32	1.8	-40~125	•	•

## Digital multiplexers

Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	6-pin		8-pin
						SOT363 (GW)	SOT457 (GV)	SOT765-1 (DC)
74AUP1G157	single 2-input multiplexer	1.1 - 3.6	+/-1.9	3.2	-40~125	•		
74AUP1G158	single 2-input multiplexer; inverting	1.1 - 3.6	+/-1.9	3.2	-40~125	•		
74AUP2G157	single 2-input multiplexer	1.1 - 3.6	+/-1.9	3.4	-40~125			•
74LVC1G157	single 2-input multiplexer	1.65 - 5.5	+/- 32	2.2	-40~125	•	•	

## Latches/registered drivers

							6-pin
Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	SOT363 (GW)	
74AUP1G373	single D-type transparent latch (3-state)	1.1 - 3.6	+/-1.9	8.5	-40~125	•	

## Flip-flops

							5-pin		6-pin		8-pin	
Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DP)	SOT765-1 (DC)	
74AHC1G79	single D-type flip-flop; positive-edge trigger	2.0 - 5.5	+/- 8	3.5	-40~125	•	•					
74AHCT1G79	single D-type flip-flop; positive-edge trigger; TTL enabled	4.5 - 5.5	+/- 8	3.5	-40~125	•	•					
74AUP1G175	single D flip-flop with reset; positive-edge trigger	1.1 - 3.6	+/- 1.9	7.4	-40~125			•				
74AUP1G374	single D-type flip-flop; positive-edge trigger (3-state)	1.1 - 3.6	+/- 1.9	7.9	-40~125			•				
74AUP1G74	single D-type flip-flop with set and reset; positive-edge trigger	1.1 - 3.6	+/- 1.9	9.2	-40~125						•	
74AUP1G79	single D-type flip-flop; positive-edge trigger	1.1 - 3.6	+/- 1.9	9.1	-40~125	•	•					
74AUP1G80	single D-type flip-flop; positive-edge trigger	1.1 - 3.6	+/- 1.9	9.1	-40~125	•						
74AUP2G79	dual D-type flip-flop; positive-edge trigger	1.1 - 3.6	+/- 1.9	8.5	-40~125						•	
74AUP2G80	dual D-type flip-flop; positive-edge trigger	1.1 - 3.6	+/- 1.9	9.1	-40~125						•	
74LVC1G175	single D flip-flop with reset; positive-edge trigger	1.65 - 5.5	+/- 32	3.1	-40~125			•	•			
74LVC1G74	single D-type flip-flop with set and reset; positive-edge trigger	1.65 - 5.5	+/- 32	3.5	-40~125					•	•	
74LVC1G79	single D-type flip-flop; positive-edge trigger	1.65 - 5.5	+/- 32	2.2	-40~125	•	•					
74LVC1G80	single D-type flip-flop; positive-edge trigger	1.65 - 5.5	+/- 32	2.4	-40~125	•	•					
74LVC2G74	single D-type flip-flop with set and reset; positive-edge trigger	1.65 - 5.5	+/- 32	3.5	-40~125					•	•	

## Counters/frequency dividers

							5-pin
Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	SOT353-1 (GW)	
74AHC1G4210	10-stage divider and oscillator	2.0 - 5.5	+/- 8	17	-40~125	•	
74AHC1G4212	12-stage divider and oscillator	2.0 - 5.5	+/- 8	20	-40~125	•	
74AHC1G4214	14-stage divider and oscillator	2.0 - 5.5	+/- 8	23	-40~125	•	

## Multivibrators

							8-pin	
Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	SOT505-2 (DP)	SOT765-1 (DC)	
74LVC1G123	single retriggerable monostable multivibrator	1.65 - 5.5	+/- 32	3.5	-40~125	•	•	

## Gates

Type number	Description	V <sub>cc</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin		10-pin
						SOT1353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT1505-2 (DP)	SOT765-1 (DC)	SOT1552-1 (DP)
74AHC1G00	single 2-input NAND gate	2.0 - 5.5	+/- 8	3.5	-40~125	•	•					
74AHC1G02	single 2-input NOR gate	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
74AHC1G08	single 2-input AND gate	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
74AHC1G09	single 2-input AND gate; open-drain	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
74AHC1G32	single 2-input OR gate	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
74AHC1G86	2-input EXCLUSIVE-OR gate	2.0 - 5.5	+/- 8	3.4	-40~125	•	•					
74AHC2G00	dual 2-input NAND gate	2.0 - 5.5	+/- 8	3.5	-40~125					•	•	
74AHC2G08	dual 2-input AND gate	2.0 - 5.5	+/- 8	3.2	-40~125					•	•	
74AHC2G32	dual 2-input OR gate	2.0 - 5.5	+/- 8	3.2	-40~125					•	•	
74AHCT1G00	single 2-input NAND gate; TTL enabled	4.5 - 5.5	+/- 8	3.6	-40~125	•	•					
74AHCT1G02	single 2-input NOR gate; TTL enabled	4.5 - 5.5	+/- 8	3.5	-40~125	•	•					
74AHCT1G08	single 2-input AND gate; TTL enabled	4.5 - 5.5	+/- 8	3.6	-40~125	•	•					
74AHCT1G32	single 2-input OR gate	4.5 - 5.5	+/- 8	3.3	-40~125	•	•					
74AHCT1G86	2-input EXCLUSIVE-OR gate; TTL enabled	4.5 - 5.5	+/- 8	3.5	-40~125	•	•					
74AHCT2G00	dual 2-input NAND gate; TTL enabled	4.5 - 5.5	+/- 8	3.6	-40~125						•	
74AHCT2G08	dual 2-input AND gate; TTL enabled	4.5 - 5.5	+/- 8	3.6	-40~125					•	•	
74AHCT2G32	dual 2-input OR gate	4.5 - 5.5	+/- 8	3.3	-40~125					•	•	
74AUP1G00	single 2-input NAND gate	1.1 - 3.6	+/- 1.9	8.3	-40~125	•						
74AUP1G02	single 2-input NOR gate	1.1 - 3.6	+/- 1.9	8.3	-40~125	•						
74AUP1G08	single 2-input AND gate	1.1 - 3.6	+/- 1.9	8.2	-40~125	•						
74AUP1G0832	single 3-input AND-OR gate	1.1 - 3.6	+/- 1.9	6.7	-40~125			•				
74AUP1G09	single 2-input AND gate; open-drain	1.1 - 3.6	1.9	8.5	-40~125	•						
74AUP1G11	single 3-input AND gate	1.1 - 3.6	+/- 1.9	6.9	-40~125			•				
74AUP1G32	single 2-input OR gate	1.1 - 3.6	+/- 1.9	7.9	-40~125	•						
74AUP1G3208	single 3-input OR-AND gate	1.1 - 3.6	+/- 1.9	7.4	-40~125			•				
74AUP1G332	single 3-input OR gate	1.1 - 3.6	+/- 1.9	6.8	-40~125			•				
74AUP1G38	single 2-input NAND gate; open-drain	1.1 - 3.6	1.9	8.5	-40~125	•						
74AUP1G386	single 3-input EXCLUSIVE-OR gate	1.1 - 3.6	+/- 1.9	8.6	-40~125			•				
74AUP1G57	configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1G58	configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1G86	single 2-input EXCLUSIVE-OR gate	1.1 - 3.6	+/- 1.9	9	-40~125	•						
74AUP1G885	dual function gate	1.1 - 3.6	+/- 1.9	7.6	-40~125						•	
74AUP1G97	configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1G98	configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.9	-40~125			•				
74AUP1T57	configurable gate with voltage level translation	2.3 - 3.6	+/- 1.9	8.7	-40~125			•				

## Gates (continued)

Type number	Description	V <sub>cc</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin		10-pin
						SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DP)	SOT765-1 (DC)	SOT552-1 (DP)
74AUP1T58	configurable gate with voltage level translation	2.3 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1T97	configurable gate with voltage level translation	2.3 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1T98	configurable gate with voltage level translation	2.3 - 3.6	+/- 1.9	8.7	-40~125			•				
74AUP1Z04	crystal driver with enable and internal resistor	1.1 - 3.6	+/- 1.9	5.6	-40~125			•				
74AUP1Z125	crystal driver with enable and internal resistor (3-state)	1.1 - 3.6	+/- 1.9	4.7	-40~125			•				
74AUP2G00	dual 2-input NAND gate	1.1 - 3.6	+/- 1.9	8.3	-40~125						•	
74AUP2G02	dual 2-input NOR gate	1.1 - 3.6	+/- 1.9	8.3	-40~125						•	
74AUP2G08	dual 2-input AND gate	1.1 - 3.6	+/- 1.9	8.2	-40~125						•	
74AUP2G32	dual 2-input OR gate	1.1 - 3.6	+/- 1.9	7.9	-40~125						•	
74AUP2G3404	buffer and inverter	1.1 - 3.6	+/- 1.9	4	-40~125			•				
74AUP2G3407	buffer and buffer with open-drain	1.1 - 3.6	+/- 1.9	4.1	-40~125			•				
74AUP2G38	dual 2-input NAND gate; open-drain	1.1 - 3.6	1.9	8.5	-40~125						•	
74AUP2G57	dual configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125							•
74AUP2G58	dual configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125							•
74AUP2G86	dual 2-input EXCLUSIVE-OR gate	1.1 - 3.6	+/- 1.9	9	-40~125						•	
74AUP2G97	dual configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.7	-40~125							•
74AUP2G98	dual configurable gate; Schmitt trigger	1.1 - 3.6	+/- 1.9	8.9	-40~125							•
74HC1G00	single 2-input NAND gate	2.0 - 6.0	+/- 2.6	7	-40~125	•	•					
74HC1G02	single 2-input NOR gate	2.0 - 6.0	+/- 2.6	7	-40~125	•	•					
74HC1G08	single 2-input AND gate	2.0 - 6.0	+/- 5.2	7	-40~125	•	•					
74HC1G32	single 2-input OR gate	2.0 - 6.0	+/- 2.6	8	-40~125	•	•					
74HC1G86	single 2-input EXCLUSIVE-OR gate	2.0 - 6.0	+/- 2.6	9	-40~125	•	•					
74HC2G00	dual 2-input NAND gate	2.0 - 6.0	+/- 5.2	9	-40~125					•	•	
74HC2G02	dual 2-input NOR gate	2.0 - 6.0	+/- 5.2	9	-40~125					•	•	
74HC2G08	dual 2-input AND gate	2.0 - 6.0	+/- 5.2	9	-40~125					•	•	
74HC2G32	dual 2-input OR gate	2.0 - 6.0	+/- 5.2	9	-40~125					•	•	
74HC2G86	dual 2-input EXCLUSIVE-OR gate	2.0 - 6.0	+/- 5.2	9	-40~125					•	•	
74HCT1G00	single 2-input NAND gate; TTL enabled	4.5 - 5.5	+/- 2	10	-40~125	•	•					
74HCT1G02	single 2-input NOR gate; TTL enabled	4.5 - 5.5	+/- 2	9	-40~125	•	•					
74HCT1G08	single 2-input AND gate; TTL enabled	4.5 - 5.5	+/- 2	11	-40~125	•	•					
74HCT1G32	single 2-input OR gate; TTL enabled	4.5 - 5.5	+/- 2	10	-40~125	•	•					
74HCT1G86	single 2-input EXCLUSIVE-OR gate; TTL enabled	4.5 - 5.5	+/- 2	10	-40~125	•	•					
74HCT2G00	dual 2-input NAND gate; TTL enabled	4.5 - 5.5	+/- 4	12	-40~125					•	•	
74HCT2G02	dual 2-input NOR gate; TTL enabled	4.5 - 5.5	+/- 4	12	-40~125					•	•	
74HCT2G08	dual 2-input AND gate; TTL enabled	4.5 - 5.5	+/- 4	14	-40~125					•	•	

## Gates (continued)

Type number	Description	V <sub>CC</sub> (V)	I <sub>o</sub> (mA)	t <sub>pd</sub> (ns)	T <sub>amb</sub> (°C)	5-pin		6-pin		8-pin		10-pin
						SOT353-1 (GW)	SOT753 (GV)	SOT363 (GW)	SOT457 (GV)	SOT505-2 (DP)	SOT765-1 (DC)	SOT552-1 (DP)
74HCT2G32	dual 2-input OR gate; TTL enabled	4.5 - 5.5	+/- 4	13	-40~125					•	•	
74HCT2G86	dual 2-input EXCLUSIVE-OR gate; TTL enabled	4.5 - 5.5	+/- 4	11	-40~125						•	
74LVC104	crystal driver	1.65 - 5.5	+/- 24	2.8	-40~125			•	•			
74LVC1G00	single 2-input NAND gate	1.65 - 5.5	+/- 32	2.2	-40~125	•	•					
74LVC1G02	single 2-input NOR gate	1.65 - 5.5	+/- 32	2.1	-40~125	•	•					
74LVC1G08	single 2-input AND gate	1.65 - 5.5	+/- 24	2.1	-40~125	•	•					
74LVC1G10	single 3-input NAND gate	1.65 - 5.5	+/- 32	2.6	-40~125			•	•			
74LVC1G11	single 3-input AND gate	1.65 - 5.5	+/- 32	2.6	-40~125			•	•			
74LVC1G27	single 3-input NOR gate	1.65 - 5.5	+/- 32	2.6	-40~125			•	•			
74LVC1G32	single 2-input OR gate	1.65 - 5.5	+/- 32	2.1	-40~125	•	•					
74LVC1G332	single 3-input OR gate	1.65 - 5.5	+/- 32	2.6	-40~125			•	•			
74LVC1G38	single 2-input NAND gate; open-drain	1.65 - 5.5	32	2.3	-40~125	•	•					
74LVC1G386	single 3-Input EXCLUSIVE-OR gate	1.65 - 5.5	+/- 32	4.5	-40~125			•	•			
74LVC1G57	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	6.3	-40~125			•	•			
74LVC1G58	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	6.3	-40~125			•	•			
74LVC1G86	single 2-input EXCLUSIVE-OR gate	1.65 - 5.5	+/- 32	2.4	-40~125	•	•					
74LVC1G97	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	6.3	-40~125			•	•			
74LVC1G98	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	6.3	-40~125			•	•			
74LVC1G99	configurable gate; Schmitt trigger	1.65 - 5.5	+/- 32	8.4	-40~125					•		
74LVC2G00	dual 2-input NAND gate	1.65 - 5.5	+/- 32	2.2	-40~125					•	•	
74LVC2G02	dual 2-input NOR gate	1.65 - 5.5	+/- 32	2.4	-40~125					•	•	
74LVC2G08	dual 2-input AND gate	1.65 - 5.5	+/- 24	2.1	-40~125					•	•	
74LVC2G32	dual 2-input OR gate	1.65 - 5.5	+/- 32	2.2	-40~125					•	•	
74LVC2G38	dual 2-input NAND gate; open-drain	1.65 - 5.5	32	2.1	-40~125					•	•	
74LVC2G86	dual 2-input EXCLUSIVE-OR gate	1.65 - 5.5	+/- 32	2.3	-40~125					•	•	
XC7SET02	single 2-input NOR gate; TTL enabled	4.5 - 5.5	+/- 8	3.5	-40~125	•	•					
XC7SET08	single 2-input AND gate; TTL enabled	4.5 - 5.5	+/- 8	3.6	-40~125	•	•					
XC7SET32	single 2-input OR gate; TTL enabled	4.5 - 5.5	+/- 8	3.3	-40~125	•	•					
XC7SET86	2-input EXCLUSIVE-OR gate; TTL enabled	4.5 - 5.5	+/- 8	3.5	-40~125	•	•					
XC7SH02	single 2-input NOR gate	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
XC7SH08	single 2-input AND gate	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
XC7SH32	single 2-input OR gate	2.0 - 5.5	+/- 8	3.2	-40~125	•	•					
XC7SH86	2-input EXCLUSIVE-OR gate	2.0 - 5.5	+/- 8	3.4	-40~125	•	•					





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**Date of release:**

January 2019

**Printed:**

In the Netherlands