Dynamically create your own combination logic device
Dual PCB configurable logic

One device =

**Dual PCB configurable logic places
two sets of configurable gates into a single package**

Seven unique functions

X

Seven unique functions

Each set of gates is individually configurable (on the PCB)
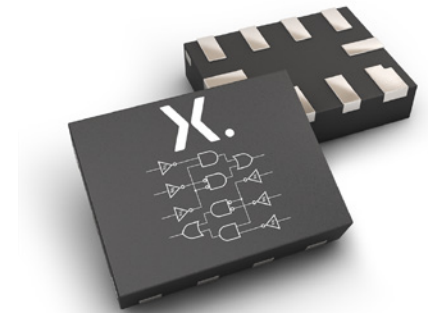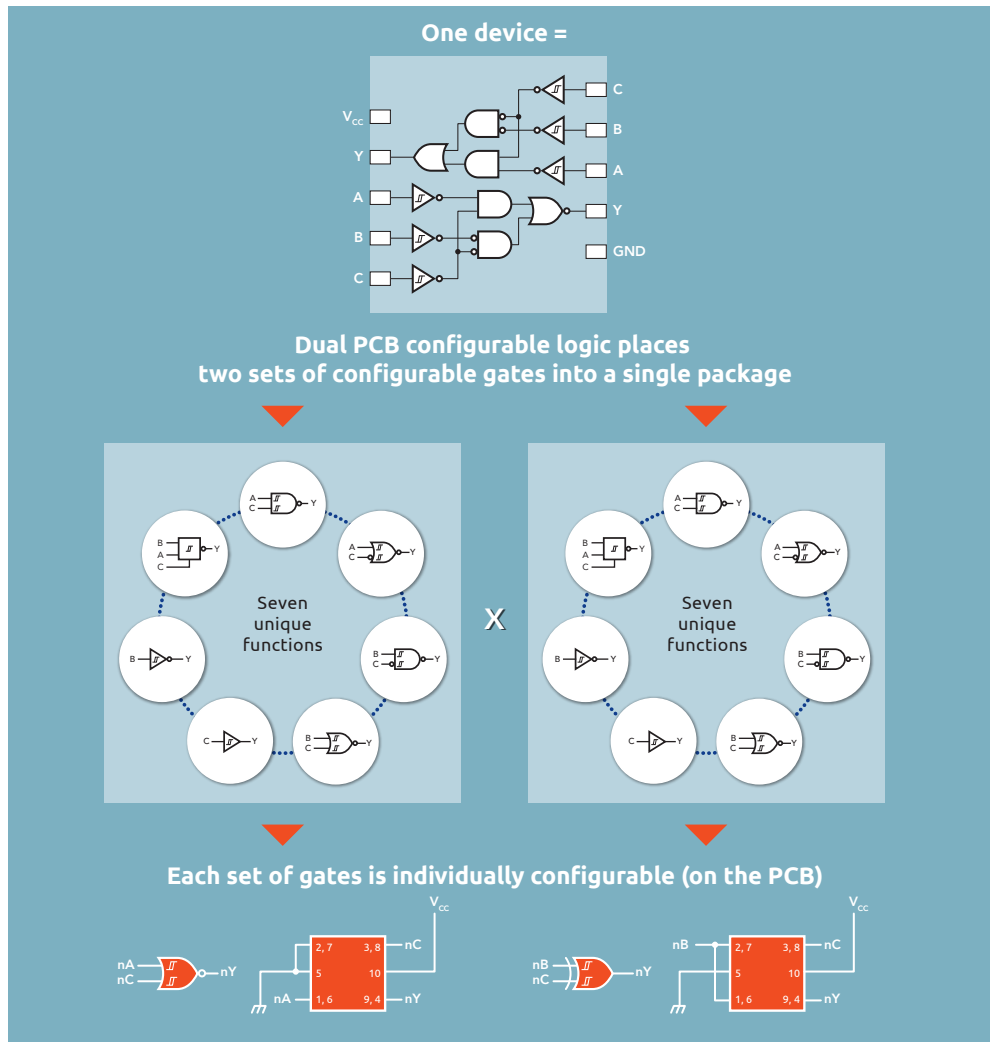
Benefits

Design flexibility
› Large selection standard and non-standard functions
› Up to 49 different configuration options available in one package

Systems level savings
› Up to a 20% reduction in static power
› Up to a 20% reduction in space savings

Features
› Wide supply voltage range: 0.8 V to 3.6 V
› Low static power consumption: I_{CC} = 0.9µA (max)
› I_{OFF} circuitry enables sub-module partial power down mode operation

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The savings are endless **with dual PCB configurable logic**

Each family device (-57/-58/-97/-98) implements two independent copies of the PCB configurable logic gates into one package

Part number	Available functions						
74AUP2G57	B C → Y	B C → Y	A C → Y	A C → Y	B C → Y	B → Y	A → Y
74AUP2G58	B C → Y	B C → Y	A C → Y	A C → Y	B C → Y	B → Y	A → Y
74AUP2G97	A C → Y	B C → Y	A C → Y	B A C → Y	B C → Y	B → Y	A → Y
74AUP2G98	A C → Y	B C → Y	A C → Y	B A C → Y	B C → Y	B → Y	A → Y

Package suffix	DP	GU
	10-pin (TSSOP)	10-pin (XQFN)
Package	SOT552-1	SOT1160-1
Width x length x height, minimum pin spacing (pitch) (dimensions in mm)	3.0 x 4.9 x 1.1, 0.5	1.4 x 1.8 x 0.5, 0.4

For product datasheets, additional information, and the latest list of released functions please visit:
www.nexperia.com/products/logic/gates/configurable-gates/

nexperia.com

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E F F I C I E N C Y   W I N S.