

Quarterly Reliability Monitoring Results

Quarters: Q1/2021 to Q4/2021

Based on structural similarity

Supplier		User Part Number						
Nexperia B.V. Name of Laboratory Assembly reliability labs Based on AEC-Q101 Test		2PD601ARW Part Description						
								Nexperia DHAM
		SMD package						
		Test Conditions	Duration	# Lots	# Quantity	# Rejects		
			TEST					
	Pre- and Post-Stress							
# E1	Electrical Test	Tamb = 25 °C	N/A	see below	all parts	see below		
	РС	JESD22-A113 Bake Tamb = 125 °C Soak Tamb = 85 °C, RH = 85%	24 hours 168 hours					
# A1	Preconditioning	Reflow soldering	3 cycles	849	61170	0		
# B1	HTRB High Temperature Reverse Bias	MIL-STD-750-1 M1039 Method A Tj = Tjmax, Vr = 100% of max. datasheet reverse voltage	1000 hours	202	16160	0		
		-				-		
# A4	TC Temperature Cycling	JESD22-A104 -65 °C to Tjmax, not to exceed 150°C	1000 cycles	171	13680	0		
# A3 alt	AC Autoclave	JESD22-A102 Tamb = 121 °C, RH = 100 % Pressure = 205 kPa (29.7 psia)	96 hours	173	13840	0		
	H3TRB High Humidity High	JESD22-A101 Tamb = 85 °C, RH = 85%, VR = 80 % of						
# A2 alt	Temperature Reverse Bias	-	1000 hours	173	13840	0		
	IOL Intermittent Operating Life	MIL-STD-750 Method 1037 ton = toff, devices powered to insure $\Delta T j =$		407	15760	0		
# A5	Internittent Operating Life	100 °C 101 13000 Cycles	1000 hours	197	15760	0		
# C8	RSH Resistance to Solder Heat	JESD22-A111 260 °C ± 5 °C	10 s	135	4050	0		
# C0	SD	200 0 - 5 0	10.2	132	4030	U		
# C10	SD Solderability	J-STD-002		342	3420	0		

[1] The maximum applied voltage is limited by test chamber set up and does not exceed 115V.

Calculation of FIT and MTTF

Test considered for FIT calculation: High Temperature Reverse Bias (HTRB, Test #B1) Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

Wafer Fab	Technology	Quantity	Rejects	Failure Rate (FIT)	MTTF (hrs)
Nexperia DHAM	Small Signal Bipolar Transistor	16160	0	0.26	3.81E+09

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