

Reliability Monitoring Results

Quarters: Q1/2021 to Q4/2021 Based on structural similarity

Supplier **User Part Number**

Nexperia B.V. 74HCT166D

Part Description: 8-bit parallel or serial-in/serial-out shift register; TTL enabled

Function Family: HC(T) Process family: Super micron Package family: SO

JESD47	' Test	Test Conditions	Duration	# Lots	# Quantity	# Rejects
<i>4</i> .4	TEST	Tb 25.00	NI/A		-11	see
# 1	Pre- and Post-Stress Electrical Test	Tamb = 25 °C	N/A	see below	all parts	below
# 2	PC	JESD22-A113	N/A	378	26431	0
	Preconditioning	MSL 1	IN/A			
	HTOL EFR	JESD22-A108	48 hours			
# 5a	High Temperature	Tj = 150°C	or	58	23674	0
	Operating Life Extrinsic	$V_{CCMAX} \le V \le 1.2*V_{CCMAX}$	168 hours			
	HTOL IFR	JESD22-A108				
# 5b	High Temperature	Tj = 150°C	≥500 hours	55	2997	0
	Operating Life Intrinsic	$V_{CCMAX} \le V \le 1.2*V_{CCMAX}$				
# 7	TC	JESD22-A104	≥500 cycles	49	15770	4
	Temperature Cycling	-65 °C to 150°C	≥300 Cycles			
	uHAST / HAST	JESD22-A101				
# 9	unbiased or biased High	Tamb = 130 °C,	96 hours	187	10661	0
	Accelerated Stress Test	$RH = 85\%$, $V = V_{CCMAX}$				

Calculation of PPM, FIT and MTTF

Test considered for PPM calculation: High Temperature Operating LifeTest Extrinsic (HTOL EFR, Test # 5a above) Test considered for FIT and MTTF calculations: High Temperature Operating LifeTest Intrinsic(HTOL IFR, Test # 5b above)

Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

Product Family	Package Family	Quantity	Rejects	Extrinsic Failure Rate (PPM)	Intrinsic Failure Rate (FIT)	MTTF (hrs)
HC(T)	SO	2997	0	39	1.3	8.12 E+08