

Reliability Monitoring Results

Quarters: Q1/2021 to Q4/2021 Based on structural similarity

Supplier User Part Number

Nexperia B.V. 74LVCU04AD Part Description: Hex inverter; unbuffered

Function Family: LVC Process family: Sub micron Package family: SO

JESD47 Test Test Conditions # Quantity **Duration** # Lots Rejects **TEST** see # 1 Pre- and Post-Stress Tamb = 25 °C N/A see below all parts below **Electrical Test** JESD22-A113 PC # 2 378 26431 0 N/A Preconditioning MSL 1 **HTOL EFR** JESD22-A108 48 hours # 5a High Temperature Ti = 150°C 356 51713 0 or Operating Life Extrinsic $V_{CCMAX} \le V \le 1.2*V_{CCMAX}$ 168 hours JESD22-A108 **HTOL IFR** # 5b High Temperature $T_j = 150$ °C ≥500 hours 134 9791 0 Operating Life Intrinsic $V_{CCMAX} \le V \le 1.2*V_{CCMAX}$ JESD22-A104 # 7 ≥500 cycles 49 15770 4 Temperature Cycling -65 °C to 150°C uHAST / HAST JESD22-A101 unbiased or biased High Tamb = $130 \, ^{\circ}$ C,

96 hours

187

10661

0

Calculation of PPM, FIT and MTTF

Accelerated Stress Test

9

Test considered for PPM calculation: High Temperature Operating LifeTest Extrinsic (HTOL EFR, Test # 5a above) Test considered for FIT and MTTF calculations: High Temperature Operating LifeTest Intrinsic(HTOL IFR, Test # 5b above)

Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

RH = 85%, $V = V_{CCMAX}$

Product Family	Package Family	Quantity	Rejects	Extrinsic Failure Rate (PPM)	Intrinsic Failure Rate (FIT)	MTTF (hrs)
LVC	SO	9791	0	18	0.5	2.22 E+09