

## **Quarterly Reliability Monitoring Results**

## Quarters: Q1/2021 to Q4/2021

Based on structural similarity

Supplier Nexperia B.V.		User Part Number						
		BC857AM						
Name of Laboratory  Assembly reliability labs  Based on AEC-Q101 Test		Part Description						
		Nexperia DHAM Small Signal Bipolar Transistor						
		MCD package						
		Test Conditions	Duration	# Lots	# Quantity	# Rejects		
	TEST							
	Pre- and Post-Stress							
# E1	Electrical Test	Tamb = 25 °C	N/A	see below	all parts	see below		
		JESD22-A113						
	<b>DC</b>	Bake Tamb = 125 °C	24 hours					
	PC Preconditioning	Soak Tamb = 85 °C, RH = 85% Reflow soldering	168 hours 3 cycles			_		
# A1	Preconditioning		3 cycles	208	16640	0		
		MIL-STD-750-1						
	HTRB	M1039 Method A						
		Tj = Tjmax, Vr = 100% of max. datasheet reverse voltage				_		
# B1	Bias	reverse voitage	1000 hours	202	16160	0		
	тс	JECD22 A104						
# 44	Temperature Cycling	JESD22-A104 -65 °C to Timax, not to exceed 150°C	1000	F2	4160	0		
# A4	remperature Cycling	<u> </u>	1000 cycles	52	4160	0		
	AC	JESD22-A102 Tamb = 121 °C, RH = 100 %						
# A2 - It	AC Autoclave	Pressure = 205 kPa (29.7 psia)	96 hours	F2	4160	0		
# A3 alt	Autociave	riessule – 203 kra (23.7 psia)	96 nours	52	4160	0		
	H3TRB	JESD22-A101						
	High Humidity High	Tamb = 85 °C, RH = 85%, VR = 80 % of						
# A2 alt	Temperature Reverse Bias		1000 hours	52	4160	0		
# AZ dIL	remperature Reverse bias		1000 nours	52	4160	U		
	IOL	MIL-STD-750 Method 1037 ton = toff, devices powered to insure $\Delta T_j$ =						
# A5	Intermittent Operating Life		1000	52	41.60	0		
# A3	Intermittent Operating Life	100 C 101 15000 Cycles	1000 hours	32	4160	0		
	RSH	JESD22-A111						
# C8	Resistance to Solder Heat	260 °C ± 5 °C	10 s	n.a.	n.a.	n.a.		
# CO	SD	200 0 = 0 0	10.2	11.4.	11.4.	11.4.		
# C10	Solderability	J-STD-002		111	1110	0		
# CIU	Soluciusmicy	7 0.0 002		111	1110	U		

<sup>[1]</sup> The maximum applied voltage is limited by test chamber set up and does not exceed 115V.

## **Calculation of FIT and MTTF**

Test considered for FIT calculation: High Temperature Reverse Bias (HTRB, Test #B1)
Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

Wafer Fab	Technology	Quantity	Rejects	Failure Rate (FIT)	MTTF (hrs)
Nexperia	Small Signal Bipolar				
DHAM	Transistor	16160	0	0.26	3.81E+09

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