

Quarterly Reliability Monitoring Results

Quarters: Q1/2021 to Q4/2021

Based on structural similarity

Supplier Nexperia B.V. Name of Laboratory Assembly reliability labs		User Part Number PBSS5620PA Part Description											
									Nexperia DHAM Small Signal Bipolar Transistor				
									MCD package				
		Test		Test Conditions	Duration	# Lots	# Quantity	# Rejects					
		F	FEST Pre- and Post-Stress										
# 1 E	Electrical Test	Tamb = 25 °C	N/A	see below	all parts	see below							
	PC Preconditioning	JESD22-A113 Bake Tamb = 125 °C Soak Tamb = 85 °C, RH = 85% Reflow soldering	24 hours 168 hours 3 cycles	208	16640	0							
ŀ	HTRB High Temperature Reverse Bias	MIL-STD-750-1 M1039 Method A Tj = Tjmax, $Vr = 100\%$ of max. datasheet reverse voltage	1000 hours	202	16160	0							
	FC Temperature Cycling	JESD22-A104 -65 °C to Tjmax, not to exceed 150°C	500 cycles	52	4160	0							
	AC Autoclave	JESD22-A102 Tamb = 121 °C, RH = 100 % Pressure = 205 kPa (29.7 psia)	96 hours	52	4160	0							
ŀ	H3TRB High Humidity High Femperature Reverse Bias	JESD22-A101 Tamb = 85 °C, RH = 85%, VR = 80 % of rated reverse voltage $^{[1]}$	1000 hours	52	4160	0							
_	OL Intermittent Operating Life	MIL-STD-750 Method 1037 ton = toff, devices powered to insure $\Delta Tj = 100$ °C for 15000 cycles	1000 hours	52	4160	0							
# 20 F	RSH Resistance to Solder Heat	JESD22-A111 260 °C ± 5 °C	10 s	n.a.	n.a.	n.a.							
_	SD Solderability	J-STD-002		111	1110	0							

^[1] The maximum applied voltage is limited by test chamber set up and does not exceed 115V.

Calculation of FIT and MTTF

Test considered for FIT calculation: High Temperature Reverse Bias (HTRB, Test # 5) Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

Wafer Fab	Technology	Quantity	Rejects	Failure Rate (FIT)	MTTF (hrs)
Nexperia	Small Signal Bipolar				
DHAM	Transistor	16160	0	0.26	3.81E+09

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