nexperia

Quarterly Reliability Monitoring Results

Quarters: Q1/2021 to Q4/2021

Based on structura	similarity
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tory y labs	PESD1V2Y1BSF Part Description NXP ICN8 PROVENTION	Protoction INC						
	NXP ICN8	Protection INC						
y labs		Protection IND	Part Description					
y labs		NXP ICN8 Protection INDI						
	BD package							
	Test Conditions	Duration	# Lots	# Quantity	# Rejects			
ST					-			
e- and Post-Stress								
ectrical Test	Tamb = 25 °C	N/A	see below	all parts	see below			
	MIL-STD-750-1							
RB	M1038 Method A							
gh Temperature Reverse	Tj = Tjmax, Vr = 100% of max. datasheet							
as	reverse voltage	1000 hours	43	3440	0			
:	JESD22-A104							
mperature Cycling	-40 °C to 125°C	1000 cycles	67	5360	0			
	JESD22-A102							
2	Tamb = 121 °C, RH = 100 %							
toclave	Pressure = 205 kPa (29.7 psia)	96 hours	n.a.	n.a.	n.a.			
AST	JESD22-A110							
ahly Accelerated Stress	Tamb = 130 °C, RH = 85%, VR = 80 % of							
st	rated reverse voltage ^[1]	1000 hours	67	5360	0			
	MIL-STD-750 Method 1037							
L	ton = toff, devices powered to insure ΔT_j =							
	100 °C for 15000 cycles	1000 hours	n.a.	n.a.	n.a.			
. 5	·							
6H	JESD22-A111							
sistance to Solder Heat	260 °C ± 5 °C	10 s	n.a.	n.a.	n.a.			
)		20 0						
, Iderability	1-STD-002		12	120	0			
sista)		ance to Solder Heat 260 °C ± 5 °C	ance to Solder Heat 260 °C ± 5 °C 10 s ability J-STD-002	ance to Solder Heat 260 °C ± 5 °C 10 s n.a. ability J-STD-002 12	ance to Solder Heat 260 °C ± 5 °C 10 s n.a. n.a.			

[1] The maximum applied voltage is limited by test chamber set up and does not exceed 115V.

Calculation of FIT and MTTF

Test considered for FIT calculation: High Temperature Reverse Bias (HTRB, Test # 5)

Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

Wafer Fab	Technology	Quantity	Rejects	Failure Rate (FIT)	MTTF (hrs)
NXP ICN8	Protection INDI	3440	0	1.2	8.10E+08

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