

Quarterly Reliability Monitoring Results

Quarters: Q1/2021 to Q4/2021

Based on structural similarity

Supplier		User Part Number						
Nexperia B.V. Name of Laboratory Assembly reliability labs Based on AEC-Q101 Test		PUMD19 Part Description						
								Nexperia DHAM Small Signal Bipolar Transistor SMD package
		Test Conditions	Duration	# Lots	# Quantity	# Rejects		
			TEST					
			Pre- and Post-Stress					
# E1	Electrical Test	Tamb = 25 °C	N/A	see below	all parts	see below		
		JESD22-A113						
		Bake Tamb = 125 °C	24 hours					
	PC Deconditioning	Soak Tamb = 85 °C, RH = 85%	168 hours					
# A1	Preconditioning	Reflow soldering	3 cycles	849	61170	0		
		MIL-STD-750-1						
	HTRB	M1039 Method A						
# D1	Bias	Tj = Tjmax, Vr = 100% of max. datasheet reverse voltage	1000 have	202	16160	0		
# B1	Dias		1000 hours	202	16160	0		
	тс	JESD22-A104						
# A4	Temperature Cycling	-65 °C to Tjmax, not to exceed 150°C	1000 cycles	171	13680	0		
		JESD22-A102	1000 cycles	1/1	15000	0		
	AC	Tamb = $121 ^{\circ}$ C, RH = $100 ^{\circ}$						
# A3 alt	Autoclave	Pressure = $205 \text{ kPa} (29.7 \text{ psia})$	96 hours	173	13840	0		
			Joniouro	1,0	100.0	0		
	H3TRB	JESD22-A101						
	High Humidity High	Tamb = 85 °C, RH = 85%, VR = 80 % of						
# A2 alt	Temperature Reverse Bias	rated reverse voltage ^[1]	1000 hours	173	13840	0		
		MIL-STD-750 Method 1037						
	IOL	ton = toff, devices powered to insure ΔT_j =						
# A5	Intermittent Operating Life	100 °C for 15000 cycles	1000 hours	197	15760	0		
	RSH	JESD22-A111						
# C8	Resistance to Solder Heat	260 °C ± 5 °C	10 s	135	4050	0		
	SD							
# C10	Solderability	J-STD-002		342	3420	0		

[1] The maximum applied voltage is limited by test chamber set up and does not exceed 115V.

Calculation of FIT and MTTF

Test considered for FIT calculation: High Temperature Reverse Bias (HTRB, Test #B1) Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

Wafer Fab	Technology	Quantity	Rejects	Failure Rate (FIT)	MTTF (hrs)
Nexperia DHAM	Small Signal Bipolar Transistor	16160	0	0.26	3.81E+09

© 2022 Nexperia B.V.

All information hereunder is per Nexperia's best knowledge. This document does not provide for any nexperia.com representation or warranty express or implied by Nexperia. In case Nexperia has tested the product, this documentation reflects the outcome of the analysis of the actually tested parts only.