ne<mark>x</mark>peria

Important notice

Dear Customer,

On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

Instead of <u>http://www.nxp.com</u>, <u>http://www.philips.com/</u> or <u>http://www.semiconductors.philips.com/</u>, use <u>http://www.nexperia.com</u>

Instead of sales.addresses@www.nxp.com or sales.addresses@www.semiconductors.philips.com, use **salesaddresses@nexperia.com** (email)

Replace the copyright notice at the bottom of each page or elsewhere in the document, depending on the version, as shown below:

- © NXP N.V. (year). All rights reserved or © Koninklijke Philips Electronics N.V. (year). All rights reserved

Should be replaced with:

- © Nexperia B.V. (year). All rights reserved.

If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia



Thermal RC network (Foster)

SPICE thermal model

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
th(j-mb)	thermal resistance from junction to mounting base		-	-	2.36	K/W
	Cth ₁	3.575E-05 F		Ą	t:	
	Cth ₂	1.981E-04 F		_ 	tj	
	Cth ₃	2.706E-04 F			<u></u>	
	Cth ₄	1.210E-03 F			Rth1 + Cth	1
	Cth ₅	4.930E-03 F		L	יייין דיי ני	1
	Cth ₆	5.951E-03 F				
	Cth ₇	1.357E-01 F		Г	$\begin{bmatrix} \bullet & \bullet \\ \bullet & \bullet \end{bmatrix}$	
	Cth ₈	6.011E+01 F			$\int Rth_2 + Cth$	2
	Rth₁	2.533E-03 Ω				
	Rth ₂	5.928E-03 Ω		Г	\neg	
	Rth ₃	3.577E-02 Ω			Rth3 茾 Cth	3
	Rth ₄	7.656Ε-02 Ω		L	╧╼╴╴	
	Rth ₅	2.422Ε-01 Ω				
	Rth ₆	1.553E+00 Ω			Rth4 📥 Cth	
	Rth ₇	4.394E-01 Ω				4
	Rth ₈	3.829E-03 Ω	((P)		
			Ň	\checkmark	└─ ◆───	
					$\int Rth_5 + Cth$	5
				[
					$\int Rth_6 + Cth$	6
						
				- r	۲	
					Rth7 📥 Cth	7
				L	┙	
Part:	BUK9K32-100E					0
ate:	20/3/2013					0
th	2.36 K/W	,				
				Ţ	t _{amb}	
				\checkmark	001aal76	8

www.nxp.com

© 2009 NXP B.V.

All rights reserved. Reproduction in whole or in part is prohibited without prior consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of it's use. Publication thereof does not convey or imply any license under patent- or other industrial or intellectual property rights.