

Using the ADC to Measure Supply Voltage

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Keywords

- ADC
- VDD
- Supply Voltage
- CC1110
- CC1111

- CC2430
- CC2431
- CC2510
- CC2511

1 Introduction

The above mentioned SoCs contain a feature for sampling VDD/3 using the ADC. By setting the ADC to sample

VDD/3 with 1.25 V as internal voltage reference, VDD can easily be calculated.

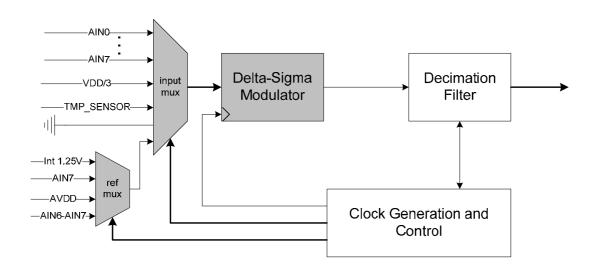


Figure 1: ADC Block Diagram



SWRA100A



Design Note DN101

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2 Voltage Measurement

The following code sets up the ADC to perform voltage measurement of VDD/3.

```
Internal 1.25 V,
/* Reference voltage:
    Resolution: 12 bits,
ADC input: VDD/3 (VDD is the battery voltage) */
#define SAMPLE BATTERY VOLTAGE(v)
     do {
ADCCON2 = 0x3F;
           ADCCON1 = 0x73;
while(!(ADCCON1 & 0x80));
           v = ADCL;
v |= (((unsigned int)ADCH) << 8); \
     } while(0)
// Max ADC input voltage = reference voltage => // (VDD/3) max = 1.25 V => max VDD = 3.75 V // 12 bits resolution means that max ADC value = 0x07FF = 2047 (dec)
// (the ADC value is 2's complement)
// Battery voltage, VDD = adc value * (3.75 / 2047) // To avoid using a float, the below function will return the battery voltage * 10 \,
// Battery voltage * 10 = adc value * (3.75 / 2047) * 10
#define CONST 0.0183195 // (3.75 / 2047) * 10
unsigned char getBatteryVoltage(void) {
   unsigned int adcValue;
     SAMPLE BATTERY VOLTAGE(adcValue);
     // Note that the conversion result always resides in MSB section of ADCH:ADCL adcValue >>= 4; // Shift 4 due to 12 bits resolution
     return CONST * adcValue;
}
```



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3 General Information

3.1 Document History

Revision	Date	Description/Changes
SWRA100A	2007.10.22	Updated code example (12 bits resolution instead of 14).
		Added reference to CC1111.
		Changes to Figure 1.
SWRA100	2006.07.06	Initial release.



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